SCHEMATIC LEARNING

DEFINITION of TERM TECHNIQUE

Basic Terms	General Definitions
AC	Alternate Current
DC	Direct current
AMPHERE	Current flowing
VOLTAGE	Power Supply
POWER/WATT	The amount of voltage required
RESISTOR	Resistance
LOAD	Load

Voltage Definition	Definition	Signal
INPUT	Input	In
OUTPUT	Output	Out
NEGATIVE	Voltage have a value below 0	-
POSITIVE		+
GROUND		GND
CLOCK		CLK
RESET	Input confirmation	Reset
DATA	Communicate	PCI & etc

Term	Information	Term	Information
AC	*Alternating Current*	VTT	*Memory Termination Voltage*
ACDRV	*AC Adapter to System - Switch Driver Output*	VTERM	*Memory Termination vo*
ACEDET	*Adaptor Current Detector*	VUSB	*Power*
ACGOOD	*Valid Adapter Active-Low Detect Logic Open-Drain Output*	VGA	*Power VGA (VGPX/VGPU/VCVOD*

ACIN	*Adaptor Current Sensor Input*	VGFX	*Power Graphic Chip*
ACN	*Adaptor Current Sense Resistor*	VREF	*Voltage References*
ACOP	*Input Over-Power pPotection*	LCDV	*Lcd Power*
ACOV	*Input Over Voltage Protection*	ODD	*Output Disc Driver*
ACP	*Adapter Current Sense Resistor,Positive Input*	PCI	*Peripheral Component Interconnect*
ADP+	*Adapter Positive Supply*	PGOOD	*Power Good Open Drai Output*
ADP_ID	*Adapter Indentity*	PIR	*Product Improved Reco*
AGND	*Anallog Ground*	PSI#	*Current Indicator Input*
ALWP	*Always Power*	PVCC	*Ic Power Positive Suppl*
B+	*Ac or Bat Power Rail for Power Circuit*	RSMRST	*Resume Reset*
BATT	*Battery*	RTC	*Real Tme Clock*
BAT+	*Bat Power Rail for Power Circuit*	SB	*South Bridge*
BAT_DRV	*Bat Fet Gate Driver*	SHDN	*Shut Down*
BAT_V	*Baterry Voltage*	SYS_SDN	*System Shutdown*
BT_EN	*Bluetooth Enable*	SPI	*Serial Peripheral Interf*
BUZER	*Connected*	TD	*Death Time*
BYP	*Baypass*	THRM	*Themal Sensor*
BOM	*Bill of Material Management*	TMDS	*Transition Minimized Difflerential signaling (TRANSMISI DATA TEKNOLOGY)*
BT	*Button*	TP	*Tes Point*
F	*FUSE*	TD	*Death Time*
FSEL	*Frequency Select Input*	THRM	*Thermal Sensor*
CHGEN	*Charge Enable Active-Low Logic Input*	TP	*Tes Point*

CIN	*Input Capasitor*	TPAD	*Thermal Pad*
CRT	*Cathode Ray Tube*	V	*Rail (POWER)*
CSIN	*Current Sensor Input Negatif*	V+	*Positive Vortage*
CSIP	*Current Sensor Input Positif*	VADJ	*Output Regulation Vort*
DC	*Direct Current*	VALW	*Always on Power*
DRAM	*Dynamic RAM: Random-Access Memory that uses a continuous clock*	VAWLP	*Valw Pad*
DOCK	*Docking Socket*	VBAT	*Battery Power*
EC	*Embedded Controler*	VCCP	*Power Chip (Ic Graphi Chips)*
EC_ON	*Embedded Controle Enable*	VCORE	*Power Processor*
EN	*Enable*	VDD	*Control Power Suplly*
ENTRIP	*Enable Terminal*	VDDR	*Power ddr (VDRAM/VRAM/VMEM*
LCDV	*Lcd Power*	VDS	*Voltage Drain Source*
LDO	*Linear Oiver output*	VFB	*Freedback Inputs Powe*
LGATE	*Lower-Side Mosfet Gate Signal*	VGS	*Voltage Gate Source*
LPC	*Low Pin Court*	VIN	*Input Vortage Range*
LVDS	*Low-Voltage Diflerential Signaling (SYSTEM PENSIGNALAN)*	VIN	*Adapter Power Supply (vol_in)*
MBAT	*Main Batteray*	VL	*Power Lock*
ODD	*Output Disc Driver*	VL	*Vortage*
PCI	*Peripheral Component Interconnect*	VLDOIN	*Power Supply of the VT VTTREF Output Stage (to powerMOS)*
PGOOD	*Power Good Open-Drain Output*	VOT	*Volt _Out*
PIR	*Product Improved Record*	VRAM	*Power Memory*

PSI#	*Current Indicator Input*	VREF	*Power References Sche Reference Permintaan schema*
PVCC	*Ic Power Positive Supply*	VS	*Suitch Power*
RSMRST	*Resume Reset*	VS+	*Support Voltage Positiv*
SYS_SDN	*System Shutdown*	VSB	*Power Switch Botton*
VIN	*Input Vortage Range*	VSS	*Signal Grount*
VIN	*Adapter Power Supply (vol_in)*	VSW	*Power Witch*
VL	*Power Lock*	VTT	*Memory Termination Voltage*
VL	*Vortage*	VTERM	*Memory Termination Voltage*
VLDOIN	*Power Supply of the Vtt and Vttref Output Stage (to powerMOS)*	VUSB	*Power usb*
VOT	*Volt _Out*	VGA	*Power vga (VGPX/VGPU/VCVOD*
VRAM	*Power Memory*	VGFX	*Power Graphic Chip*
VREF	*Power References Schema References Permintan Shcema*	VREF	*Voltage References*
VS	*Suitch Power*	HDMI	*High-definition Multime Interface*
VS+	*Support Voltage Positive*	ID	*Continuous Drain Curr*
VSB	*Power Switch Botton*	IDM	*Pulsed Drain Current*
VSS	*Signal Grount*	IIN	*Operating Supply Curre*
VSW	*Power Switch*	SPI	*Serial Peripheral Interf*
VRM	*Circuit konfirmasi*	IIN (SHDN)	*Shutdown Supply Curre*
GATE	*Trigger Gate*	IIN (STBY)	*Standby Supply Current*
GND	*Ground*	IS	*Continuous Source Cur (diode conduction)*

GP	*Ground Pin*	IPIN	*Battery Supply Current VIN pin*
GPI	*General Power Input*	JP	*JUMPER POINT*
GPIO	*General Power Input Output*	KBC	*Keyboard Controler*
		EMII	*Elektromagnetik Interfe (GANGGUAN ELEKTROMAGNETIK*

Term of Circuit	Definition
Voltage divider	Divider voltage
Rectifier	Rectifier circuit
Rectifier	AC To DC
DC Converter	DC to Dc
Converter	DC to DC
Detector	Detector load
Filter	Filter
Step Down	Lowering voltage
Step Up	Riser voltage
OP-Amp	Amplifier

Term Circuit Motherboard	Definition
DC IN & BATTERY CHARGER	Circuit for battery charging and power jack circuit in t initial gate power

SYSTEM 3V/ 5V PCU	Step-down circuit for lowering 19V to 3V or 5V
GPU CORE	Circuit and voltage power supply for chip (ICH/GMC
GPX CORE	Circuit for processor power supply
VCC CORE	Circuit for processor power supply
TERMAL PROTECTION	Circuit heat detector supply
DISCHARGE/+1,8 V	Circuit Suspend LDO
DDR1,5 V & 0.9	Circuit power supply for RAM
CLOCK GENERATOR	Circuit for produce clock signal
LVDS	Circuit for display on screen LCD
POWER BUTTON	Circuit switch on/off
CONN	Connector
RTC/CKT	Circuit CMOS & frequency generator CHIF
DIS	Circuit VGA External generator
UMA	Circuit VGA Internal generator
PWM	Circuit Pulse Widget Modulation controller
COMPARATOR	Circuit comparator
DMI	Digital Media Interference
LPC BUS	Circuit Communication
CRT	Circuit for external monitor and projector
SPI FLASH (KBC)	Circuit BIOS

DEFINITION OF MOTHERBOAR CIRCUIT

Power	Power pin	Controller Device
AC_IN	*B+ AD +*	*AC Dock Input, (19V~24V)*
+19V	*VIN*	*Input Power all Regulator Motherboard*
+0.9VS	*SUSB#*	*DDR2 SODIM*
+1.8V	*SUSB#*	*Calistoga, DDR2 SODIMM*
+1.8VS	*SUSB#*	*G72M*
+1.5VS	*SUSB#*	*Calistoga, ICH7-M, MINI Card Socket, M Card Socket*
+2.5VS	*SUSB#*	*Calistoga, G72M*
+3V	*SUSB#*	*Thermal Sensor, Calitoga, System Clock Generator, DDR2 SODIMM SPD, G72M LVDS connector, TPM, blue tooth socket ICH7-M, PCI interface, super-IO contro IrDA, keyboard controller, audio control GIGA LAN, MINI Card socket , Media co controller, New card socket, 1394 EEPR*
+5V	*SUSC#*	*USB, G913C, New card socket*
+5VS	*SUSB#*	*G72M, ICH7-M, HDD, CD-ROM, flash I keyboard controller, internal touchpad, A AMP, FN*
+3VALWAYS	*(AC IN)*	*ICH7-M, TPM*
+5VALWAYS	*(AC IN)*	*PST9013NR (protect over voltage)*
+5VLCM	*(AC/BAT IN)*	*Micro P (PIC16F57)*
+5VCHG	*(AC/BAT IN)*	*MAX8724ETI, power – ON/OFF logic*
+VCORE	*VID*	*CPU*
+VCCP	*VR_ON*	*CPU I/O*
+12V	*SUSC#*	*PCMCIA Slot 12V*
+3V	*SUSCH#*	*PCMCIA controller, PCMCIA Slot 3.3V, keyboard controller, LAN*
+3VS	*SUSB#*	*ICH4-M, Clock Generator,PCI intyerfac Super I/O,AC97 CODEC, FWH, Therma sensor,IR*
+5V	*SUSC#*	*PCMCIA Slot 5V , USB*
+5VS	*SUSB#*	*HDD, CD-ROM, Internal Touch Pad, Au*

		AMP
+5VALWAYS	*AC_BAT_SYS*	*ICH4_M (Resume Well) LAN (WOL in S4*
RTCVCC		*ICH4-M (RTC)*
+V1.5S	*SUSB#*	*GMCH+, ICH4-H*
+V1.8S	*SUSB#*	*CPU VCCA*
+V2.5	*SUSC#*	*DDR, SODIMM, GMCH LVDS*
+V1.2S	*SUSB#*	*GMCH*
+V1.25S	*SUSB#*	*DDR Termination*

VOLTAGE – SIGNAL NAME – POWER CONTROL PIN AND CONTROL DEVICE

UNDERSTANDING BASIC CODE OF SCHEMATIC PATH

Schematic diagrams are always synonymous with code and symbols.
Memorize some of the symbols below:

Path	Schematic diagram application	Information
		A path that only passes and does not have a relationship. And in principle the lane is a path connecting one components and others that connect one pin and others.
		Every time there is a dot