

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
A	0004536627	PRODUCTION RELEASED		2015-07-21

www.laptop-schematics.com

N71 MLB "DARWIN"

LAST_MODIFICATION= Tue Aug 28 14:17:32 2015

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59				

SCH 051-1902
 BRD 820-5507
 MCO 056-01060

BOM 639-00263 (BETTER, DB30)
 BOM 639-00265 (ULTRA, DB30)
 BOM 639-00266 (SUPREME, DB30)
 BOM 639-01056 (BETTER, B30)
 BOM 639-01057 (ULTRA, B30)

BOM 639-01058 (SUPREME, B30)
 BOM 639-01098 (BETTER, DB30C)
 BOM 639-01100 (ULTRA, DB30C)
 BOM 639-01099 (SUPREME, DB30C)
 BOM 939-01627 (BETTER, DARWIN)

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Apple Inc.	DRAWING NUMBER	051-1902	SIZE
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SCHEMATIC & PCB BOM CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-1902	1	SCH_SINGLE_BRD_N71	SCH	CRITICAL	?
820-5507	1	PCBF_SINGLE_BRD_N71	PCB	CRITICAL	?
825-6838	1	EEEE CODE FOR 639-00263	EEEE_G2KM	CRITICAL	EEEE_16G_DB30
825-6838	1	EEEE CODE FOR 639-00265	EEEE_G2KN	CRITICAL	EEEE_64G_DB30
825-6838	1	EEEE CODE FOR 639-00266	EEEE_G2KL	CRITICAL	EEEE_128G_DB30
825-6838	1	EEEE CODE FOR 639-01056	EEEE_GKF9	CRITICAL	EEEE_16G_B30
825-6838	1	EEEE CODE FOR 639-01057	EEEE_GKFC	CRITICAL	EEEE_64G_B30
825-6838	1	EEEE CODE FOR 639-01058	EEEE_GKFB	CRITICAL	EEEE_128G_B30
825-6838	1	EEEE CODE FOR 639-01098	EEEE_GLHL	CRITICAL	EEEE_16G_DB30C
825-6838	1	EEEE CODE FOR 639-01100	EEEE_GLHR	CRITICAL	EEEE_64G_DB30C
825-6838	1	EEEE CODE FOR 639-01099	EEEE_GLHM	CRITICAL	EEEE_128G_DB30C
825-6838	1	EEEE CODE FOR 939-01627	EEEE_GR09	CRITICAL	EEEE_16G_DARWIN

S3E NAND BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S00039	1	NAND_1YNM_16GX8_S3E_64G_T_SLGA70	U1500	CRITICAL	NAND_16G
335S00075	1	NAND_1YNM_64GX8_S3E_MLS_64G_H_SLGA70	U1500	CRITICAL	NAND_64G
335S00079	1	NAND_1YNM_128GX8_S3E_TLC_128G_H_SLGA70	U1500	CRITICAL	NAND_128G

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S00074	335S00039	NAND_16G	U1500	HYNIX 16G SLGA70 C DIE
335S00078	335S00075	NAND_64G	U1500	HYNIX 64G SLGA70
335S00064	335S00075	NAND_64G	U1500	SANDISK 64G SLGA70 LZ
335S00065	335S00079	NAND_128G	U1500	SANDISK 128G SLGA70

CARBON/ACCEL BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
338S1163	1	IC_ACCEL_3-AXIS_DIG_BMA282_LGA14	U3030	NOSTUFF
117S0202	1	RES_MF_20 OHM_5%_1/32W_01005	R3030	NOSTUFF
117S0202	1	RES_MF_20 OHM_5%_1/32W_01005	R3031	NOSTUFF
117S0202	1	RES_MF_20 OHM_5%_1/32W_01005	R3032	NOSTUFF
138S0831	1	CAP_CER_XSR_2.2UF_20%_6.3V_0201	C3031	NOSTUFF
132S0316	1	CAP_CER_XSR_0.1UF_20%_6.3V_01005	C3032	NOSTUFF
338S00017	1	IC_CARBON_MPU-6700-12_LGA16	U3010	INVENSENSE_CARBON
338S1163	1	IC_ACCEL_3-AXIS_DIG_BMA282_LGA14	U3030	INVENSENSE_CARBON
117S0202	1	RES_MF_20 OHM_5%_1/32W_01005	R3030	INVENSENSE_CARBON
117S0202	1	RES_MF_20 OHM_5%_1/32W_01005	R3031	INVENSENSE_CARBON
117S0202	1	RES_MF_20 OHM_5%_1/32W_01005	R3032	INVENSENSE_CARBON
138S0831	1	CAP_CER_XSR_2.2UF_20%_6.3V_0201	C3031	INVENSENSE_CARBON
132S0316	1	CAP_CER_XSR_0.1UF_20%_6.3V_01005	C3032	INVENSENSE_CARBON
338S00087	1	IC_CARBON_1.1_MPU-6800-00_LGA16	U3010	INVENSENSE_STANDALONE_CARBON

ALTERNATE BOM OPTIONS

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
138S00032	138S0831	ALTERNATE	C0610	TY, 2.2UF, 0201
138S00049	138S0831	ALTERNATE	C0610	KYOCERA, 2.2UF, 0201
155S0660	155S0513	ALTERNATE	FL3100	MURATA, FERR, 22-OHM
138S00005	138S00003	ALTERNATE	C2000	TY, 15UF, 0402
138S00048	138S00003	ALTERNATE	C2000	KYOCERA, 15UF, 0402
138S0702	138S0657	ALTERNATE	C2111	MURATA, 4.3UF, 0610
118S0764	118S0717	ALTERNATE	R2250	PANASONIC, 3.92K-OHM, 0201
138S00006	138S0835	ALTERNATE	C1106	TY, 4.3UF, 0402
152S2052	152S1929	ALTERNATE	L2060	CYNTEC, 1UH, 1608
155S0773	155S0453	ALTERNATE	FL3110	TY, FERR, 120-OHM, 01005
377S0168	377S0140	ALTERNATE	DZ3150	TKL, VARIOSTAR, 6.8V, 100PF, 01005
155S00067	155S0581	ALTERNATE	FL4200	TKL, FERR, 240-OHM, 0201
155S00012	155S00009	ALTERNATE	L3100	MURATA, CHOKER, 65-OHM, 0605
138S0706	138S0739	ALTERNATE	C3302_RF	ROHM, CAP, 330P, 10%_10%_0603
138S0945	138S0739	ALTERNATE	C3302_RF	YOKUCHI, CAP, 330P, 10%_10%_0603
155S00095	155S00068	ALTERNATE	FL1280	FERR 80, 100 OHM, 10%_10%_0603
138S0648	138S0652	ALTERNATE	C3650	TY, 4.7UF, 0402
132S0400	132S0436	ALTERNATE	C1280	CAP, CER, XSR, 0.22UF, 20%, 6.3V, 01005
155S0960	155S0941	ALTERNATE	FL3151	FERR 80, 70 OHM, 10%_10%_0603
138S00024	138S0986	ALTERNATE	C1107	CAP, CER, 3-TERM, 7.5UF, 20%, 4V, 0402
335S00066	335S0946	ALTERNATE	U0900	IC, EEPROM, 16KB, 1.8V, 12C, WL5294
155S0653	155S0511	ALTERNATE	FL4600	FERR 80, 31 OHM, 10%_10%_0603

NOT ALL REFERENCE DESIGNATORS LISTED. USED ~116 TIMES IN DESIGN.

USED ~116 TIMES IN DESIGN.

USED ~7 TIMES IN DESIGN.

USED ~63 TIMES IN DESIGN.

USED ~63 TIMES IN DESIGN.

USED ~3 TIMES IN DESIGN.

USED ~19 TIMES IN DESIGN.

USED ~61 TIMES IN DESIGN.

USED ~9 TIMES IN DESIGN.

USED ~8 TIMES IN DESIGN.

USED ~11 TIMES IN DESIGN.

USED ~17 TIMES IN DESIGN.

USED ~17 TIMES IN DESIGN.

USED ~12 TIMES IN DESIGN.

USED ~2 TIMES IN DESIGN.

USED ~9 TIMES IN DESIGN.

USED ~7 TIMES IN DESIGN.

USED ~4 TIMES IN DESIGN.

POWER INDUCTOR ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
152S00120	152S00077	ALTERNATE	L2070	TAIYO 2016 1.00H 0.65MM
152S00118	152S00075	ALTERNATE	L3700	TAIYO 2016 1.2UH

ACTIVE DIODE ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
376S00106	376S00047	ALTERNATE	Q2300	DIODES INC. ACT DIODE

SHIELD PART NUMBERS

PART NUMBER	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
806-02895	1	SHIELD,EMI,UPPER FRONT,WTOP,N71	SH0500	COMMON
806-04588	1	SHIELD,EMI,LOWER FRONT,CLOSED,N0M1,N71	SH0501	COMMON
806-03994	1	SHIELD,EMI,SA,OPEN,N71	SH0502	COMMON
806-02897	1	SHIELD,EMI,UPPER BACK,WTOP,N71	SH0503	COMMON
806-02898	1	SHIELD,EMI,LOWER BACK,WTOP,N71	SH0504	COMMON

SOC/PMU SUB BOMS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
685-00069	1	SUBBOM_SINGLE_BRD_N71	SUBBOM_SOC	COMMON
338S00120	1	IC_PMU_AMT100A_A0_G255A1_07P-AL_WGCEP380	U2000	MAUI
118S0631	1	RES_MF_100 OHM_1%_1/32W_01005	R0730	MAUI
131S0307	1	CAP_CER_NPO/COG_100PF_5%_16V_01005	C0730	MAUI
339S00112	1	PROD FUSED, H DRAM	U0600	MAUI
117S0161	1	RES_MF_0 OHM_1%_1/32W_01005	R0651	MAUI

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
338S00122	1	IC_PMU_AMT100A_G255A1_07P-AL_WGCEP380	U2000	MALTA
118S00009	1	RES_MF_3.0KOHM_1%_1/32W_01005	R0730	MALTA
131S0307	1	CAP_CER_NPO/COG_100PF_5%_16V_01005	C0730	NOSTUFF
339S00124	1	M DEV FUSED, M DRAM	U0600	MALTA
118S00025	1	RES_MF_330 OHM_1%_1/32W_01005	R0651	MALTA

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
685-00070	685-00069	ALTERNATE	SUBBOM_SOC	SUBBOM_SINGLE_BRD_MALTA,N71

SOC ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
339S00113	339S00112	MAUI	U0600	PROD FUSED, M DRAM
339S00114	339S00112	MAUI	U0600	PROD FUSED, S DRAM

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
339S00125	339S00124	MALTA	U0600	M PROD FUSED, H DRAM, ATR
339S00126	339S00124	MALTA	U0600	M PROD FUSED, S DRAM, ATR
339S00127	339S00124	MALTA	U0600	M PROD FUSED, M DRAM, SCK
339S00128	339S00124	MALTA	U0600	M PROD FUSED, H DRAM, SCK
339S00129	339S00124	MALTA	U0600	M PROD FUSED, S DRAM, SCK


INDUCTOR SUB BOMS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
685-00081	1	SUBBOM_SINGLE_BRD_CYNTEC,N71	SUBBOM_IND	COMMON
152S00074	6	IND_PWR_SHLD_1.0H_3.6A_0.060 OHM_2016	L2000,L2002,L2010,L2012,L2020,L2030	CYNTEC
152S00074	5	IND_PWR_SHLD_1.0H_3.6A_0.060 OHM_2016	L2040,L2050,L2090,L3300,L4021	CYNTEC
152S00081	6	IND_PWR_SHLD_0.47UH_3.8A_0.048 OHM_2012	L2001,L2003,L2011,L2013,L2021,L2041	CYNTEC

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S00117	6	IND_PWR_SHLD_1.0H_3.6A_0.060 OHM_2016	L2000,L2002,L2010,L2012,L2020,L2030	TAIYO
152S00117	5	IND_PWR_SHLD_1.0H_3.6A_0.060 OHM_2016	L2040,L2050,L2090,L3300,L4021	TAIYO
152S00121	6	IND_PWR_SHLD_0.47UH_3.8A_0.048 OHM_2012	L2001,L2003,L2011,L2013,L2021,L2041	TAIYO

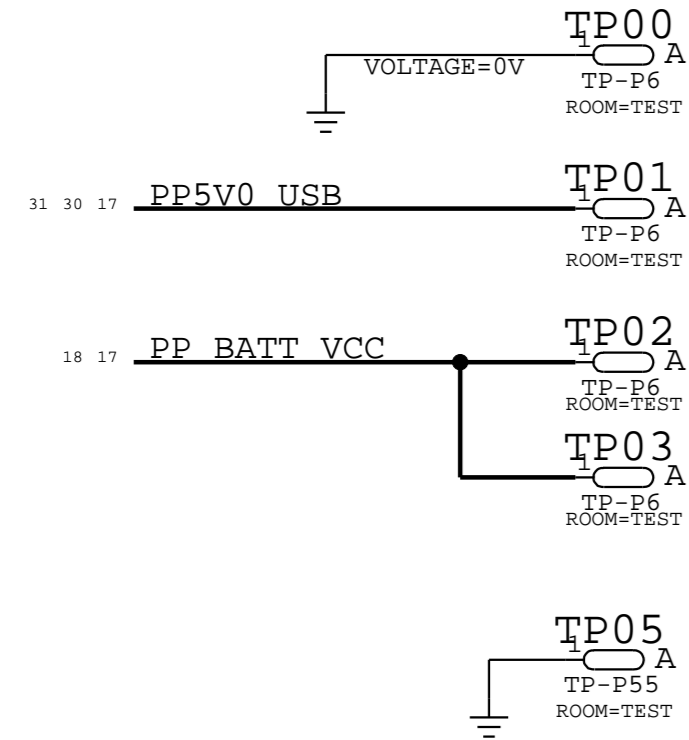
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685-00080	685-00081	ALTERNATE	SUBBOM_IND	SUBBOM_SINGLE_BRD_TAIYO,N71

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TESTPOINTS

POWER

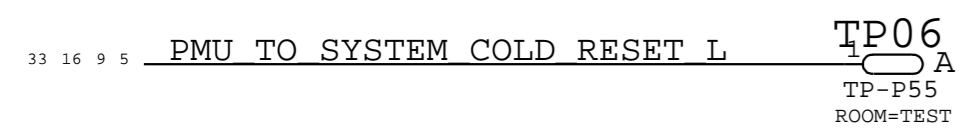


POWER GROUND

VBUS

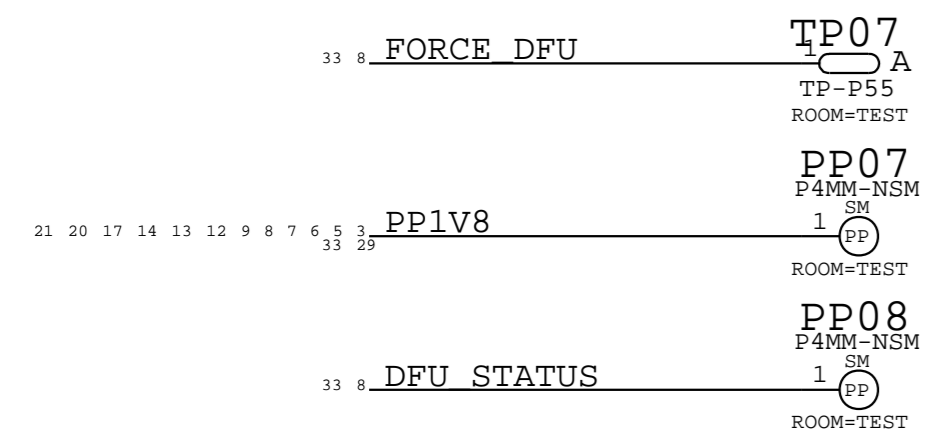
VBATT

RESET



SOC & BB RESET

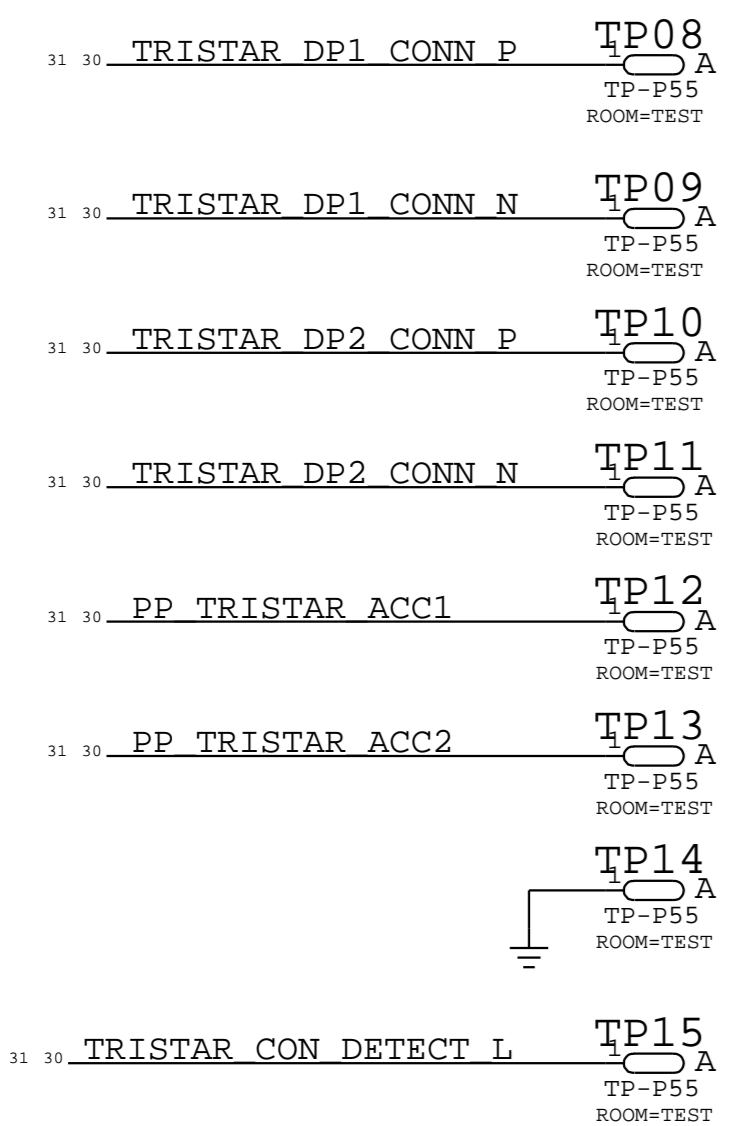
DFU



FORCE DFU PROCEDURE:

1. FROM OFF MODE SHORT TP07 TO PP07
2. PLUG IN E75 CABLE TO FORCE DFU

E75

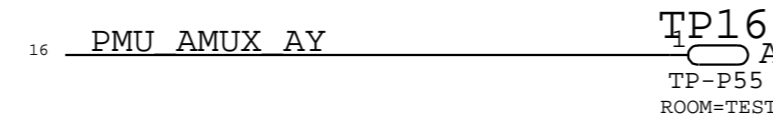


TRISTAR USB

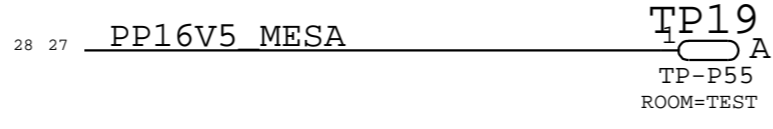
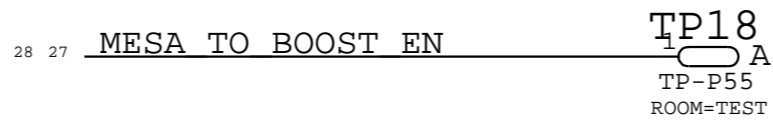
TRISTAR DEBUG UART

TRISTAR ACCESSORY ID
ACCESSORY POWER

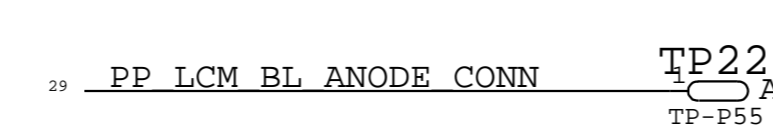
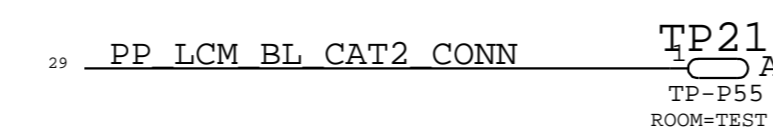
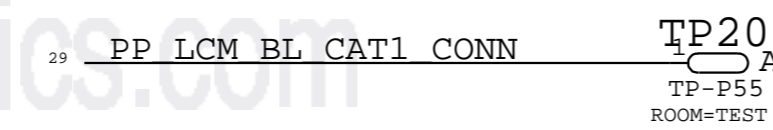
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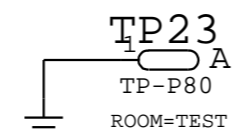
MOJAVE



LCM



SUPER SCREW



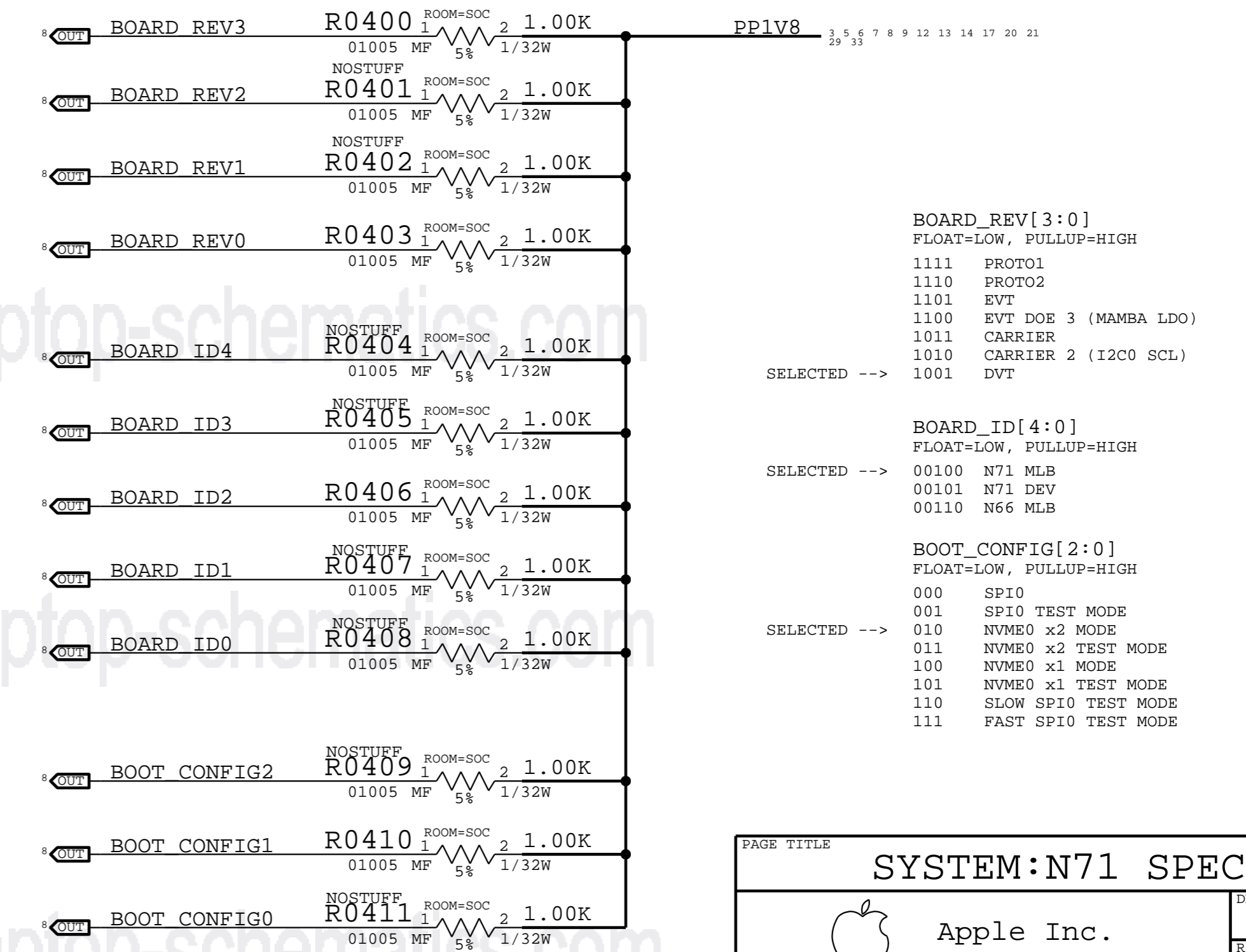
N71 I2C DEVICE MAP

I2C BUS	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
I2C0	ANTIGUA PMU	1110100X	0X74	0XE8
	CHESTNUT	0100111X	0X27	0X4E
	MUON	1100010X	0X62	0XC4
I2C1	TIGRIS	1110101X	0X75	0XE9
	ARC DRIVER	1000001X	0X41	0X82
	SPEAKER AMP	1000000X	0X40	0X80
I2C2	TRISTAR	0011010X	0X1A	0X34
	ALS	0101001X	0X29	0X52
OWL	DISP EEPROM	1010001X	0X51	0XA2
	UNUSED	N/A	N/A	N/A
ISP I2C0	REAR CAM	TBD	TBD	TBD
	LED DRIVER	1100011X	0X63	0XC6
ISP I2C1	FRONT CAM	0010000X	0X10	0X20
TOUCH I2C	MESON	1000000X	0x40	0x80
	MAMBA	1100000X	0x60	0xC0
	DOPPLER	1011000X	0x58	0xB0
SEP I2C	SEP EEPROM	1010001X	0x51	0xA2

BOOTSTRAPPING: BOARD REV

BOARD ID

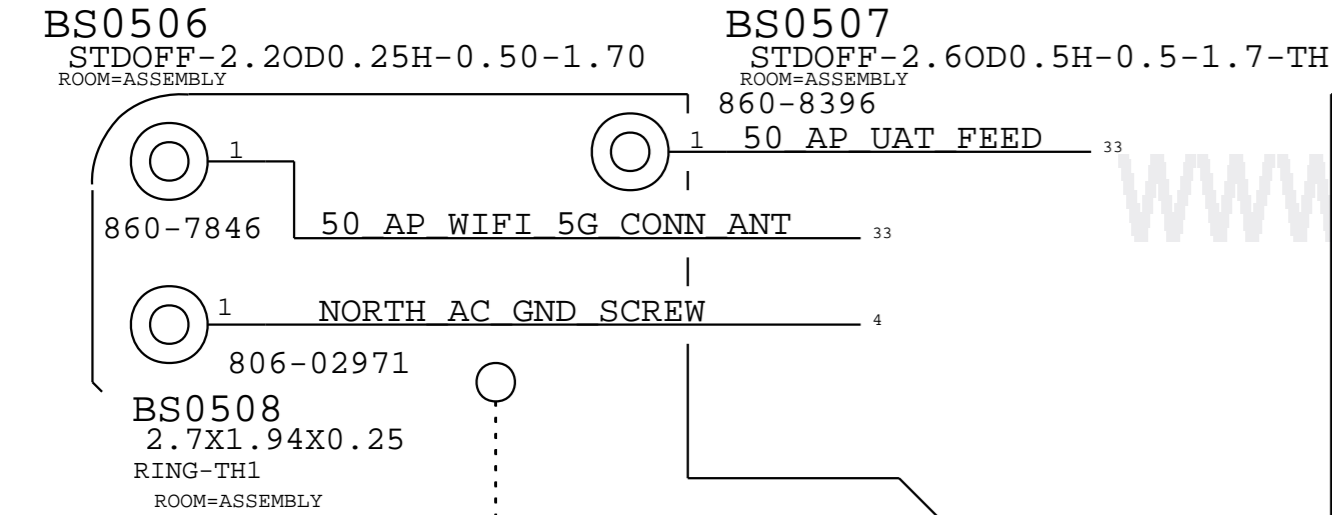
BOOT CONFIG



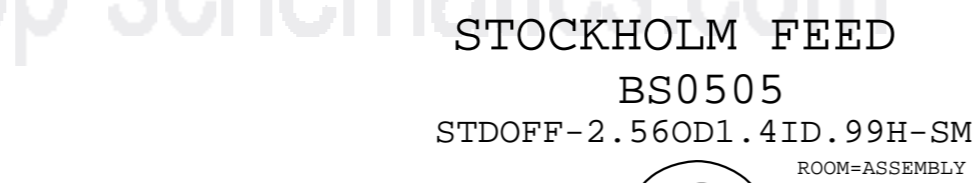
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RESISTOR STUFF = HIGH '1'
RESISTOR NOSTUFF = LOW '0'

PENINSULA STANDOFFS

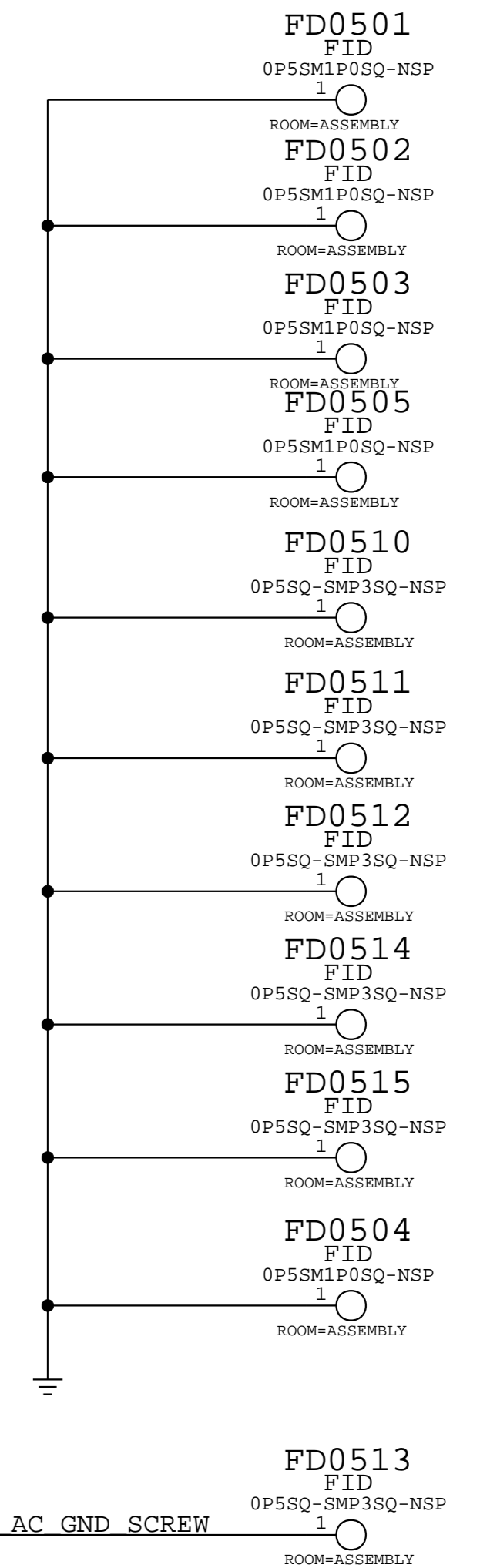


TOP-SIDE

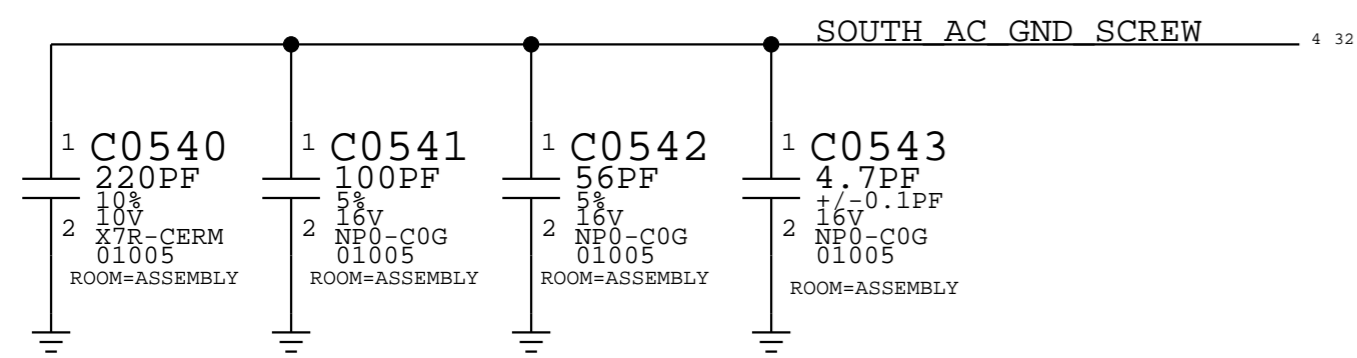


BOTTOM-SIDE

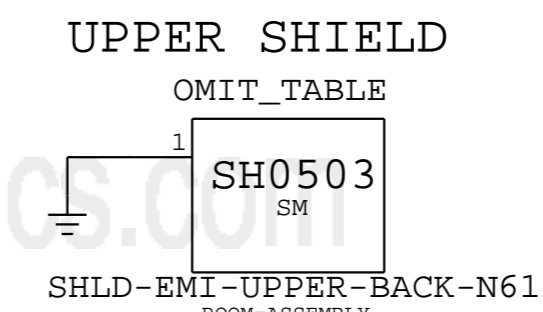
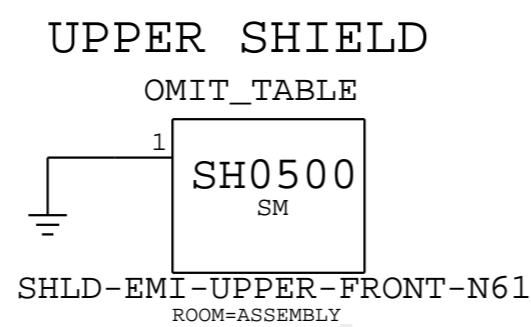
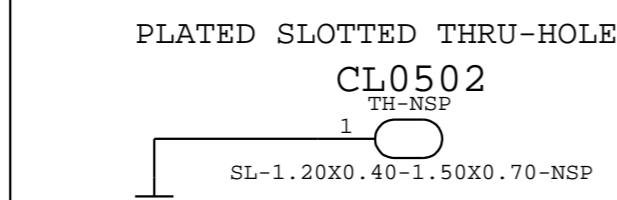
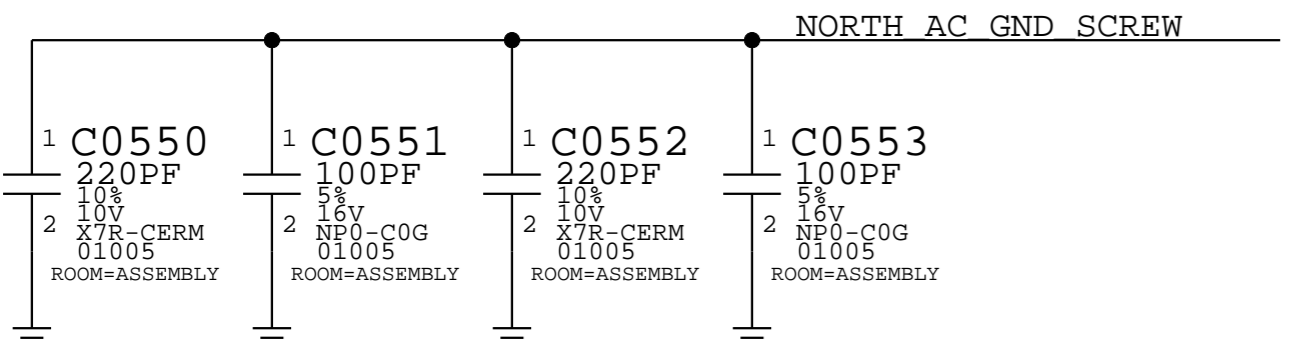
FIDUCIALS



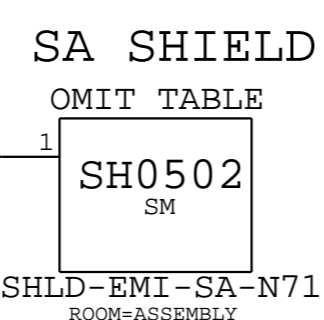
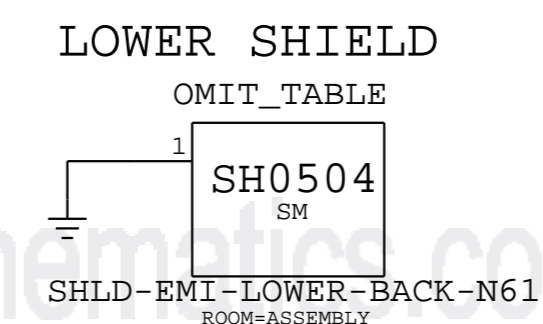
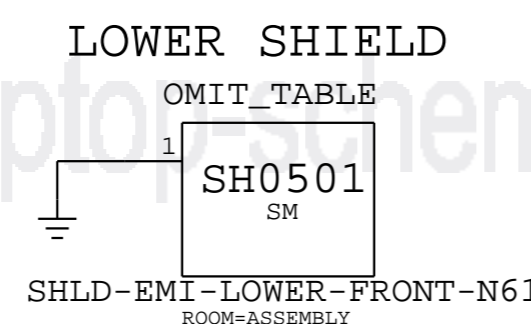
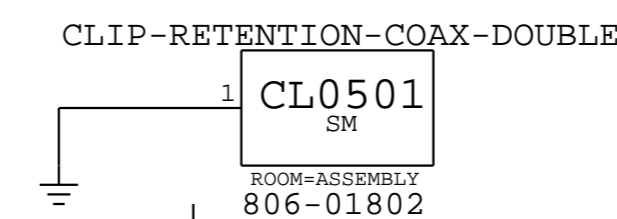
SOUTH DC CURRENT BLOCKING CAPS



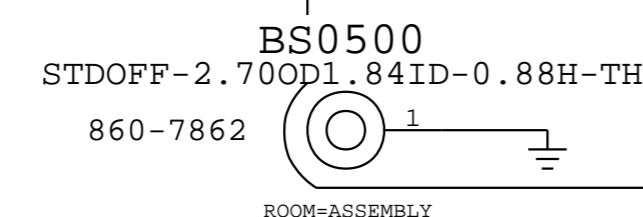
NORTH DC CURRENT BLOCKING CAPS



DUAL RF COAX CLIP



SOUTH TUBE STANDOFF



TODO:UPDATE REF DES

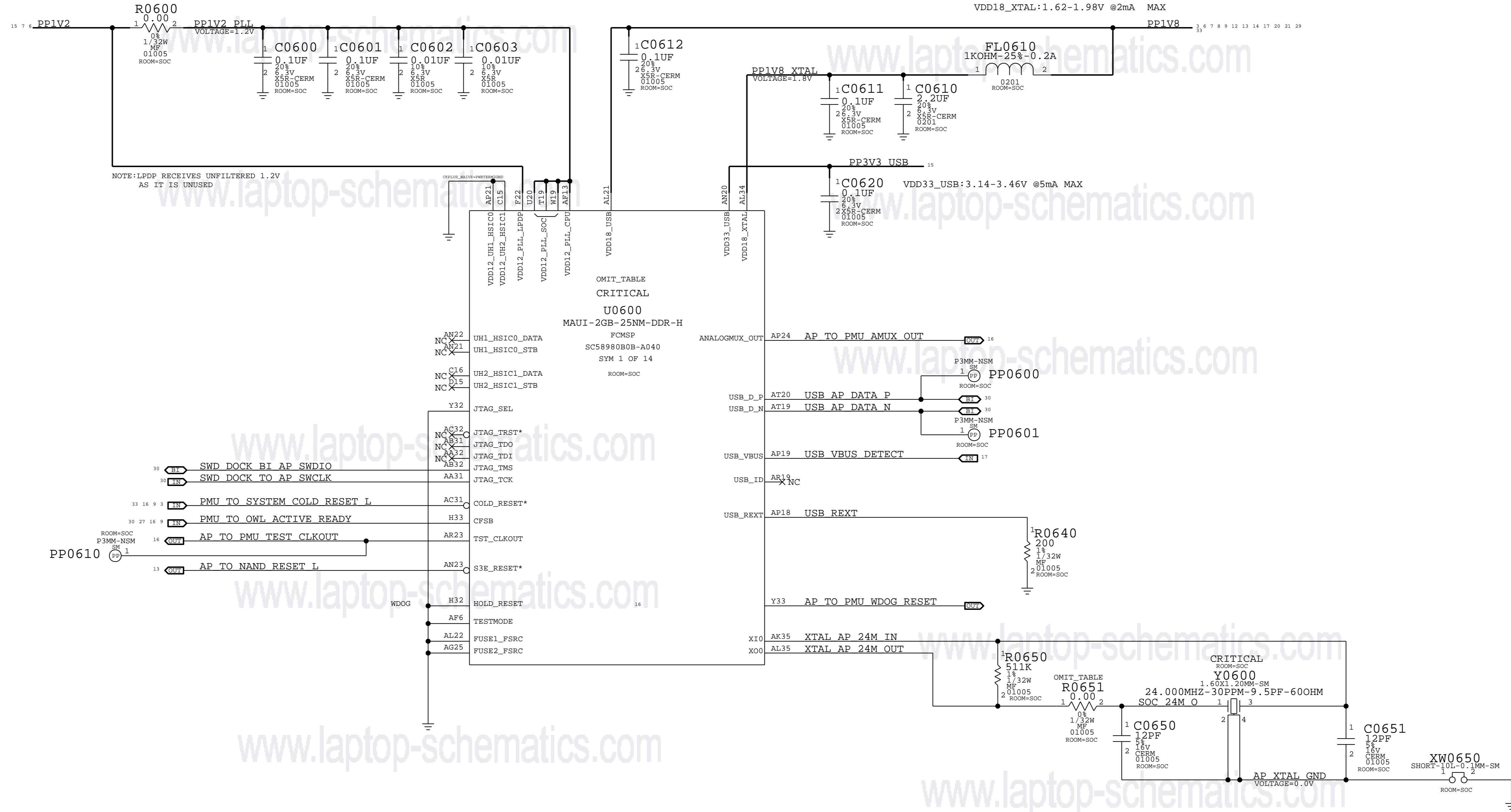
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	PAGE	5 OF 49
	SHEET	4 OF 59

MAUI - USB, JTAG, XTAL

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VDD12_PLL_LPDP: 1.14-1.26V @2mA MAX
 VDD12_PLL_SOC: 1.14-1.26V @12mA MAX
 VDD12_PLL_CPU: 1.14-1.26V @2mA MAX

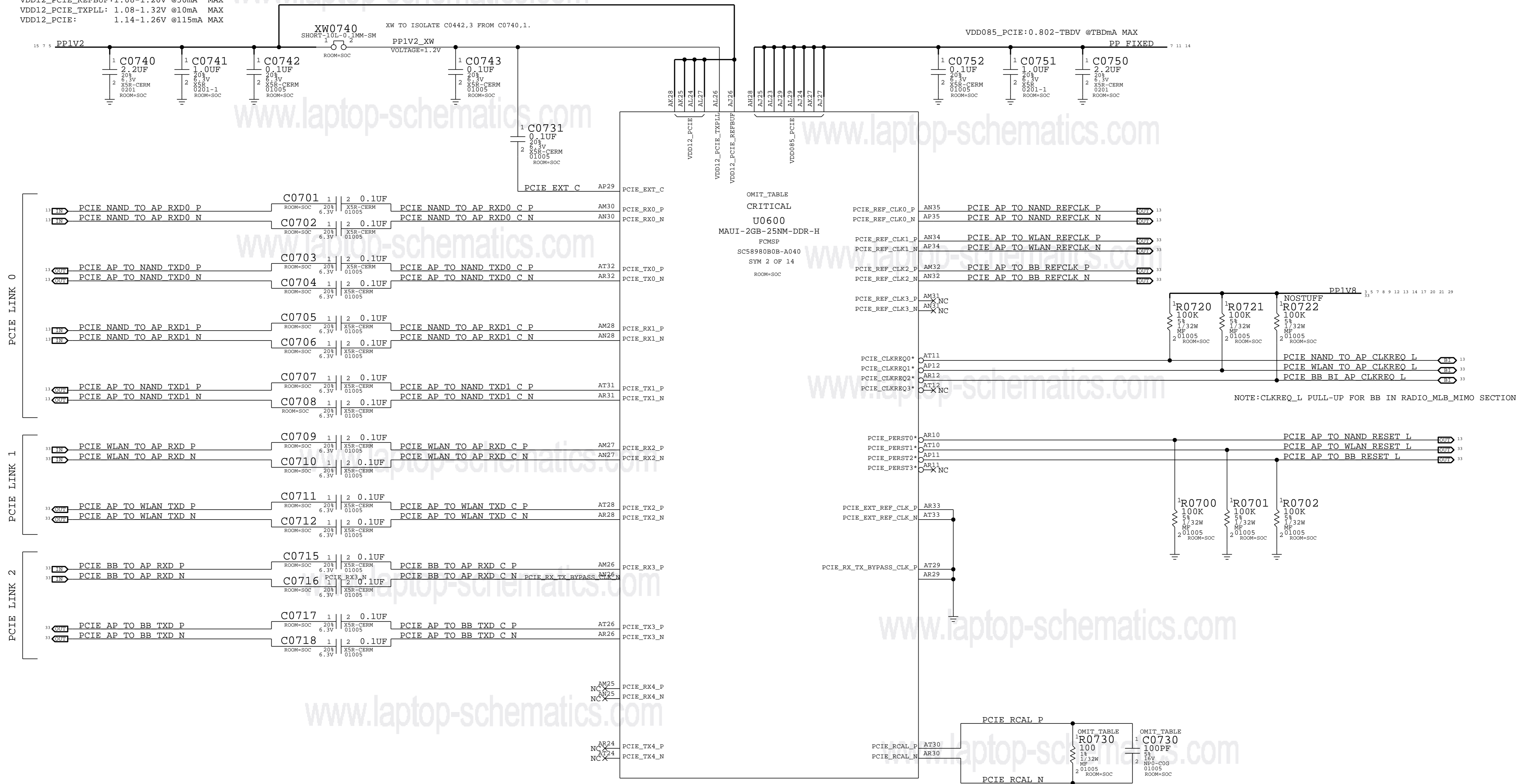
VDD18_USB: 1.71-1.89V @20mA MAX
 VDD18_XTAL: 1.62-1.98V @2mA MAX



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PAGE TITLE SOC: JTAG, USB, XTAL			
Apple Inc.	DRAWING NUMBER	SIZE	
	051-1902	D	
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	BRANCH		
	PAGE	6 OF 49	
SHEET	5 OF 59		

MAUI - PCIE INTERFACES

VDD12_PCIE_REFBUF: 1.08-1.26V @50mA MAX
 VDD12_PCIE_TXPLL: 1.08-1.32V @10mA MAX
 VDD12_PCIE: 1.14-1.26V @115mA MAX



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SOC:PCIE			
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MAUI - CAMERA & DISPLAY INTERFACES

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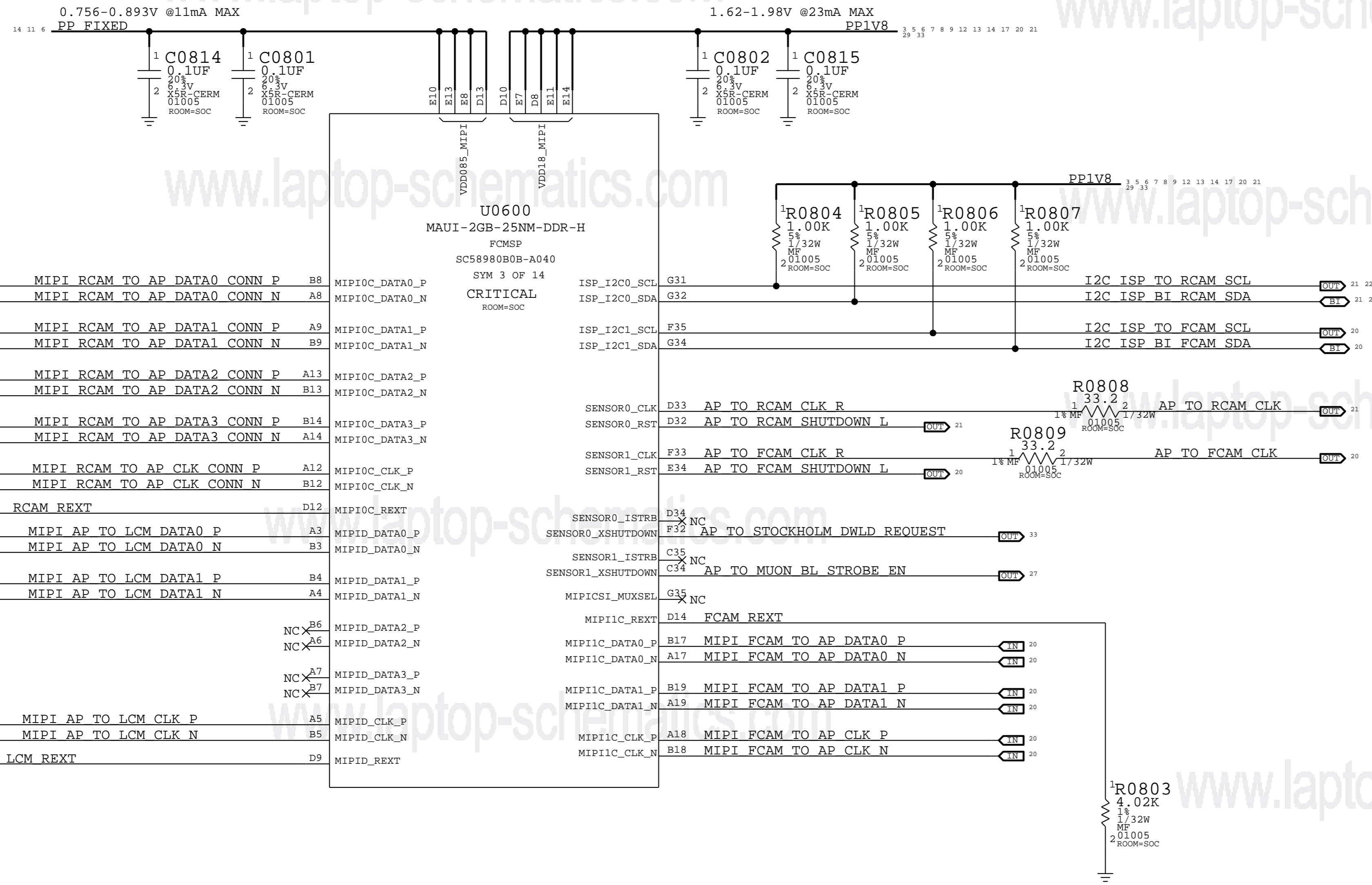
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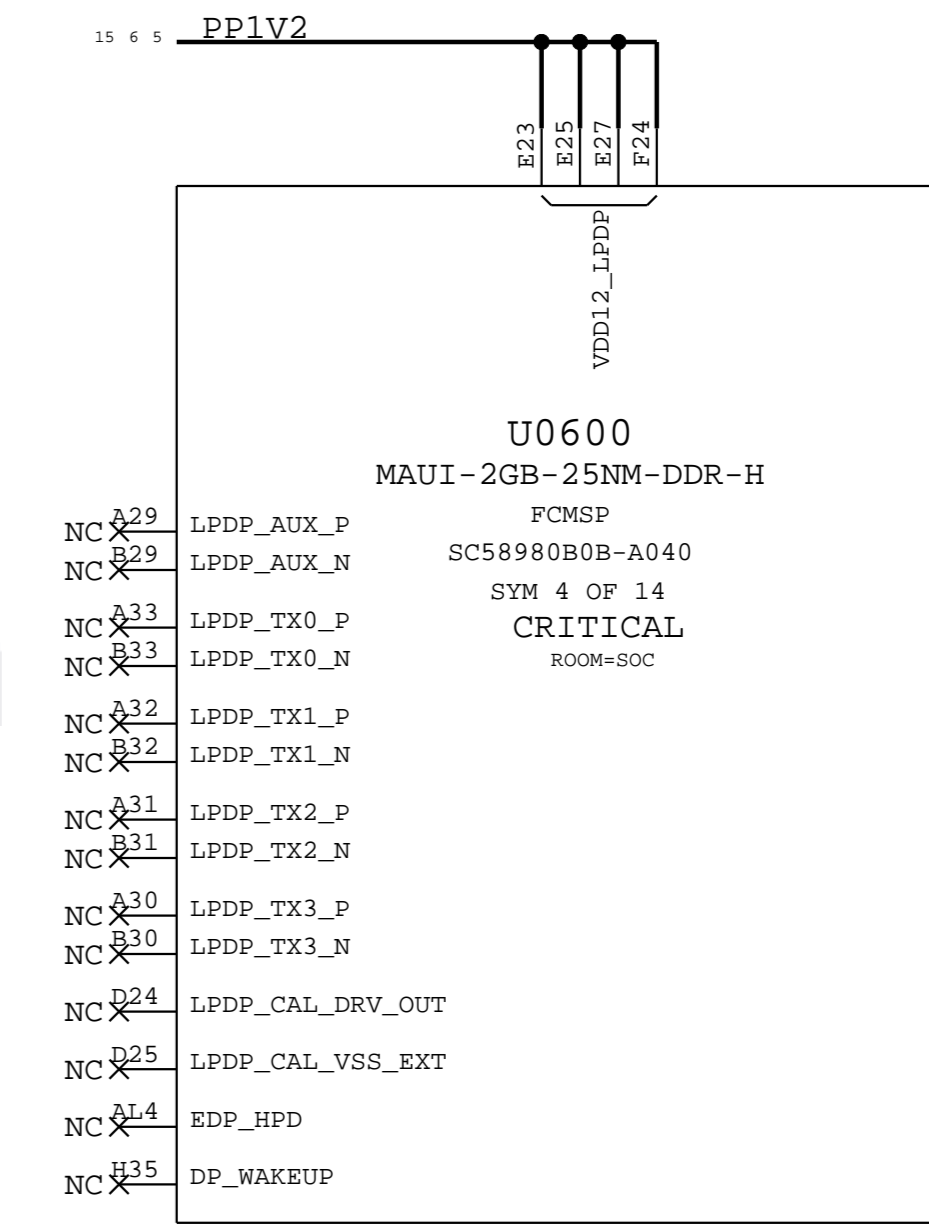
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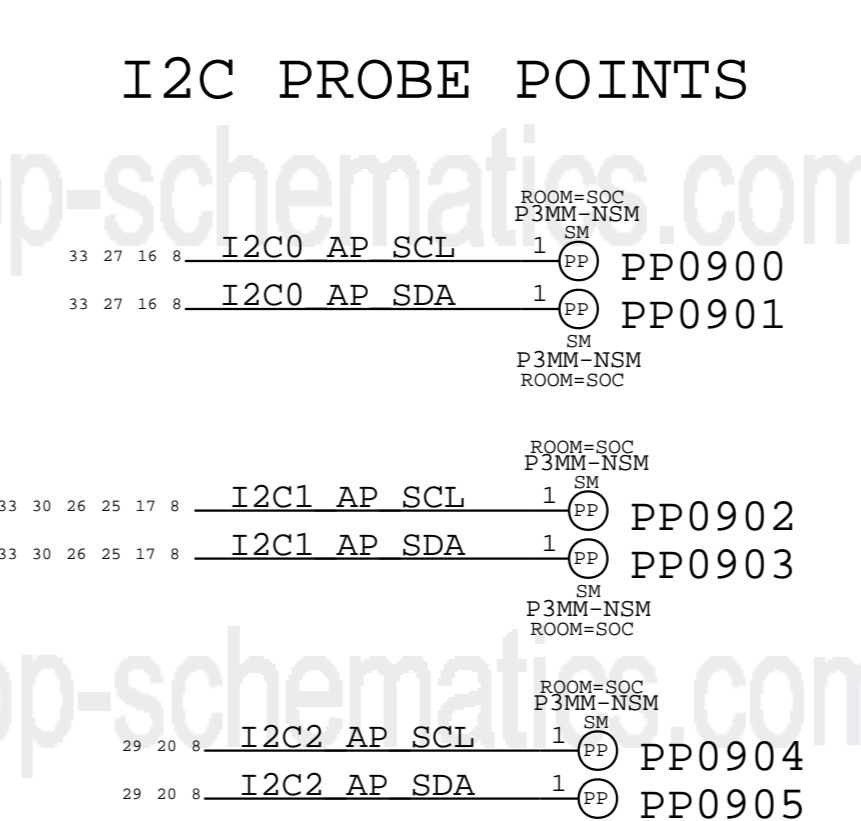
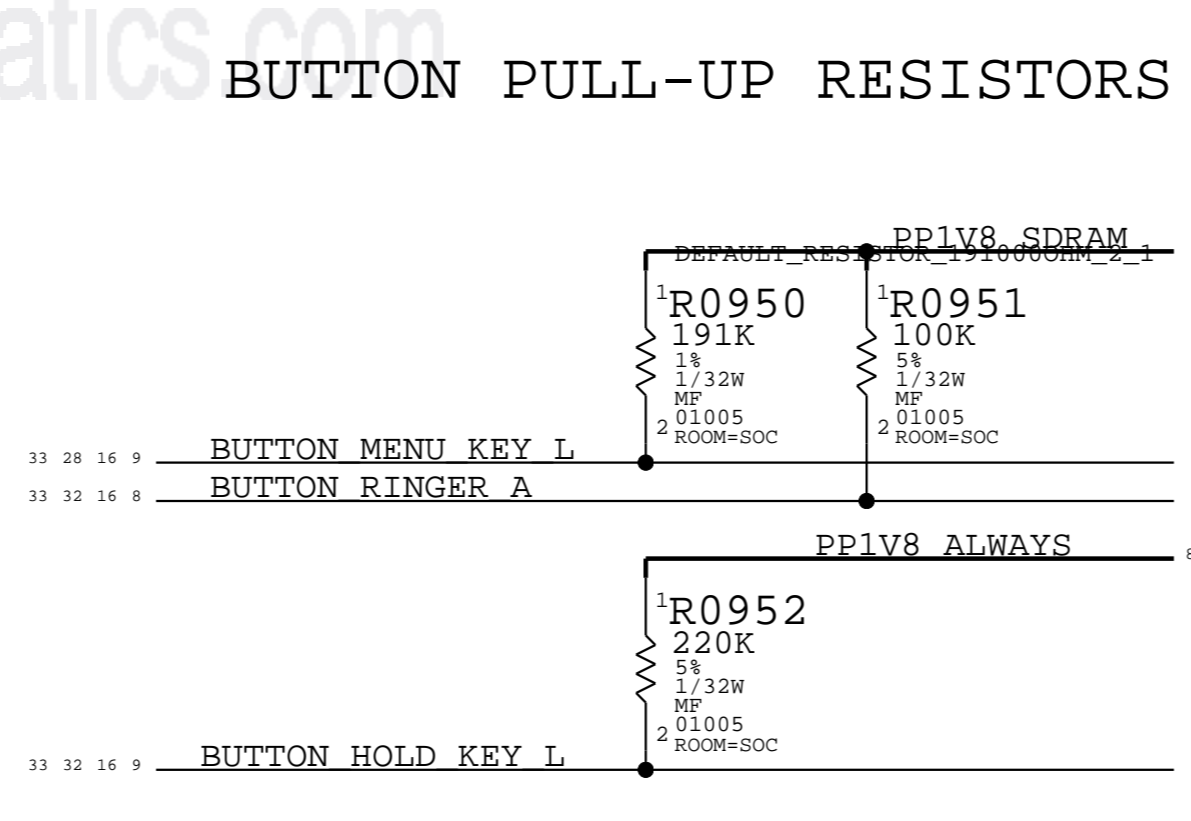
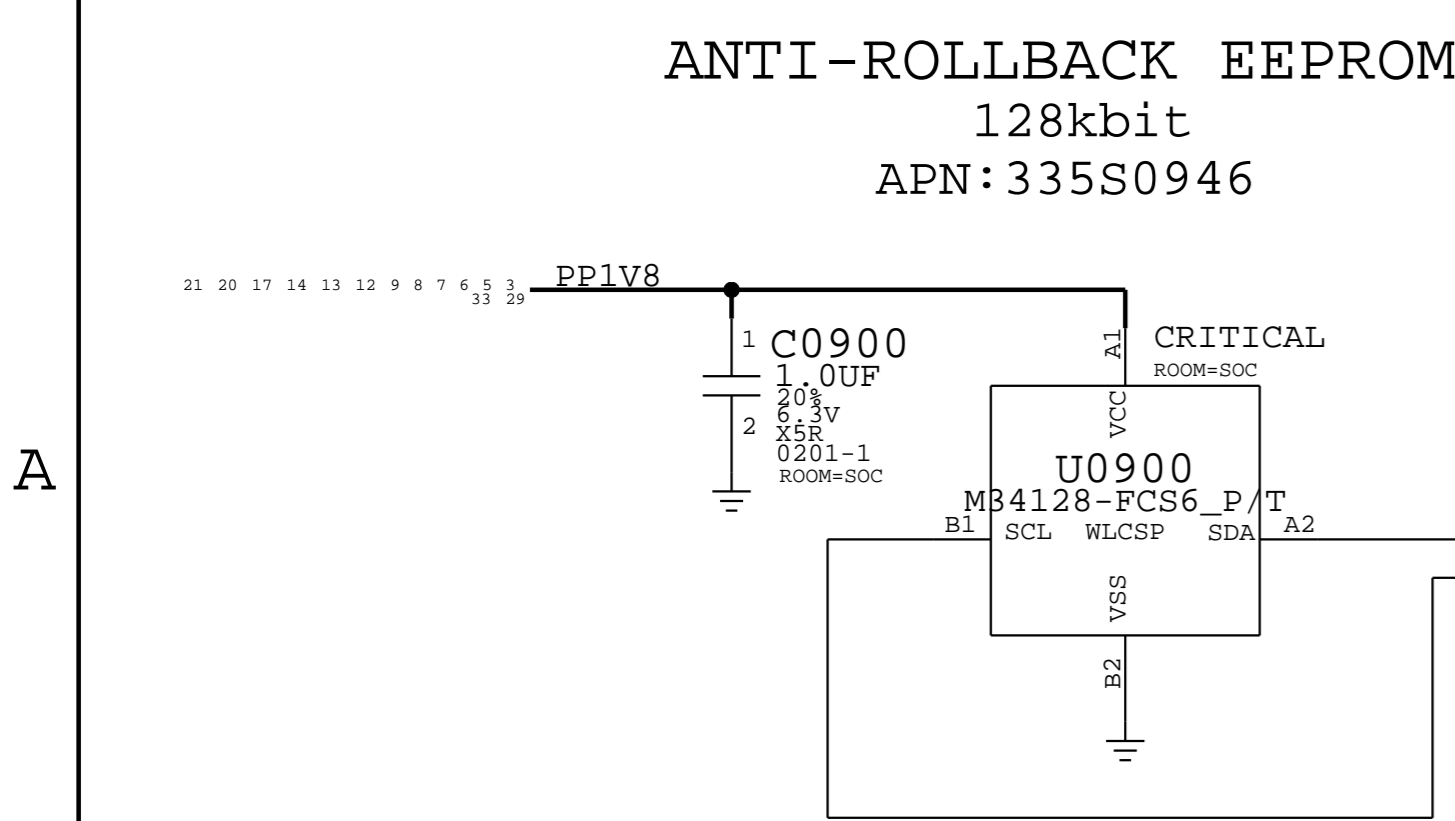
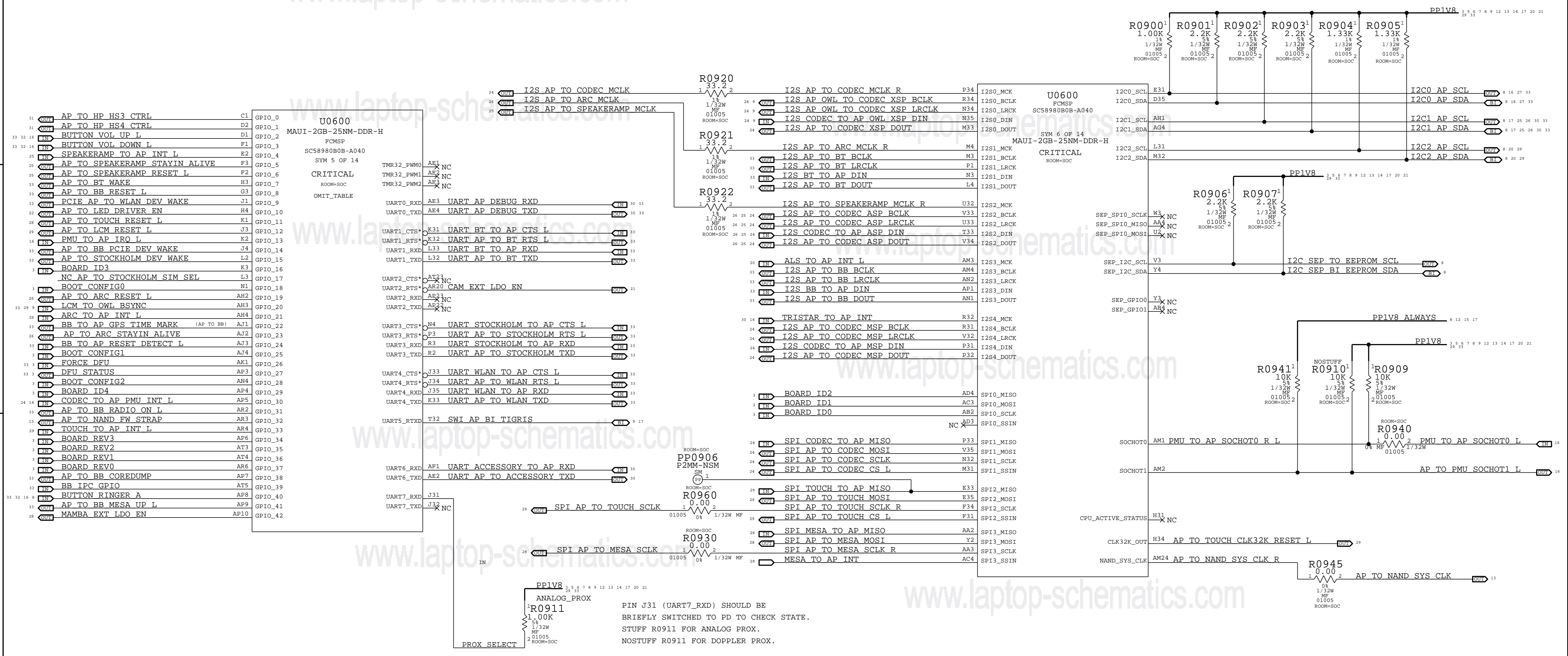
NOTE: VDD12_LPDP SHOULD BE POWERED
EVEN WHEN LPDP IS NOT USED



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SOC: CAMERA & DISPLAY			
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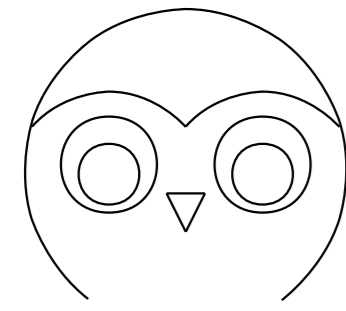
MAUI - GPIO & SERIAL INTERFACES

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MAUI - OWL

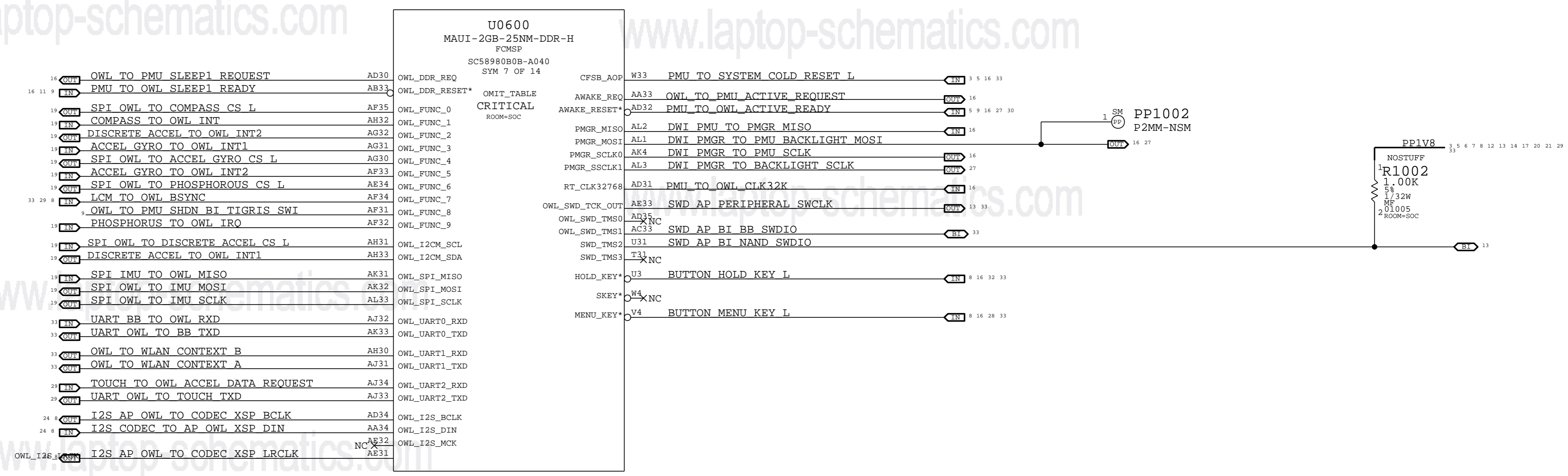
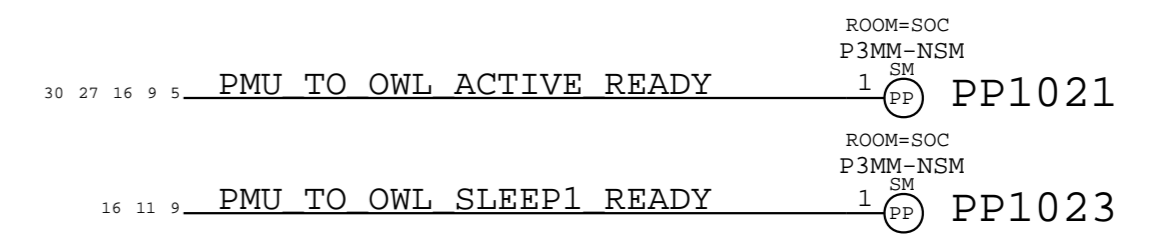


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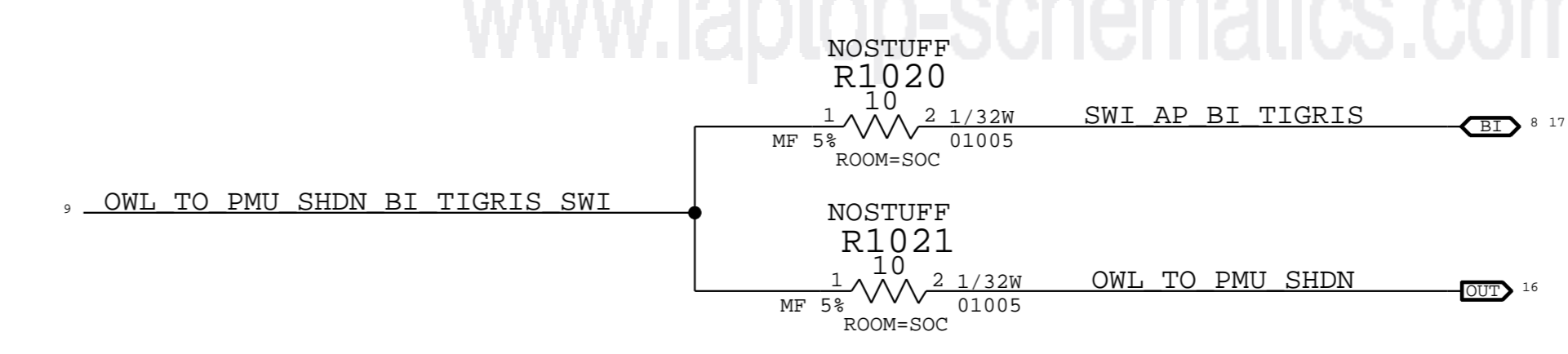
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POWER STATE CONTROL PROBE POINTS



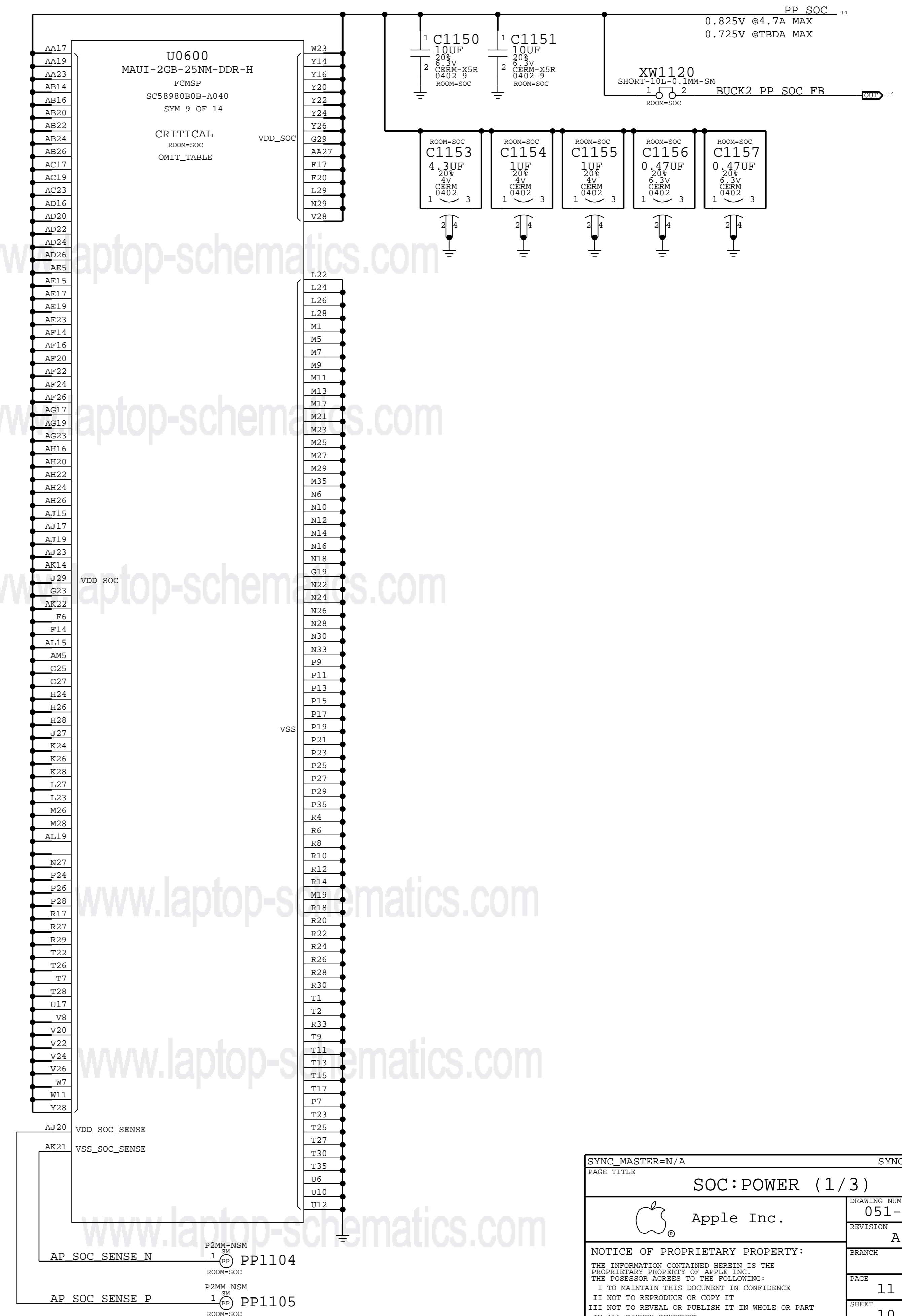
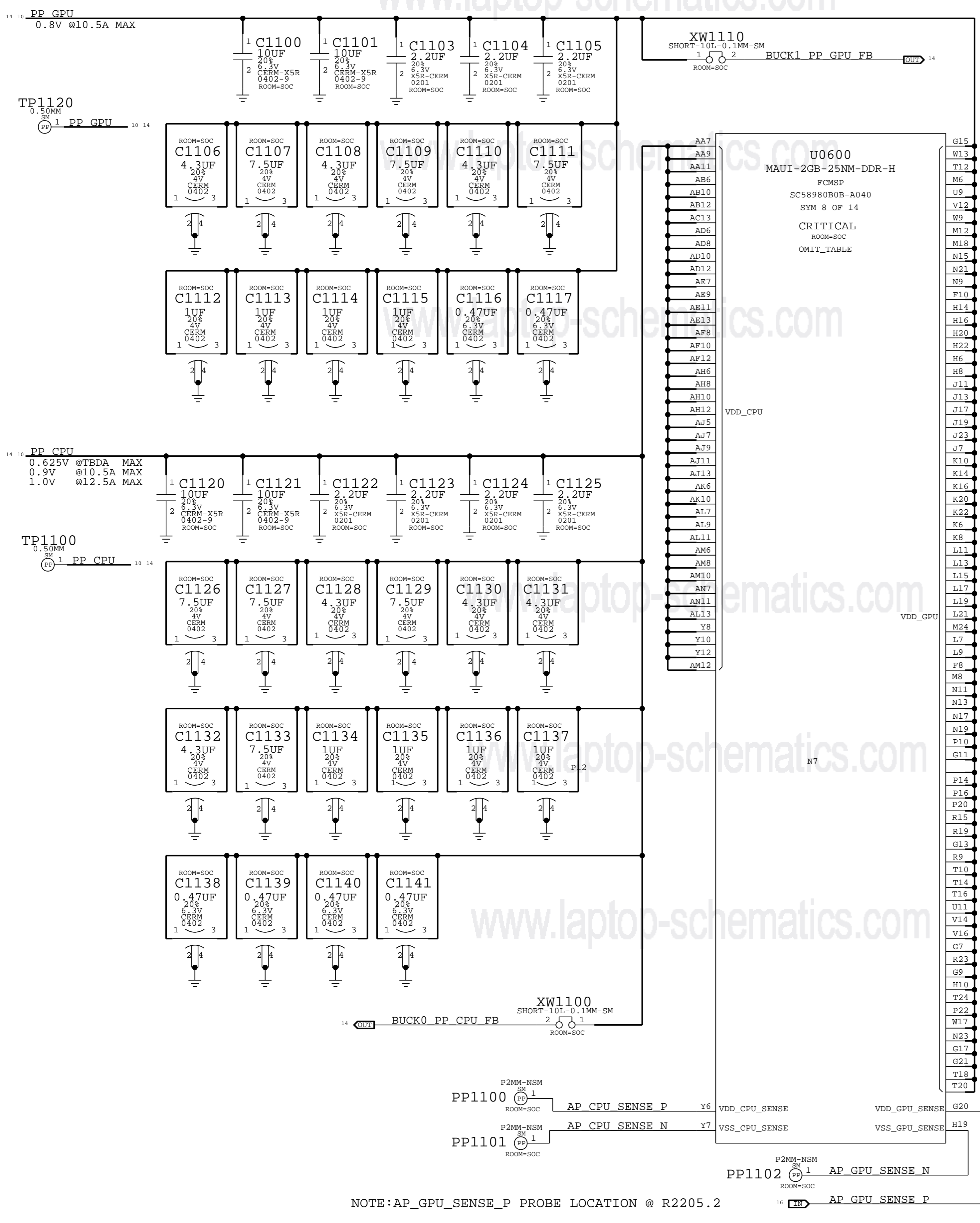
OWL SYSTEM SHUTDOWN OPTION



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MAUI - CPU, GPU & SOC RAILS

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SOC:POWER (1/3)			
Apple Inc.	DRAWING NUMBER	051-1902	
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	SHEET	10 OF 59	

MAUI - POWER SUPPLIES

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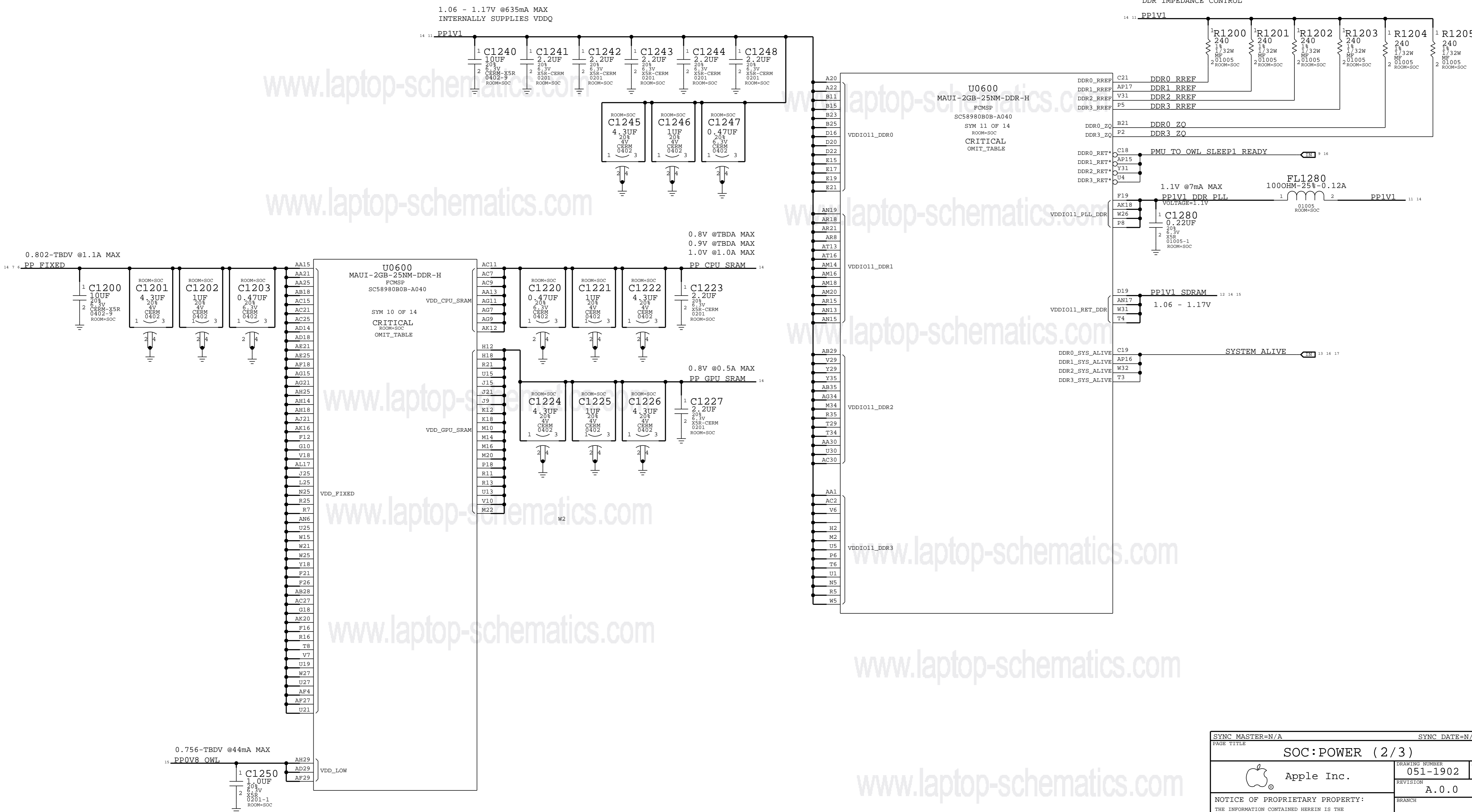
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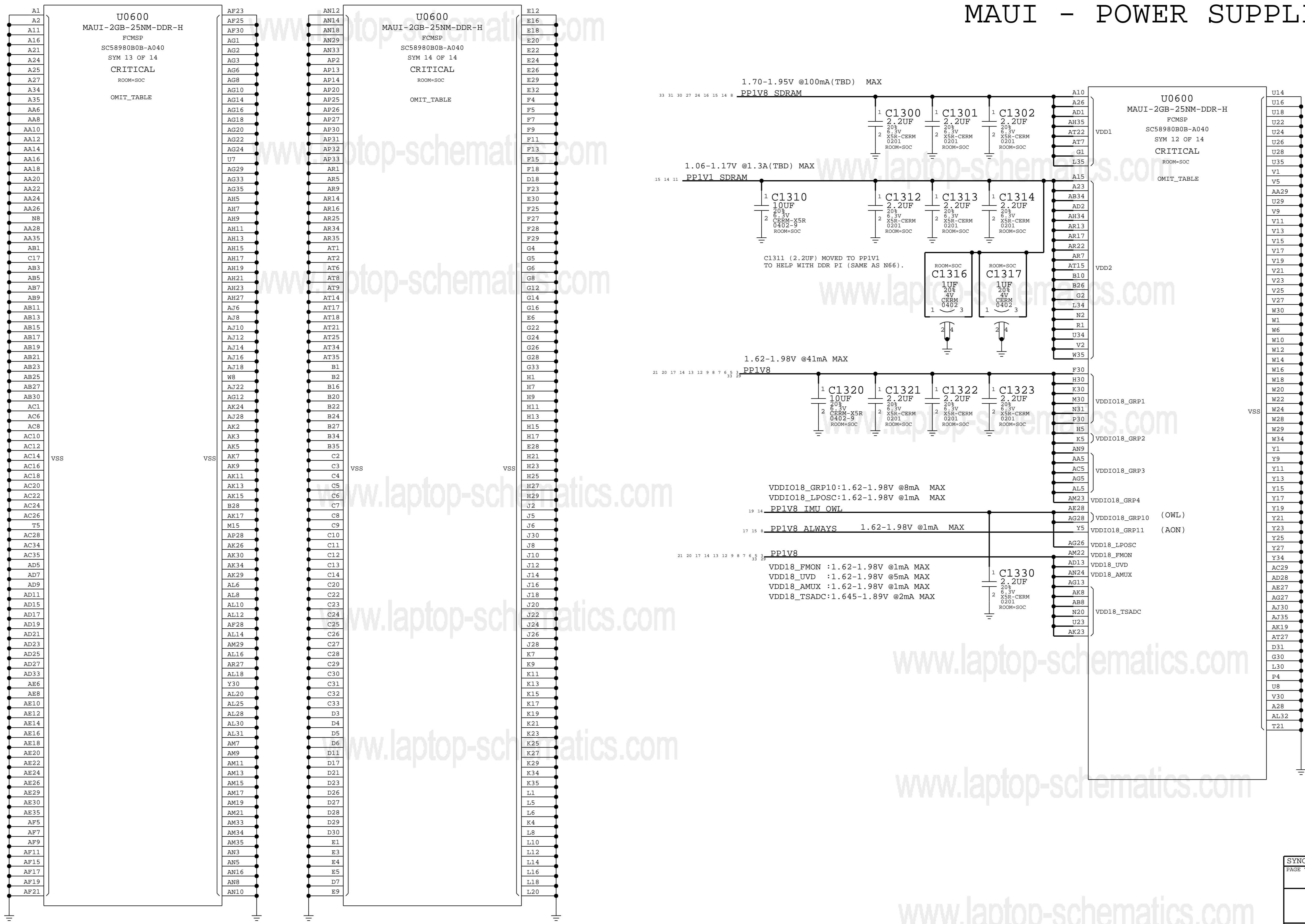
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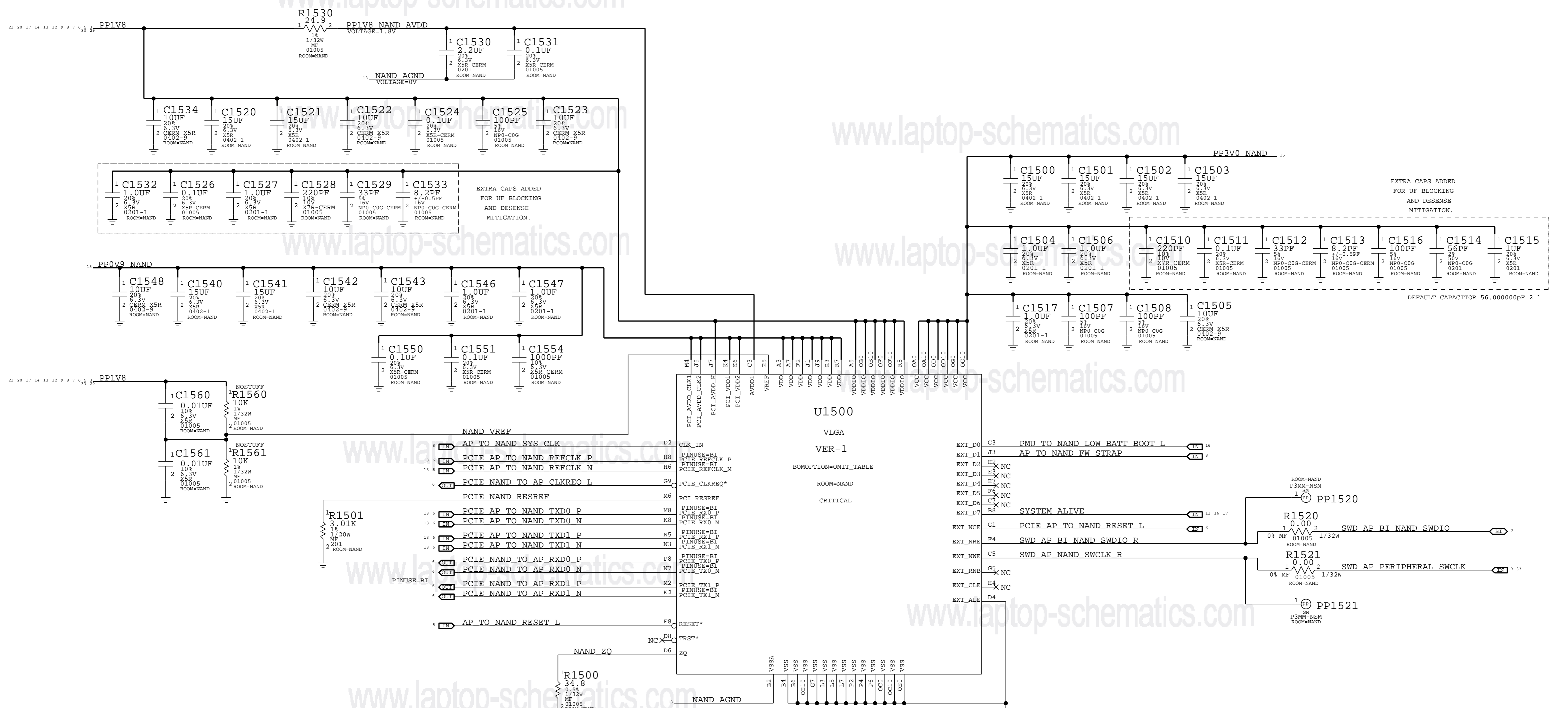
MAUI - POWER SUPPLIES



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			SHEET: 12 OF 59

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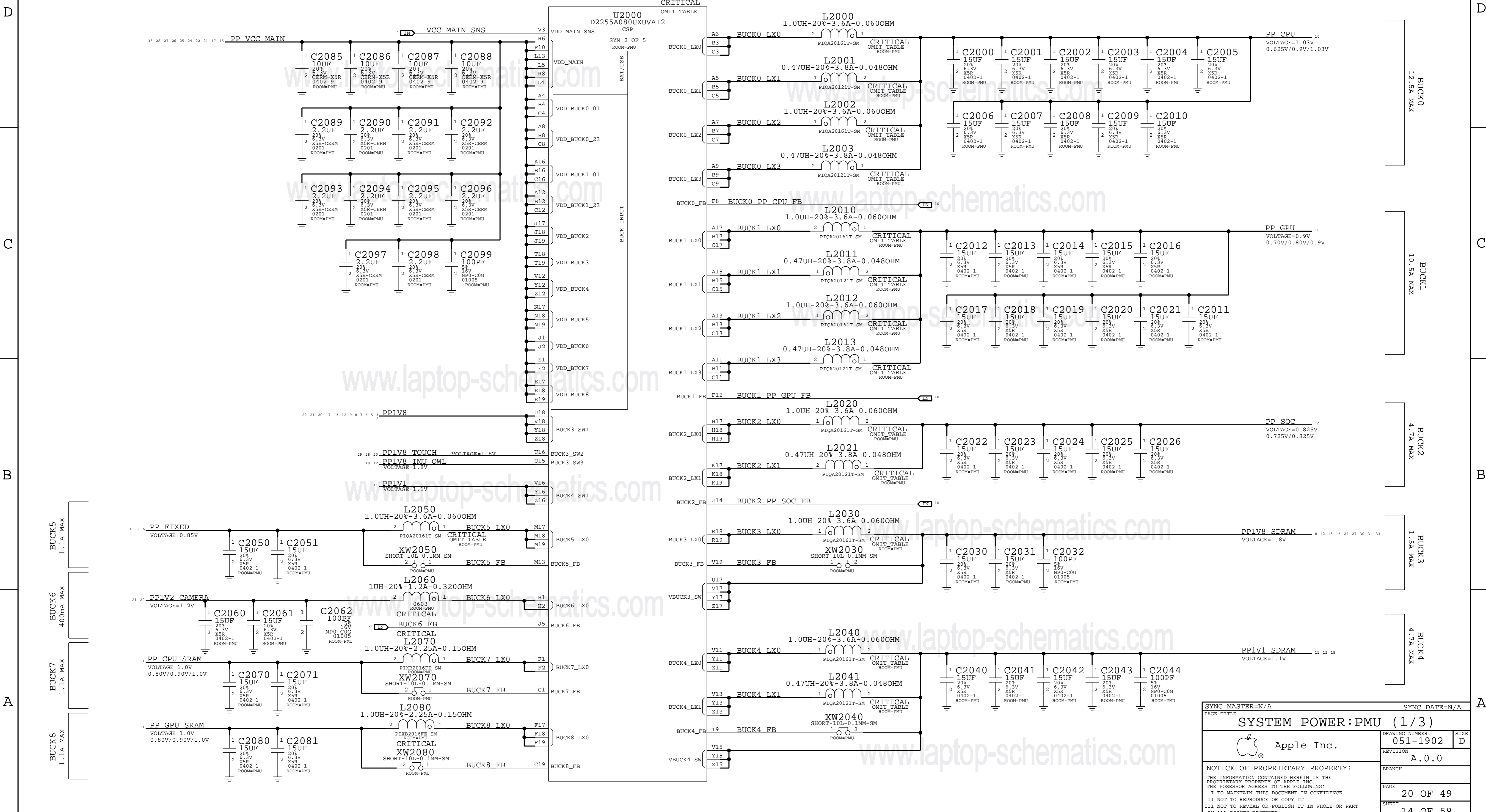
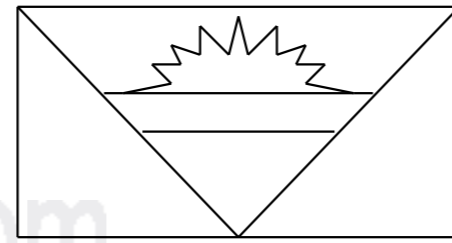
PCIE RECEIVE-SIDE PROBE POINTS

13	PCIE AP TO NAND REFCLK P	PP1500
13	PCIE AP TO NAND REFCLK N	PP1501
13	PCIE AP TO NAND TXD0 P	PP1502
13	PCIE AP TO NAND TXD0 N	PP1503
13	PCIE AP TO NAND TXD1 P	PP1504
13	PCIE AP TO NAND TXD1 N	PP1505

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ANTIGUA PMU - Buck Supplies

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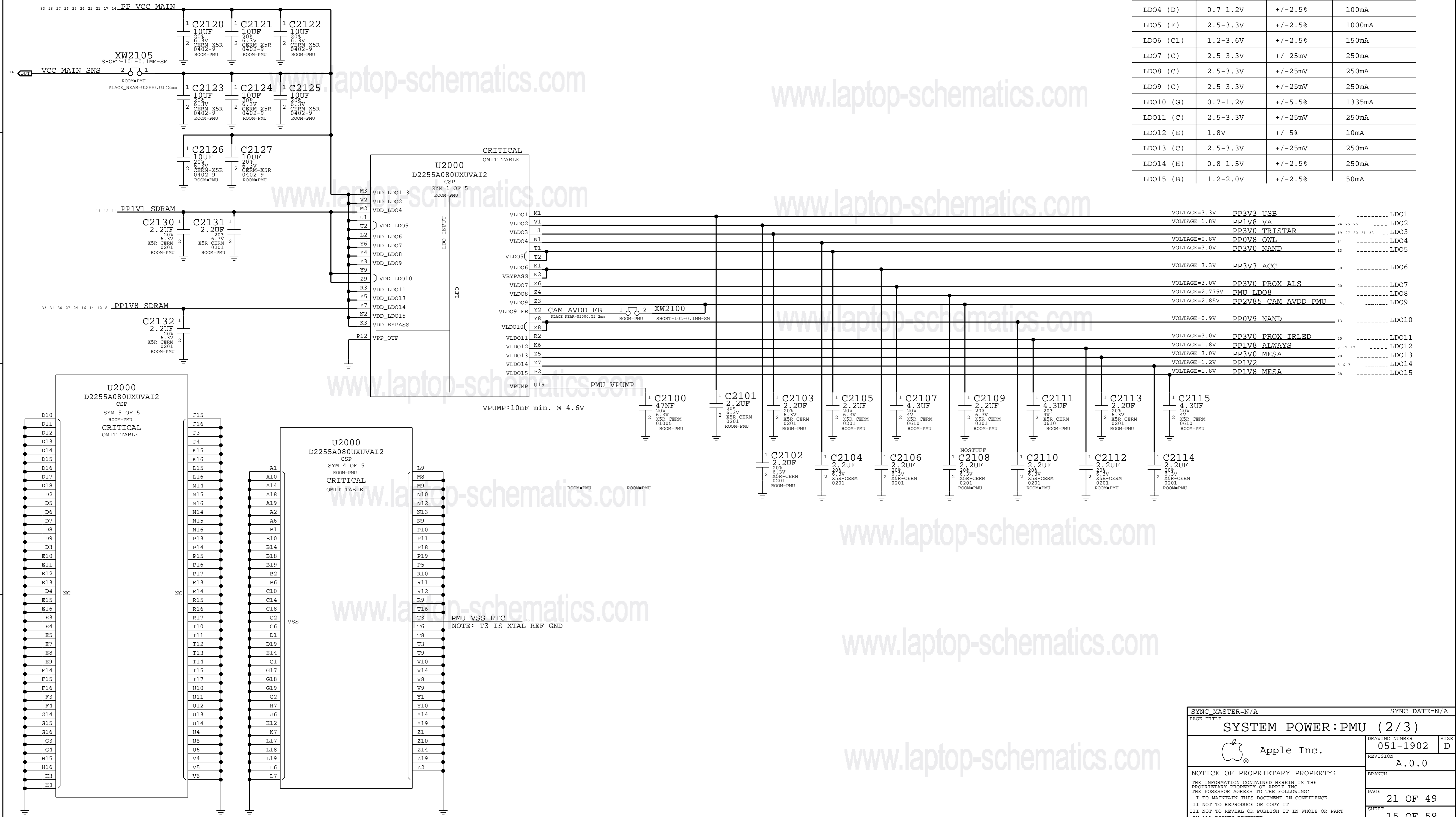
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ANTIGUA PMU - LDOs

ANTIGUA LDO SPECS

LDO#	ADJ. RANGE	ACCURACY	MAX. CURRENT
LDO1 (A)	2.5-3.3V	+/-1.4%	50mA
LDO2 (B)	1.2-2.0V	+/-2.5%	50mA
LDO3 (A)	2.5-3.3V	+/-1.4%	50mA
LDO4 (D)	0.7-1.2V	+/-2.5%	100mA
LDO5 (F)	2.5-3.3V	+/-2.5%	1000mA
LDO6 (C1)	1.2-3.6V	+/-2.5%	150mA
LDO7 (C)	2.5-3.3V	+/-25mV	250mA
LDO8 (C)	2.5-3.3V	+/-25mV	250mA
LDO9 (C)	2.5-3.3V	+/-25mV	250mA
LDO10 (G)	0.7-1.2V	+/-5.5%	1335mA
LDO11 (C)	2.5-3.3V	+/-25mV	250mA
LDO12 (E)	1.8V	+/-5%	10mA
LDO13 (C)	2.5-3.3V	+/-25mV	250mA
LDO14 (H)	0.8-1.5V	+/-2.5%	250mA
LDO15 (B)	1.2-2.0V	+/-2.5%	50mA

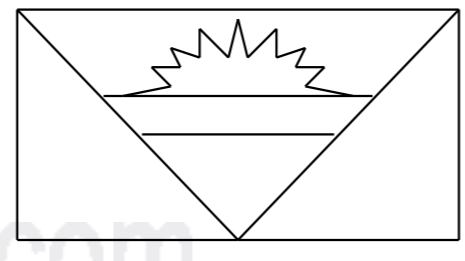


VOLTAGE	COMPONENT	LDO#
VOLTAGE=3.3V	PP3V3 USB	LDO1
VOLTAGE=1.8V	PP1V8 VA	LDO2
	PP3V0 TRISTAR	LDO3
VOLTAGE=0.8V	PP0V8 OWL	LDO4
VOLTAGE=3.0V	PP3V0 NAND	LDO5
VOLTAGE=3.3V	PP3V3 ACC	LDO6
VOLTAGE=3.0V	PP3V0 PROX ALS	LDO7
VOLTAGE=2.775V	PMU LDO8	LDO8
VOLTAGE=2.85V	PP2V85 CAM AVDD PMU	LDO9
VOLTAGE=0.9V	PP0V9 NAND	LDO10
VOLTAGE=3.0V	PP3V0 PROX IRLED	LDO11
VOLTAGE=1.8V	PP1V8 ALWAYS	LDO12
VOLTAGE=3.0V	PP3V0 MESA	LDO13
VOLTAGE=1.2V	PP1V2	LDO14
VOLTAGE=1.8V	PP1V8 MESA	LDO15

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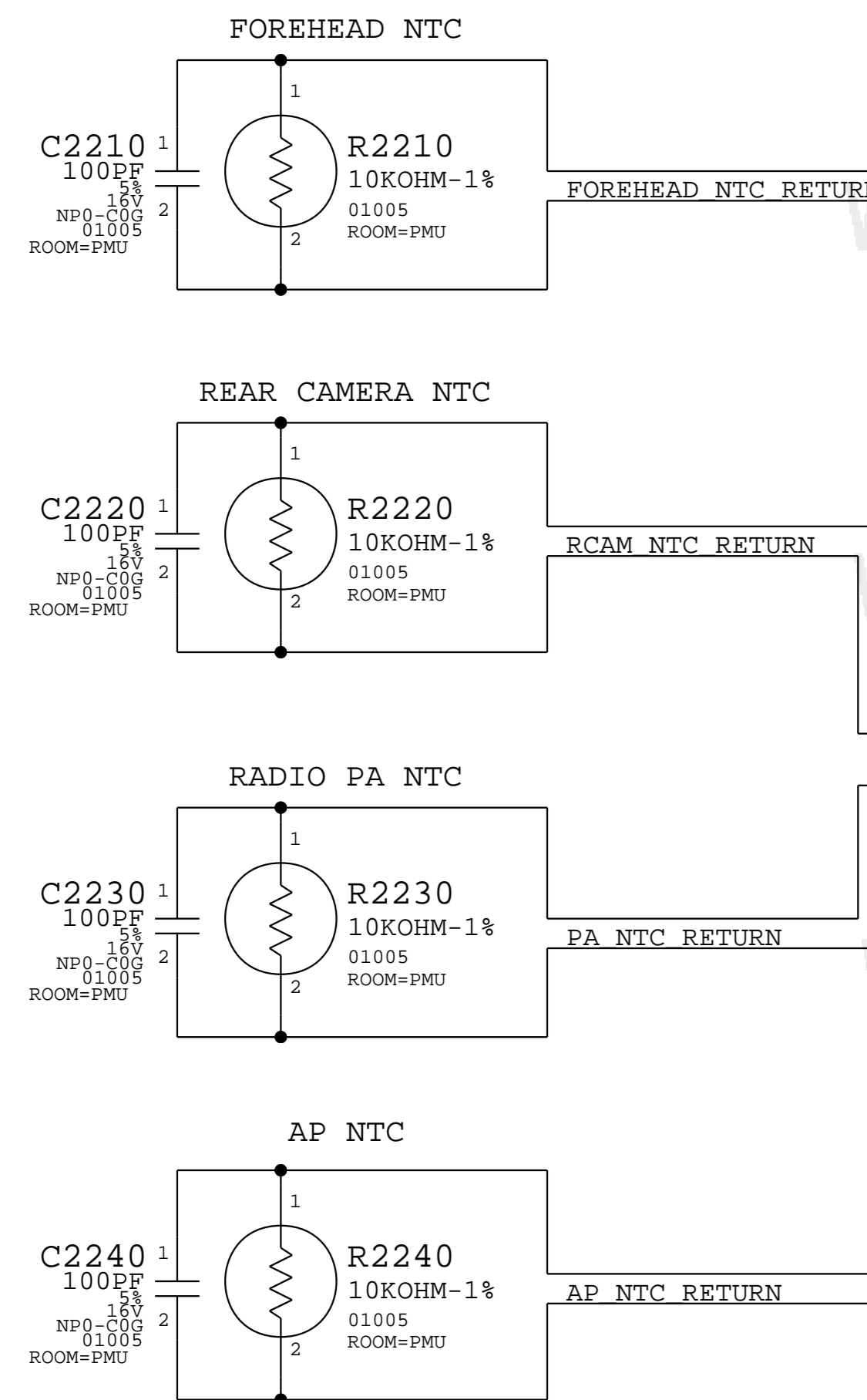
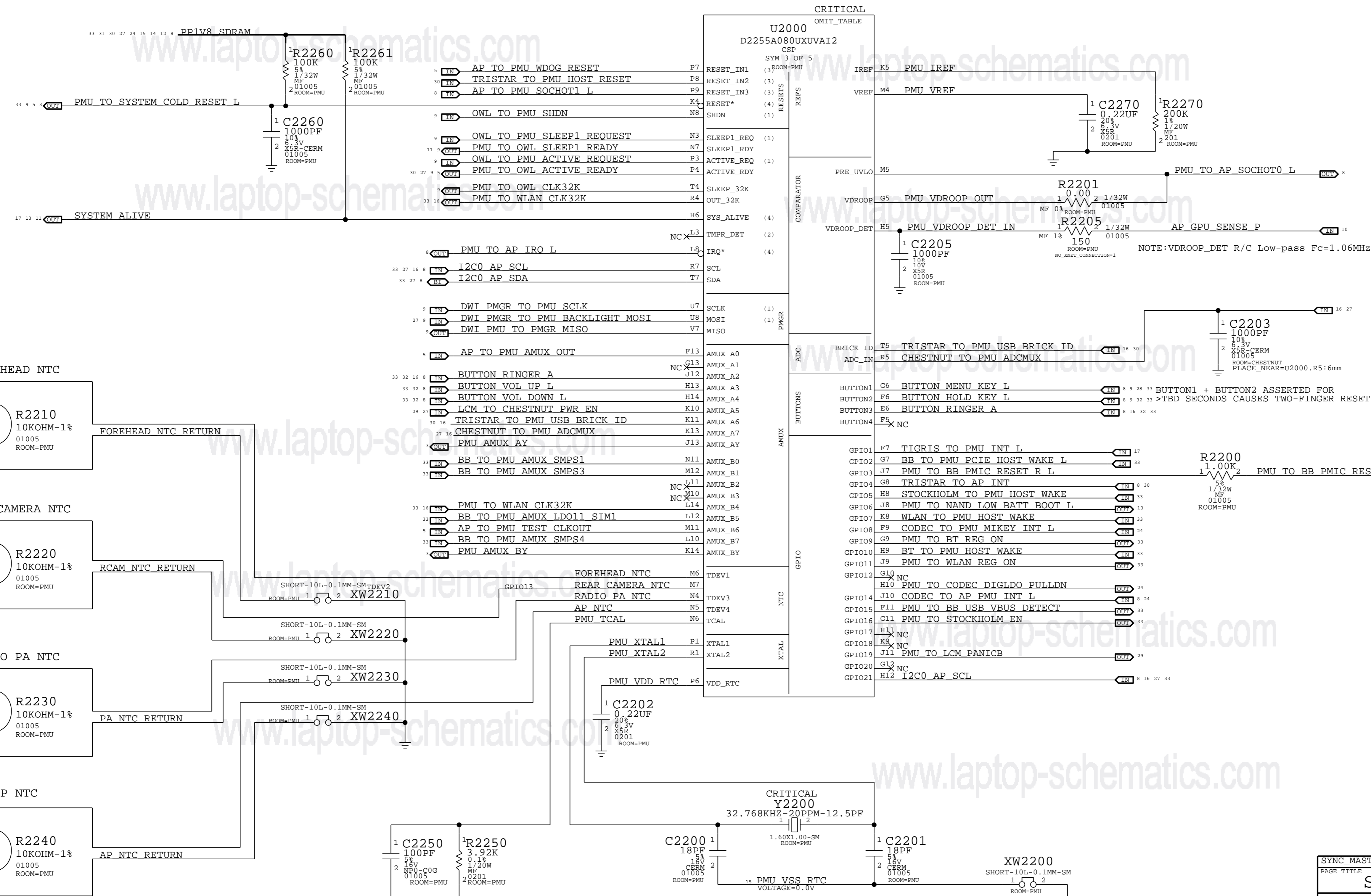
ANTIGUA PMU - GPIOs, NTCs

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CONTROL PIN NOTES:

- NOTE (1): INPUT PULL-DOWN 100-300k
- NOTE (2): INPUT PULL-DOWN 1M
- NOTE (3): INPUT PULL-UP OR DOWN 100k-300k
- NOTE (4): OUTPUT OPEN-DRAIN, REQUIRES PULL-UP



NOTE: 100PF CAPS ARE THE SAMPLING CAPS FOR PMU ADC

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		16 OF 59	

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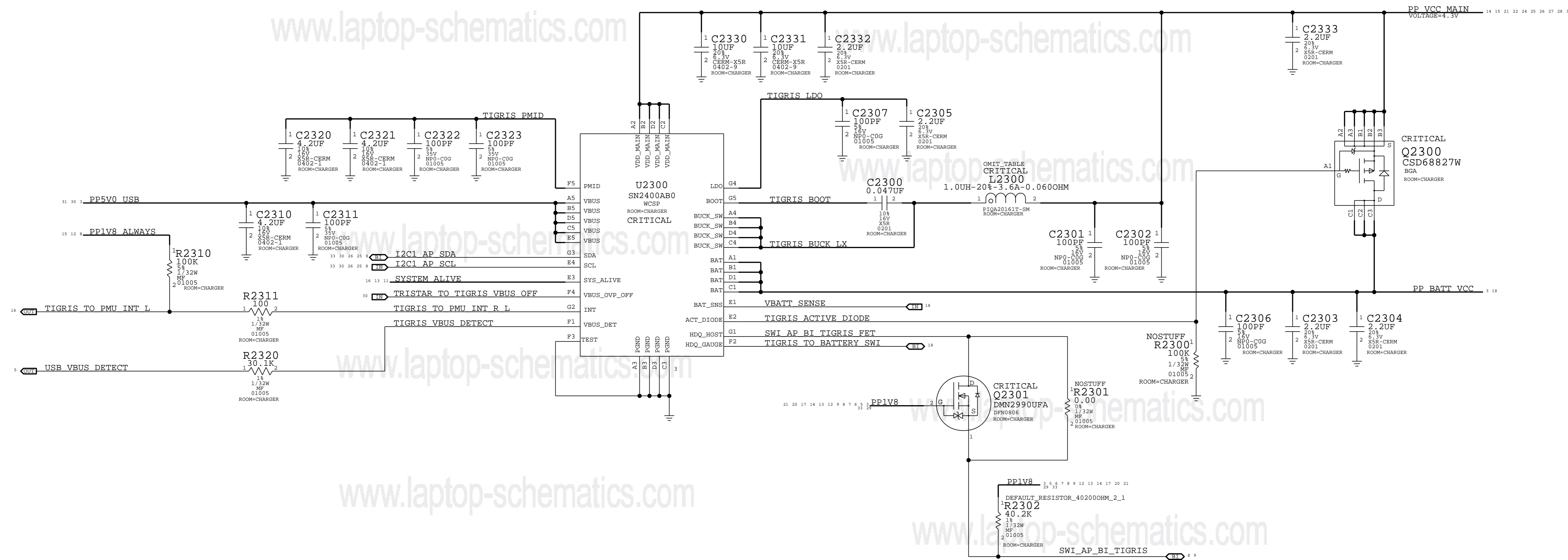
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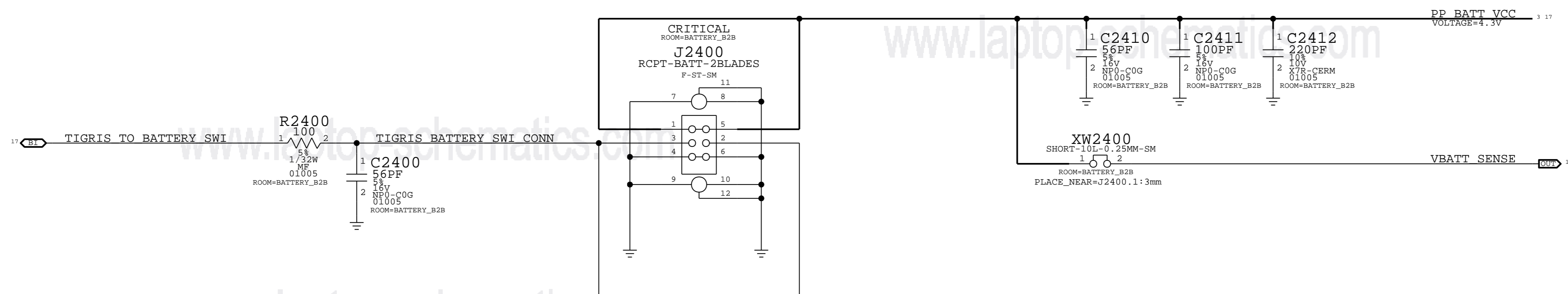
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BATTERY CONNECTOR

THIS ONE ON MLB ----> 516S00104 (RCPT)
516S00105 (PLUG)



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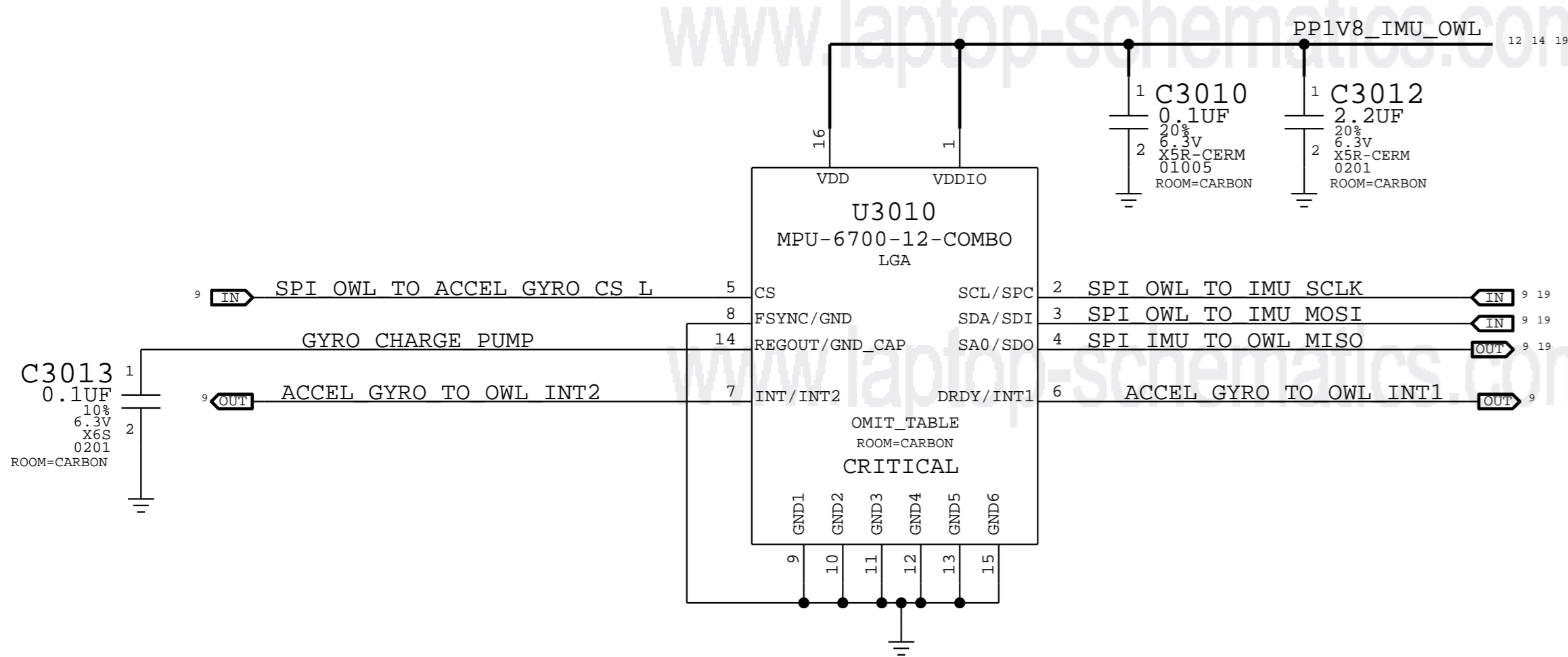
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SHEET	18 OF 59		

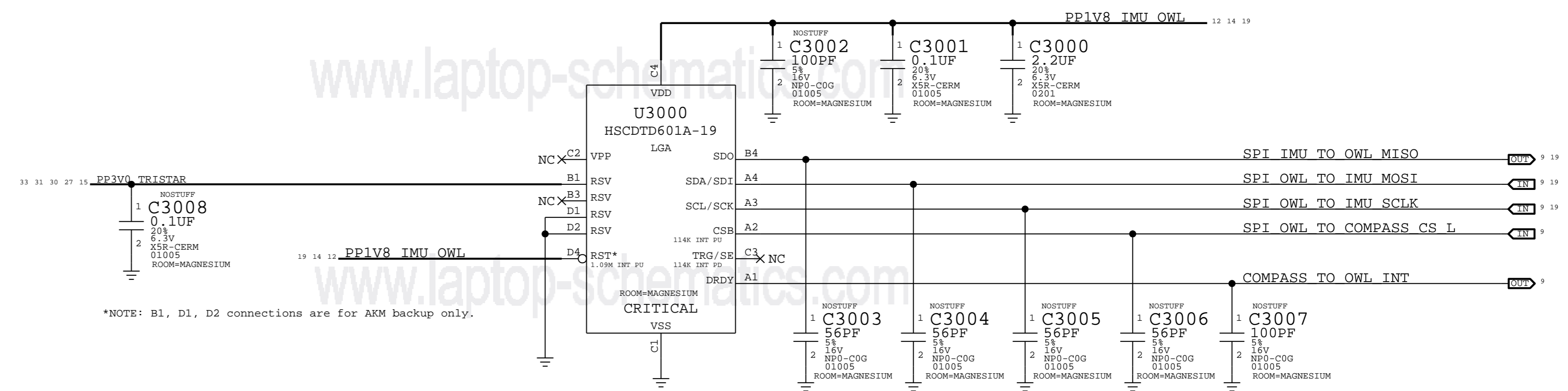
CARBON - ACCEL & GYRO

INVENSENSE (APN: 338S00017, 338S00087)



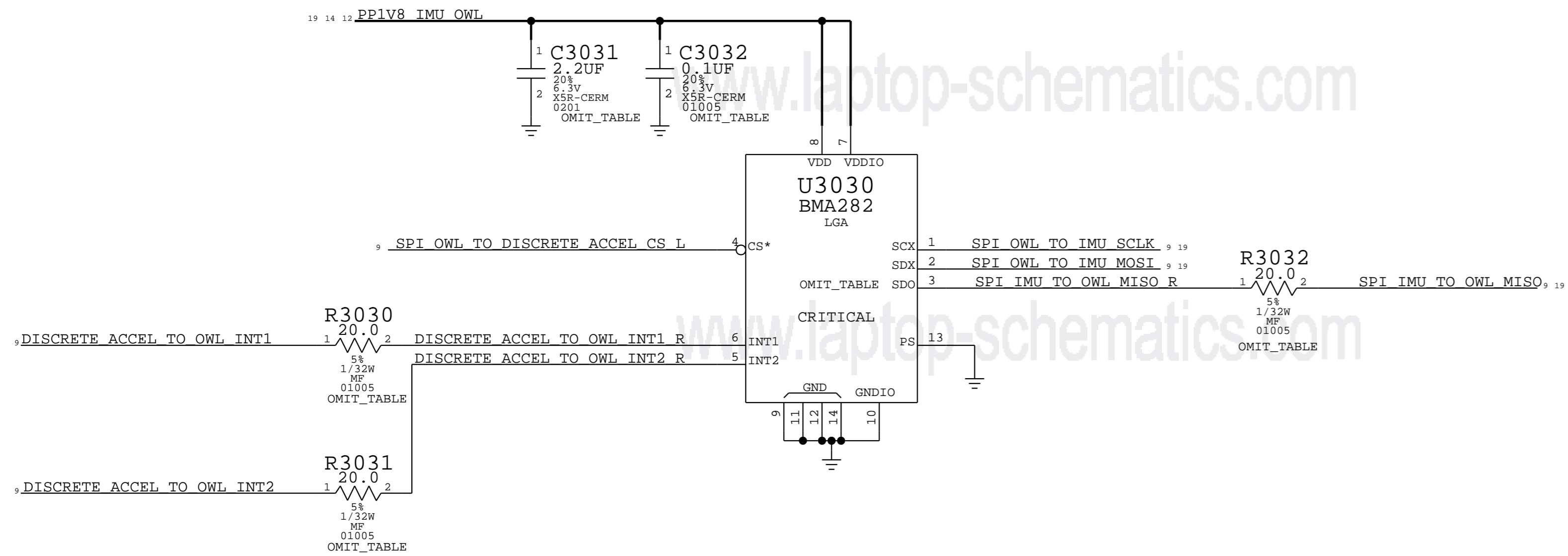
MAGNESIUM - COMPASS

ALPS (APN:338S00084)



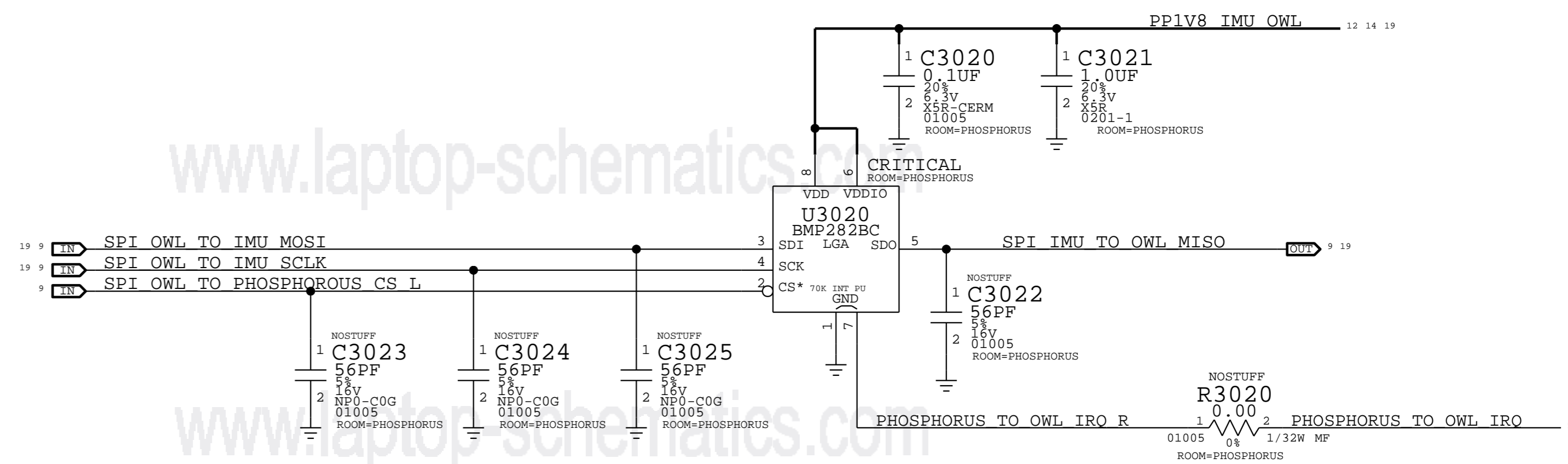
DISCRETE ACCEL

BOSCH (APN: 338S1163)



PHOSPHOROUS

BOSCH (APN:338S00044)



R3020 SHOULD BE STUFFED FOR ST PHOSPHOROUS ONLY. FOR BOSCH PHOSPHOROUS, PINS 1 AND 7 ARE SHORTED INTERNALLY, SO NO NEED FOR 0-0HM TO GROUND OPTION ON PIN 7.

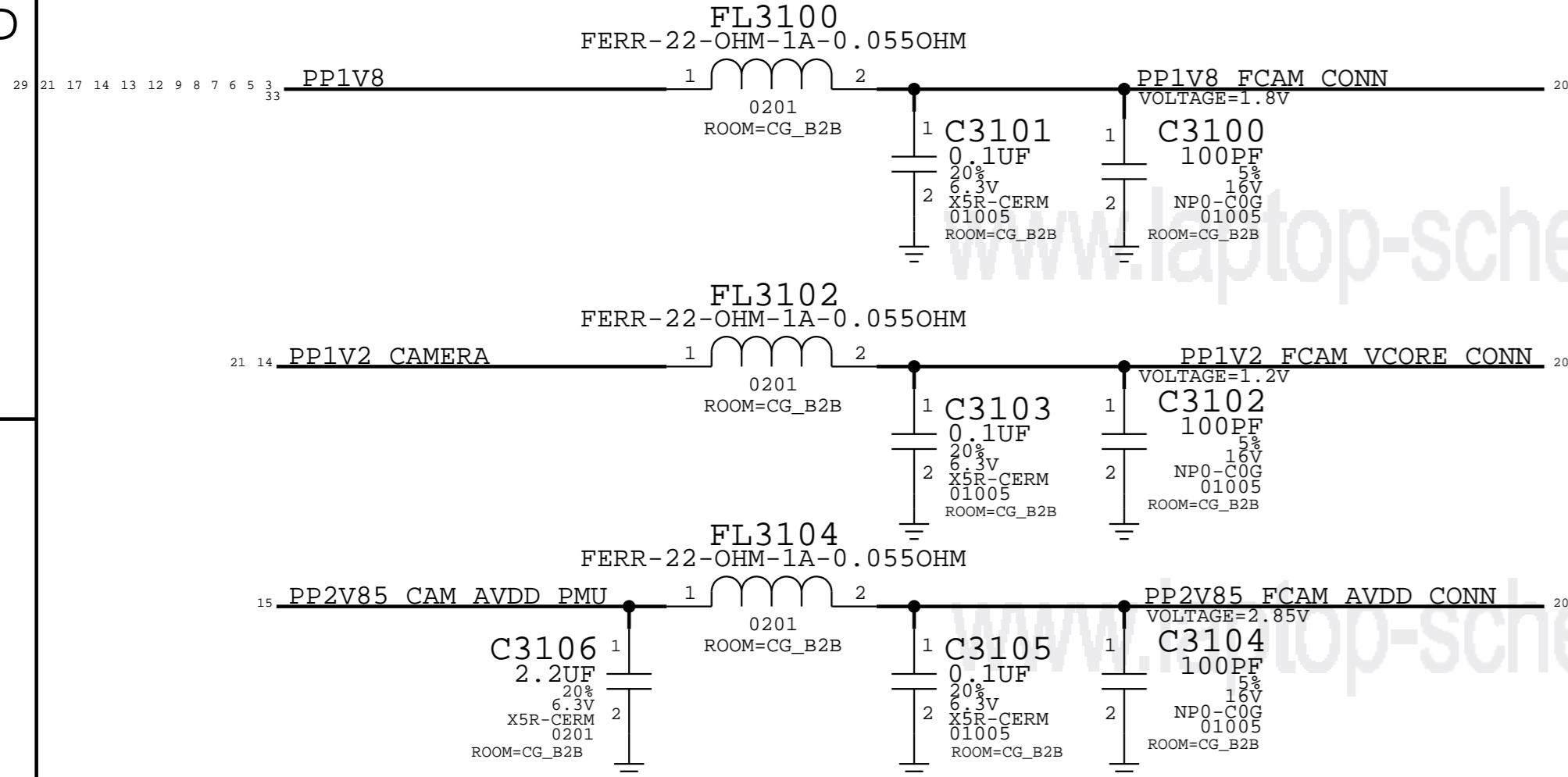
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SENSORS:MOTION SENSORS			
Apple Inc.	DRAWING NUMBER	051-1902	SIZE
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FOREHEAD FLEX (FCAM)

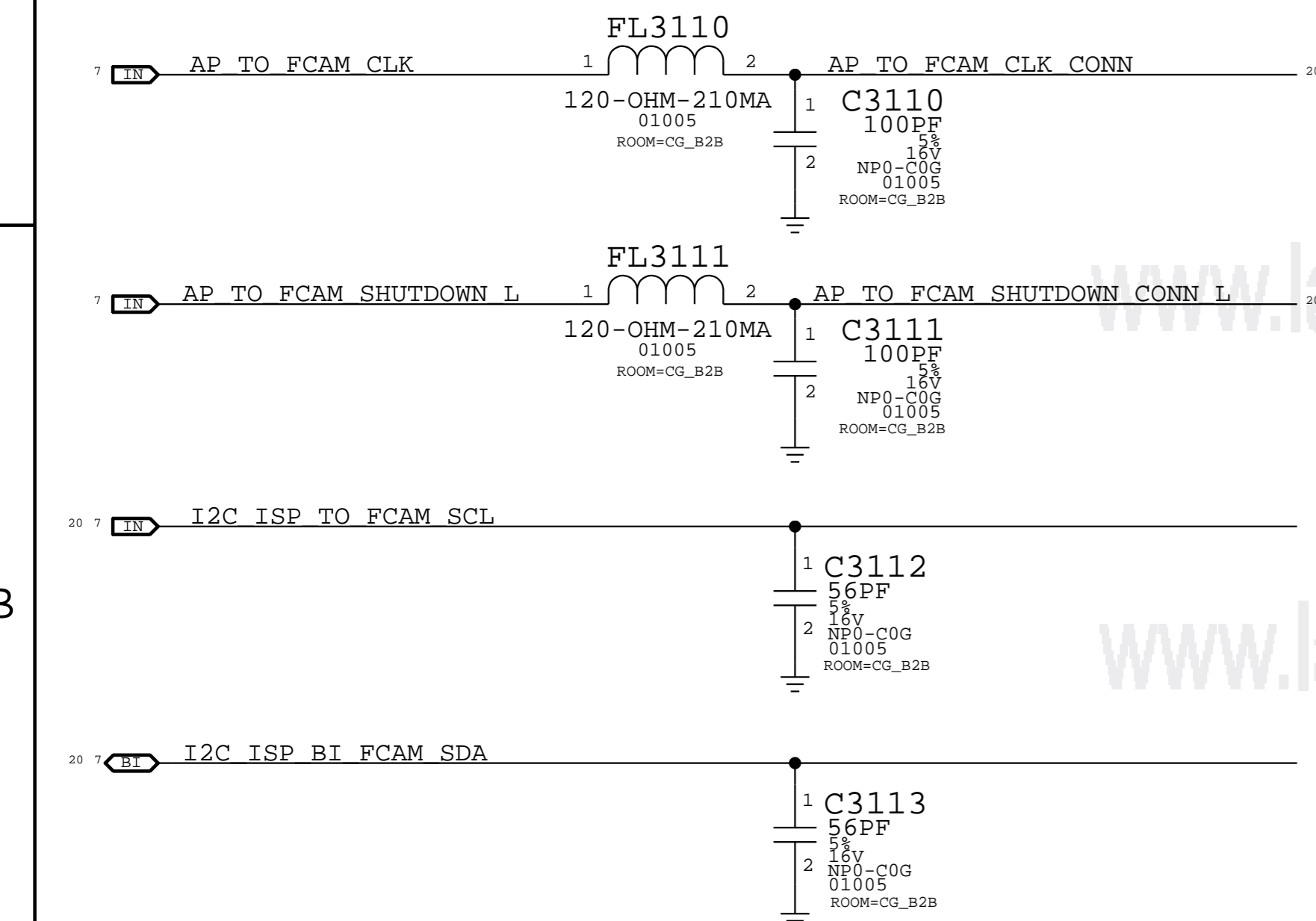
PROX & ALS POWER

FOREHEAD CONNECTOR

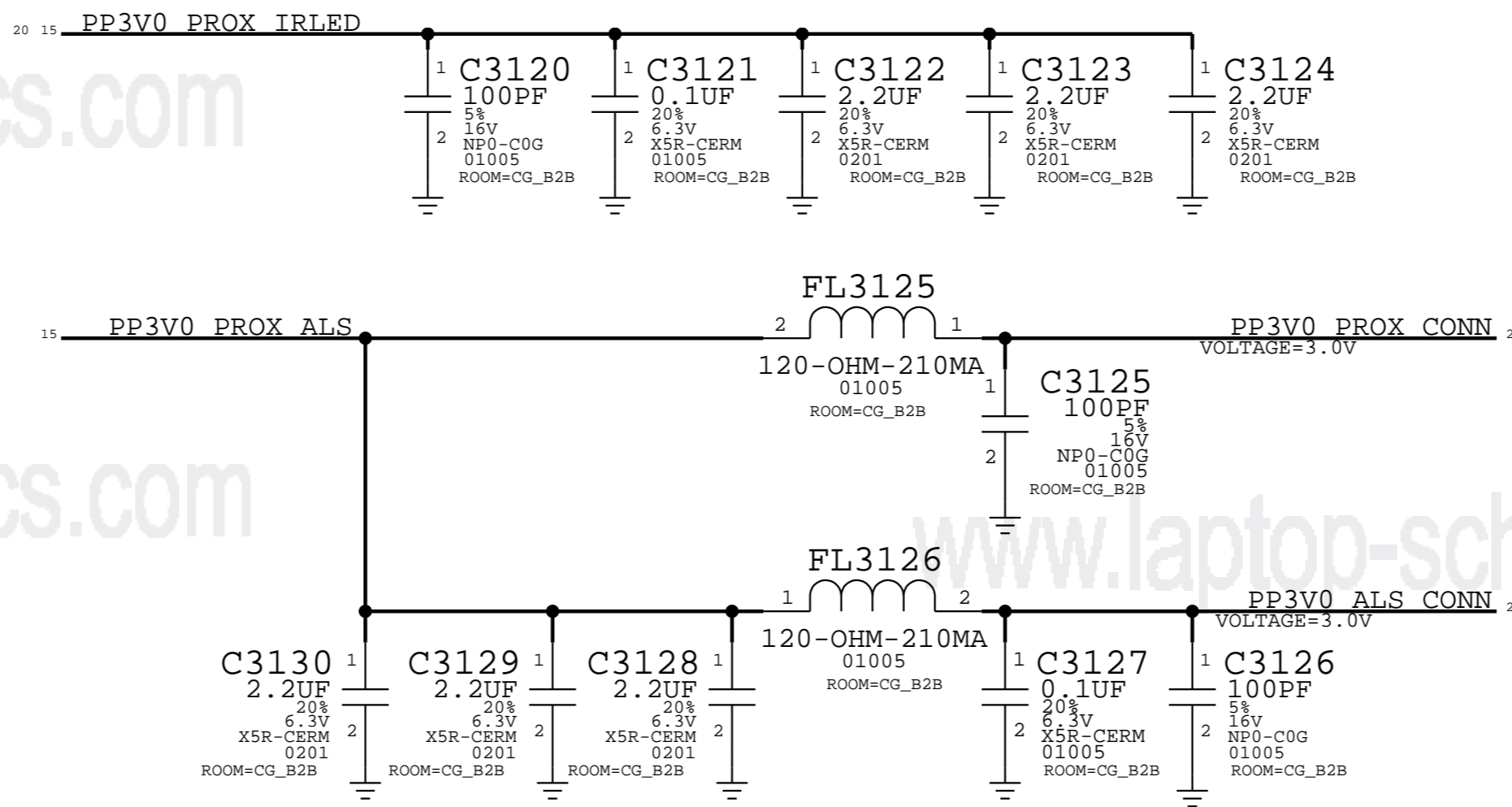
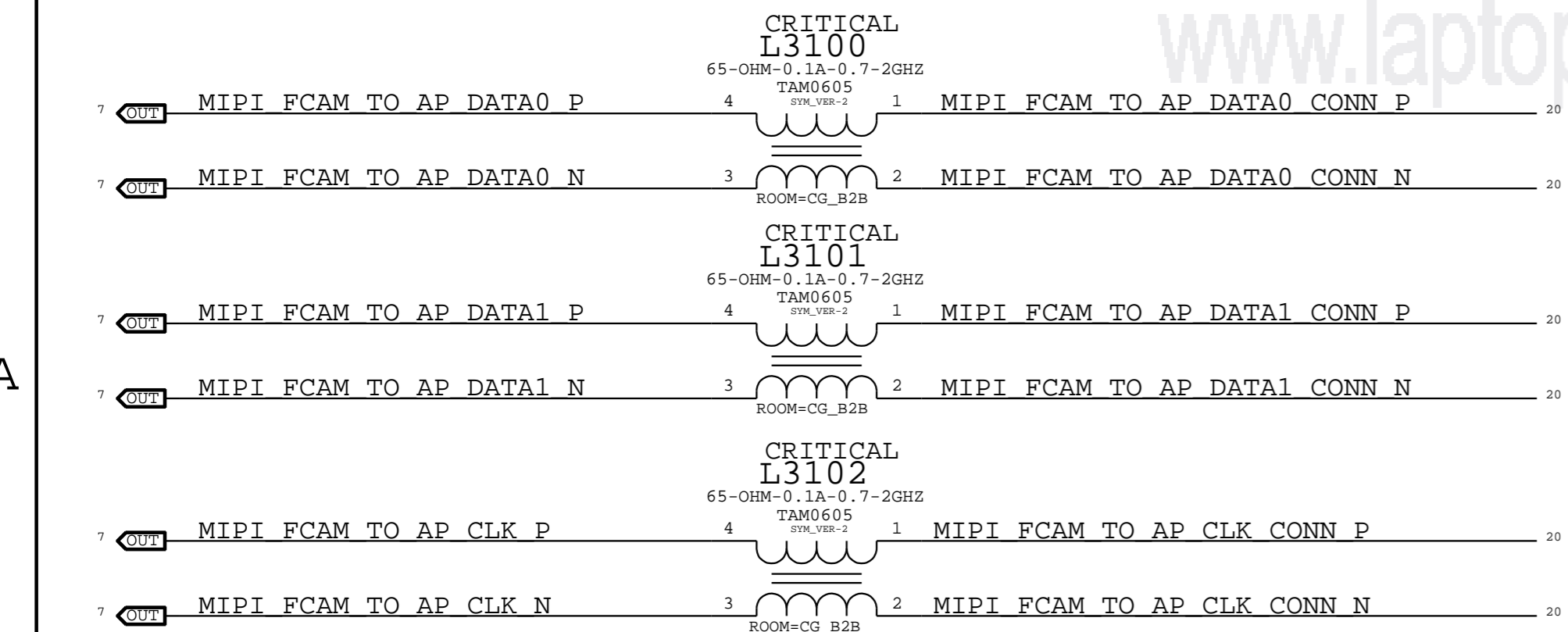
CAMERA POWER



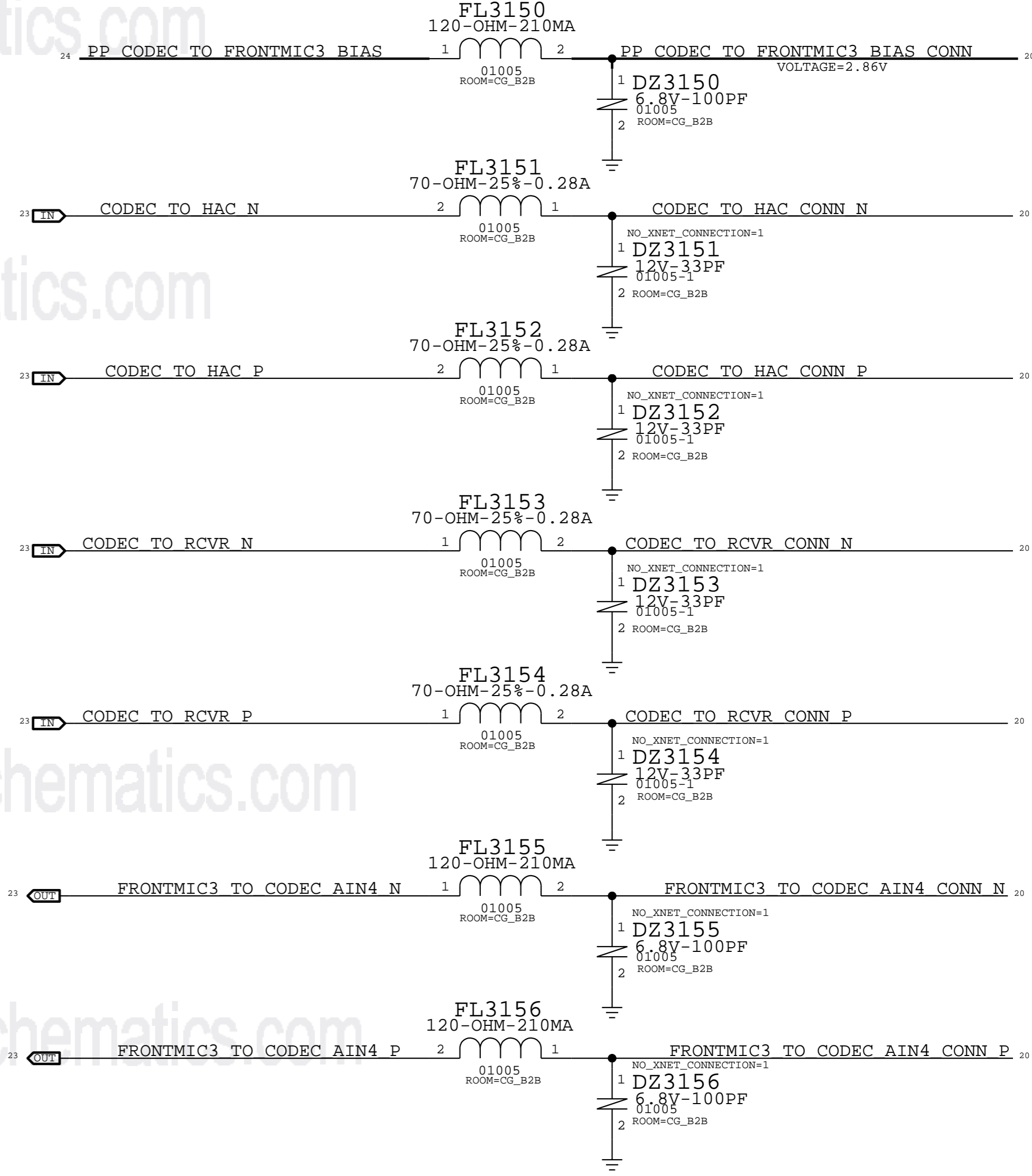
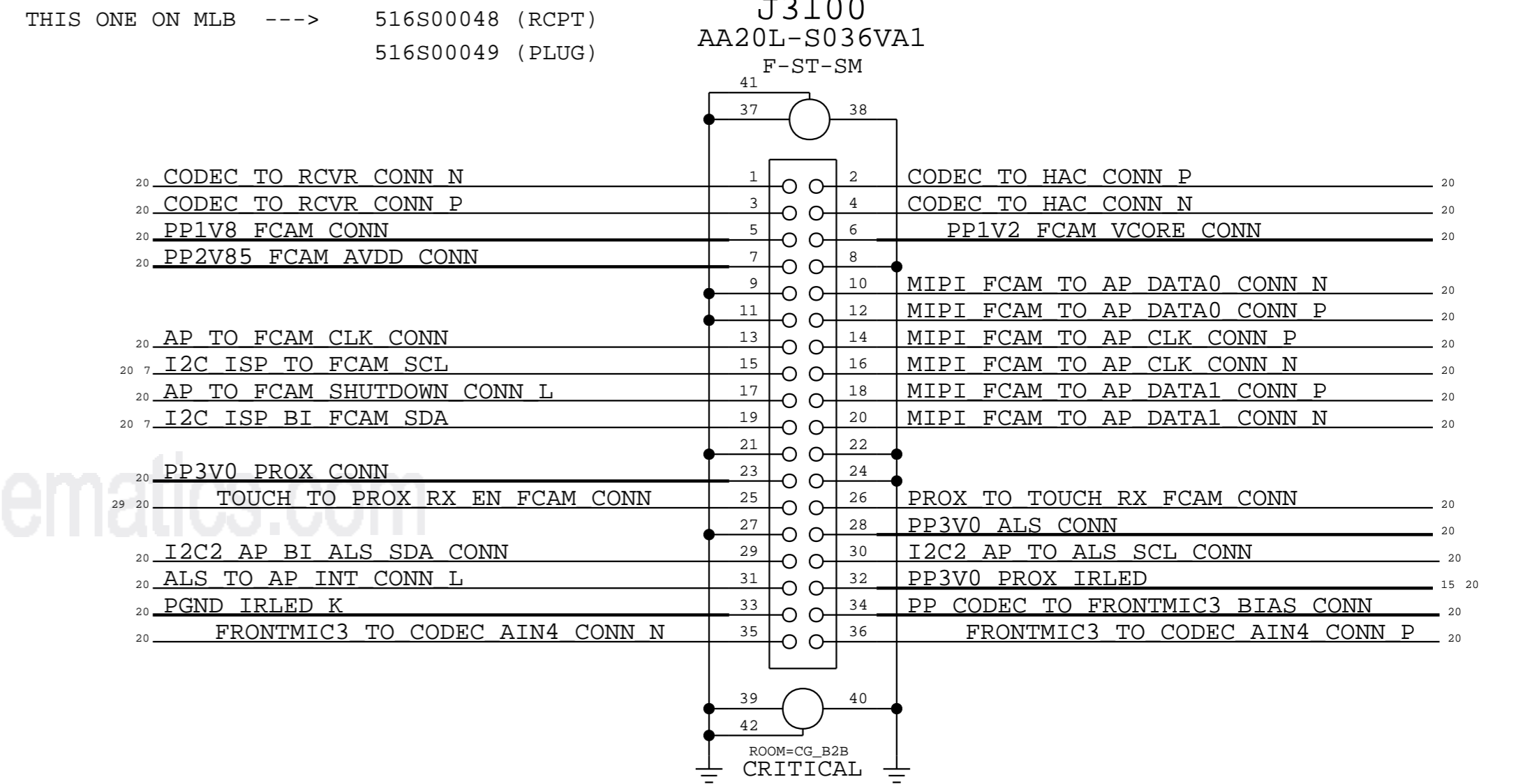
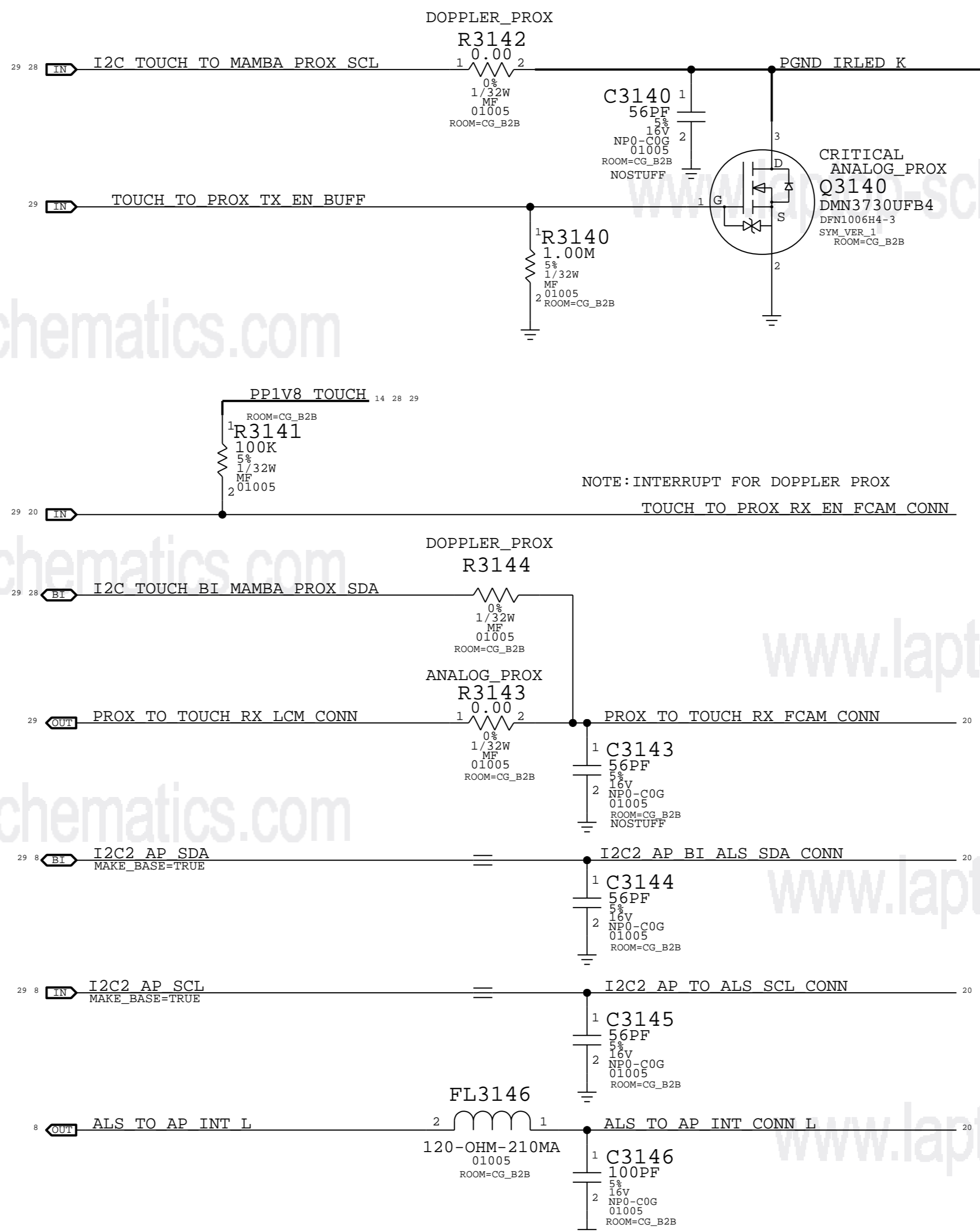
CAMERA I/O



CAMERA MIPI



PROX & ALS INTERFACE



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PAGE TITLE			
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Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
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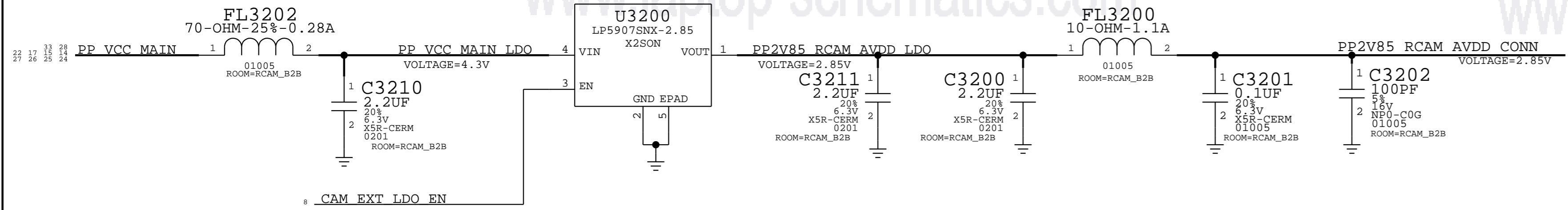
REAR CAMERA FLEX

RCAM CONNECTOR

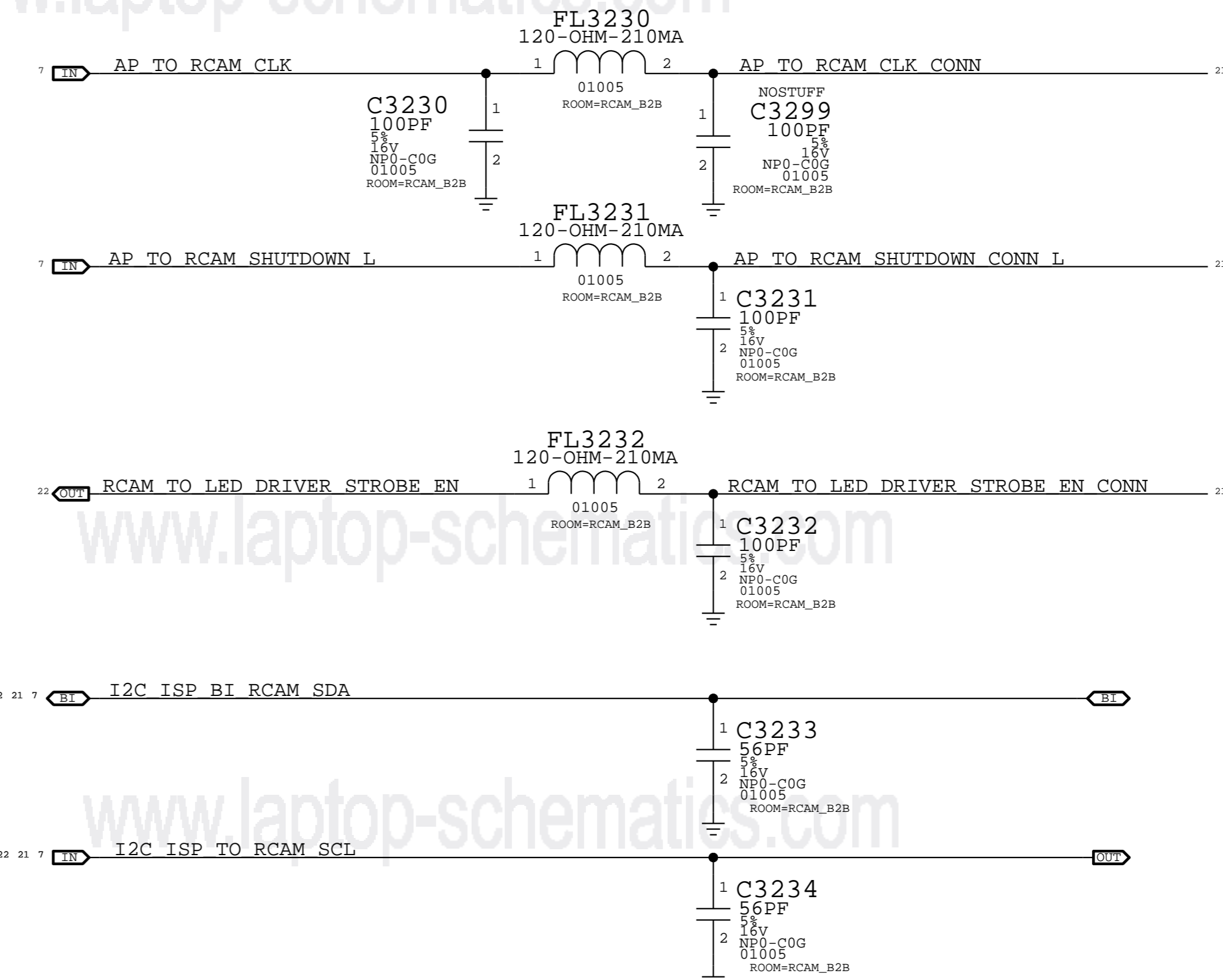
THIS ONE ON MLB ----> 516S00043 (RCPT)
516S00042 (PLUG)

CAMERA POWER

NOTE: OUTPUT IMPEDANCE MUST BE >0.01-OHM
IN ORDER TO MEET CAP ESR REQUIREMENT PER LDO SPEC.



DIGITAL I/O



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE CAMERA:REAR CAMERA B2B			
Apple Inc.	DRAWING NUMBER	051-1902	SIZE D
	REVISION	A.0.0	
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		SHEET	21 OF 59

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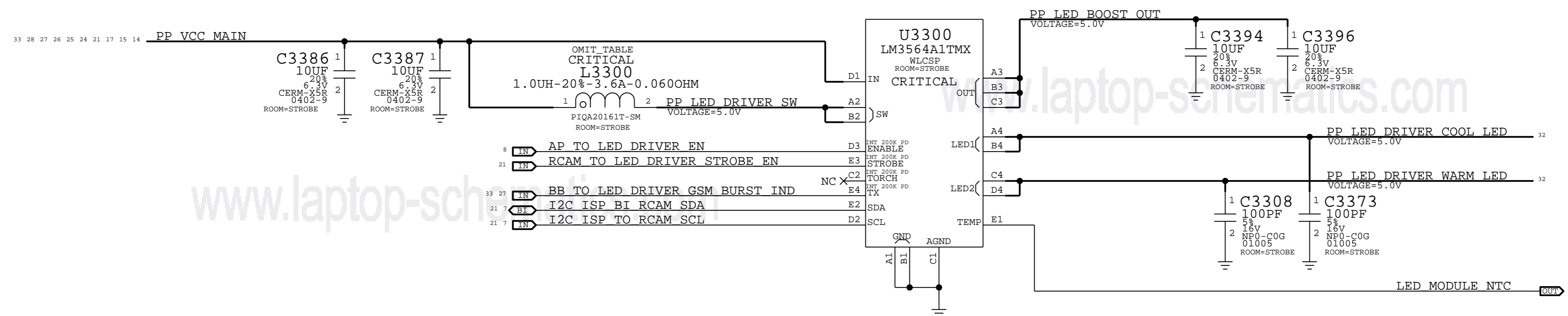
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DUAL LED STROBE DRIVER

APN: 353S3899



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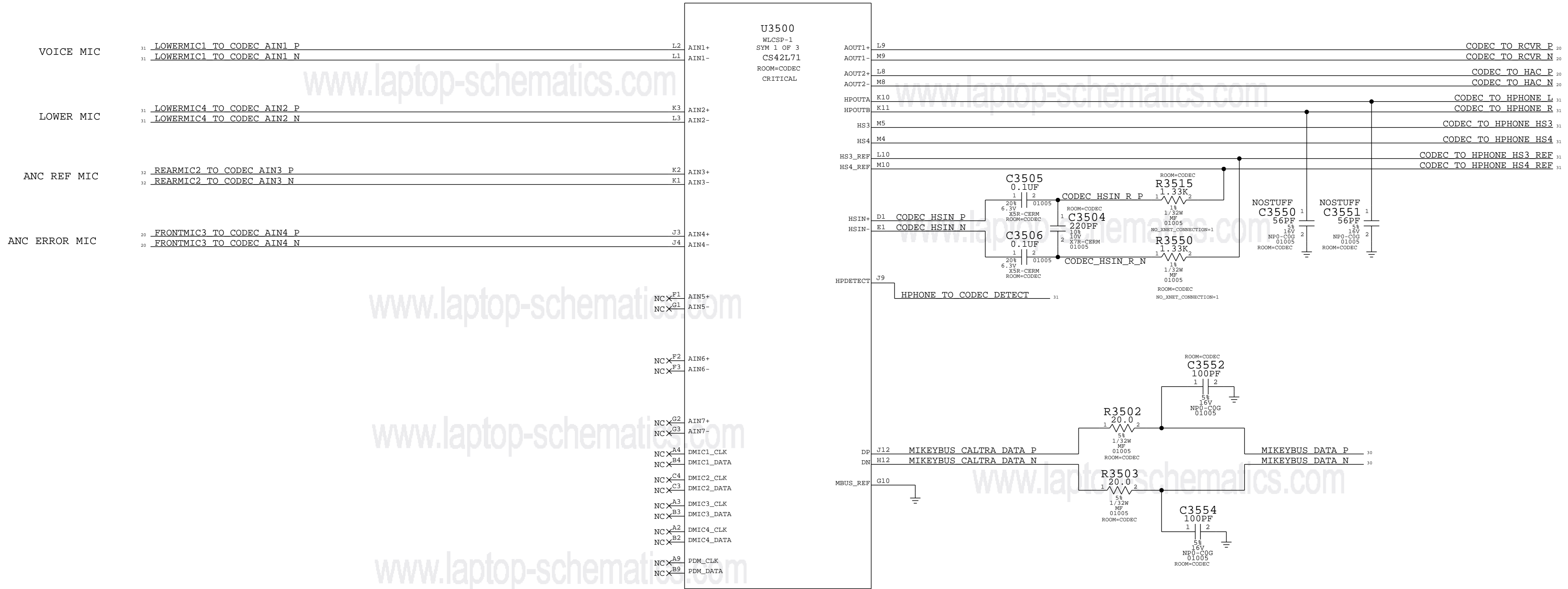
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Apple Inc.	DRAWING NUMBER	SIZE	
	051-1902	D	
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	BRANCH		
	PAGE	33 OF 49	
SHEET	22 OF 59		

CALTRA AUDIO CODEC (ANALOG INPUTS & OUTPUTS)

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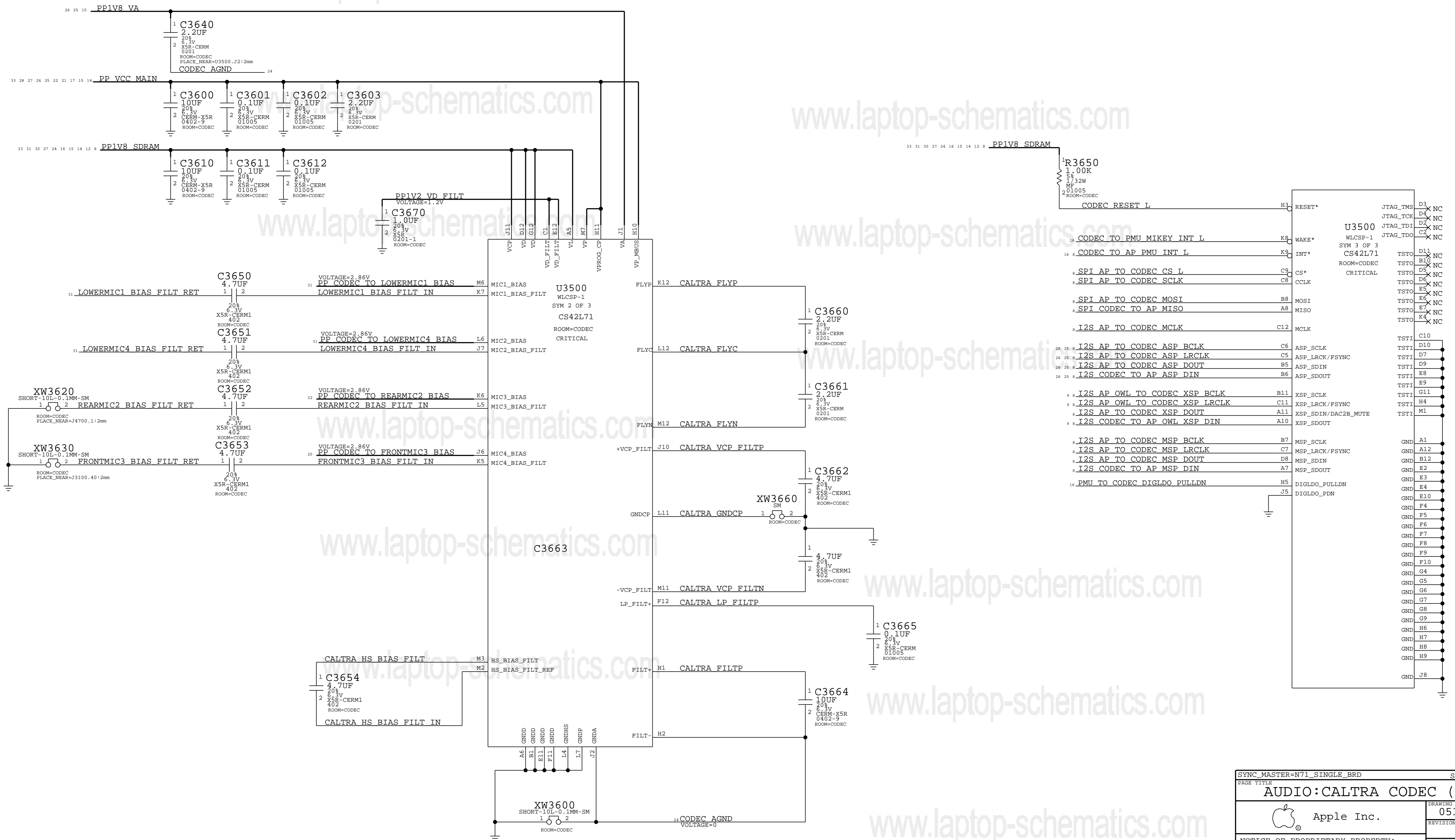
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SYNC_MASTER=N71_SINGLE_BRD		SYNC_DATE=05/29/2014	
PAGE TITLE			
AUDIO: CALTRA CODEC (1/2)			
Apple Inc.	DRAWING NUMBER	051-1902	SIZE
	REVISION	A.0.0	D
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		PAGE	35 OF 49
		SHEET	23 OF 59

CALTRA AUDIO CODEC (POWER & I/O)

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SYNC_MASTER=N71_SINGLE_BRD		SYNC_DATE=05/29/2014	
PAGE TITLE			
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	DRAWING NUMBER	051-1902	SIZE
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		SHEET	24 OF 59

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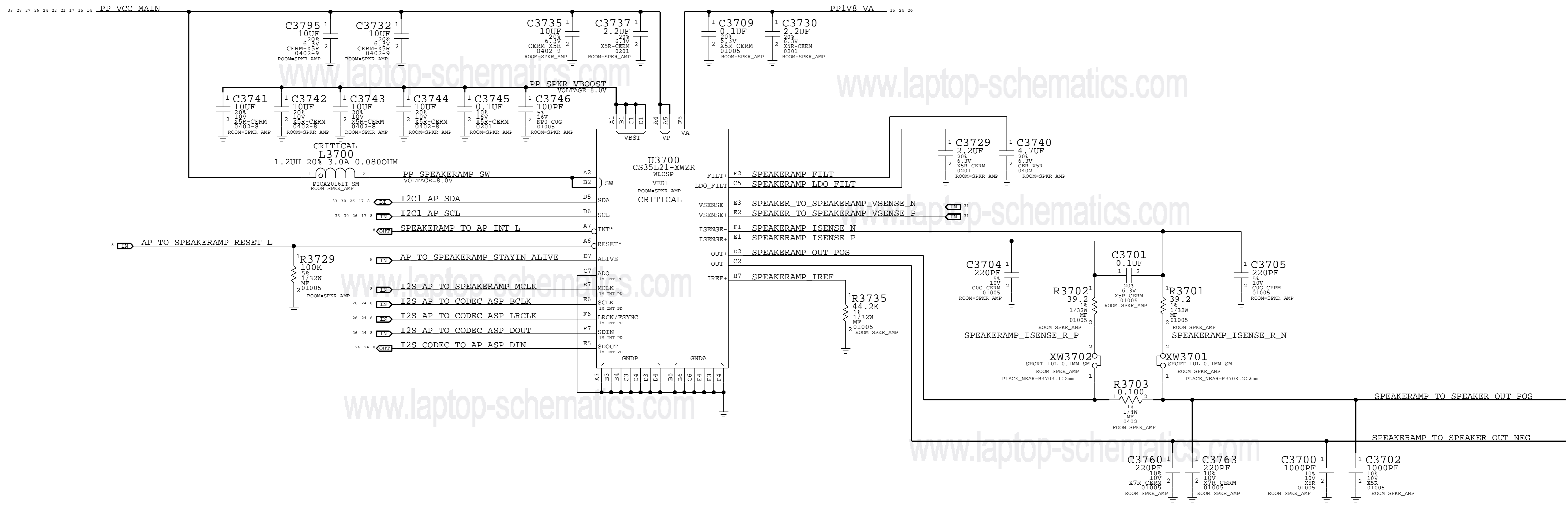
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SPEAKER AMPLIFIER

APN: 338S1285



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE AUDIO: SPEAKER DRIVER			
Apple Inc.	DRAWING NUMBER	051-1902	SIZE D
	REVISION	A.0.0	
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		PAGE	37 OF 49
		SHEET	25 OF 59

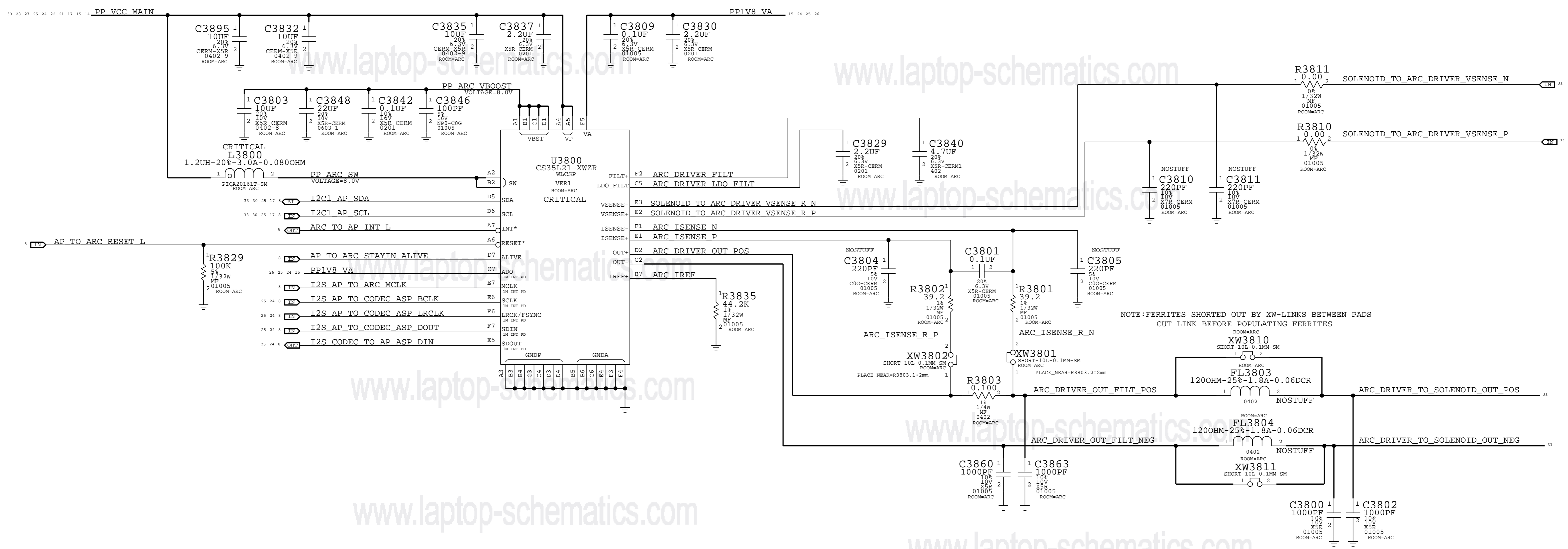
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ARC DRIVER

APN: 338S1285



NOTE: FERRITES SHORTED OUT BY XW-LINKS BETWEEN PADS
CUT LINK BEFORE POPULATING FERRITES

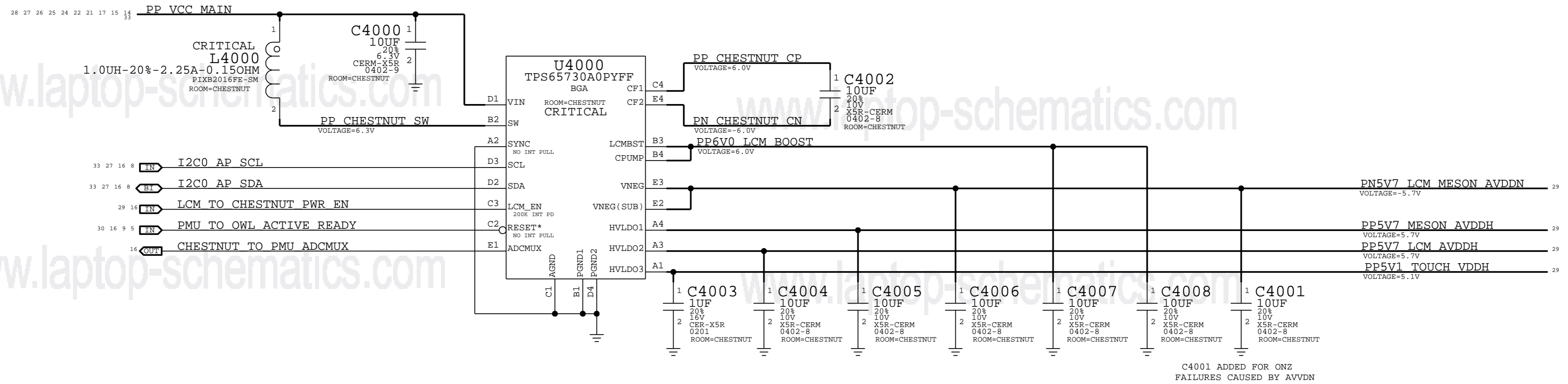
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Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
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		PAGE	38 OF 49
		SHEET	26 OF 59

DISPLAY & TOUCH - POWER SUPPLIES

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CHESTNUT DISPLAY PMU

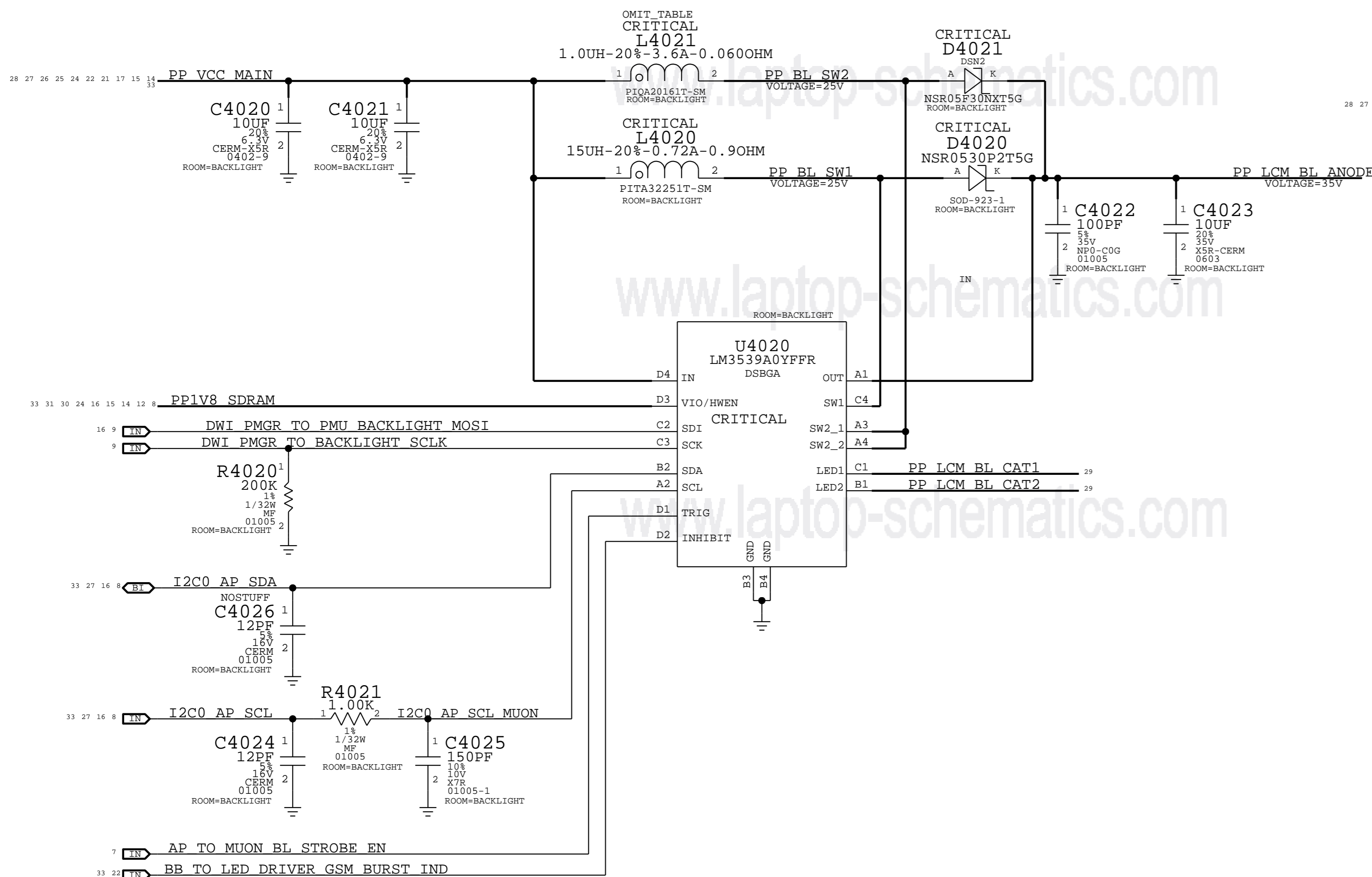
APN: 338S1172



C4001 ADDED FOR ONZ FAILURES CAUSED BY AVVDN

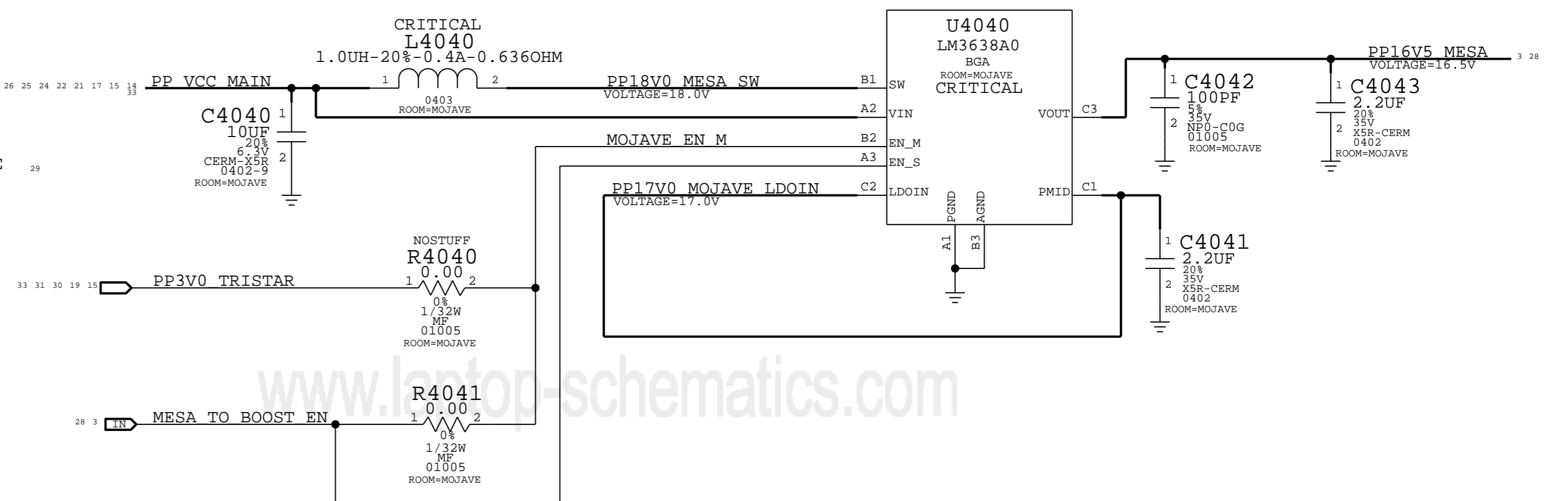
LED BACKLIGHT DRIVER

APN: 353S00407



MOJAVE MESA BOOST

APN: 353S00671

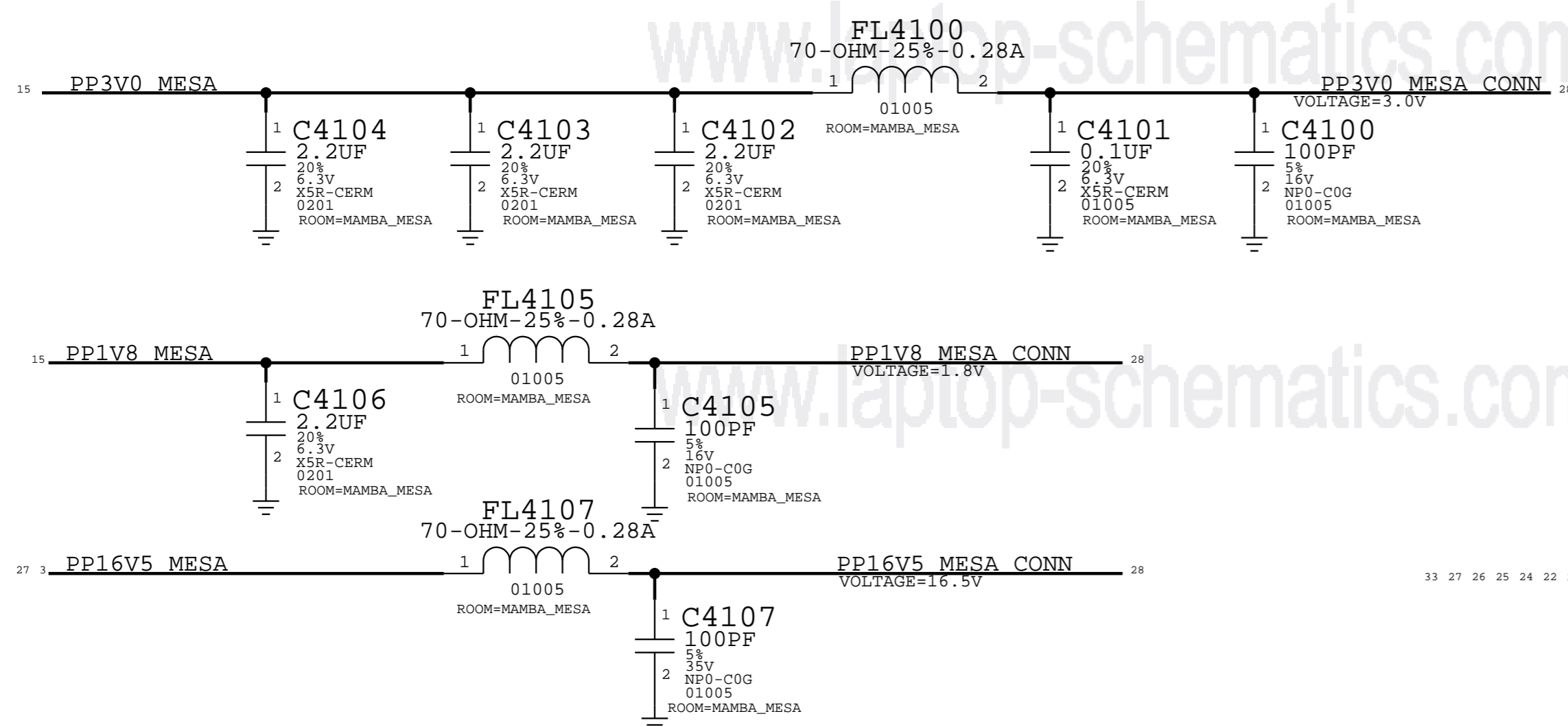


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		SHEET	27 OF 59

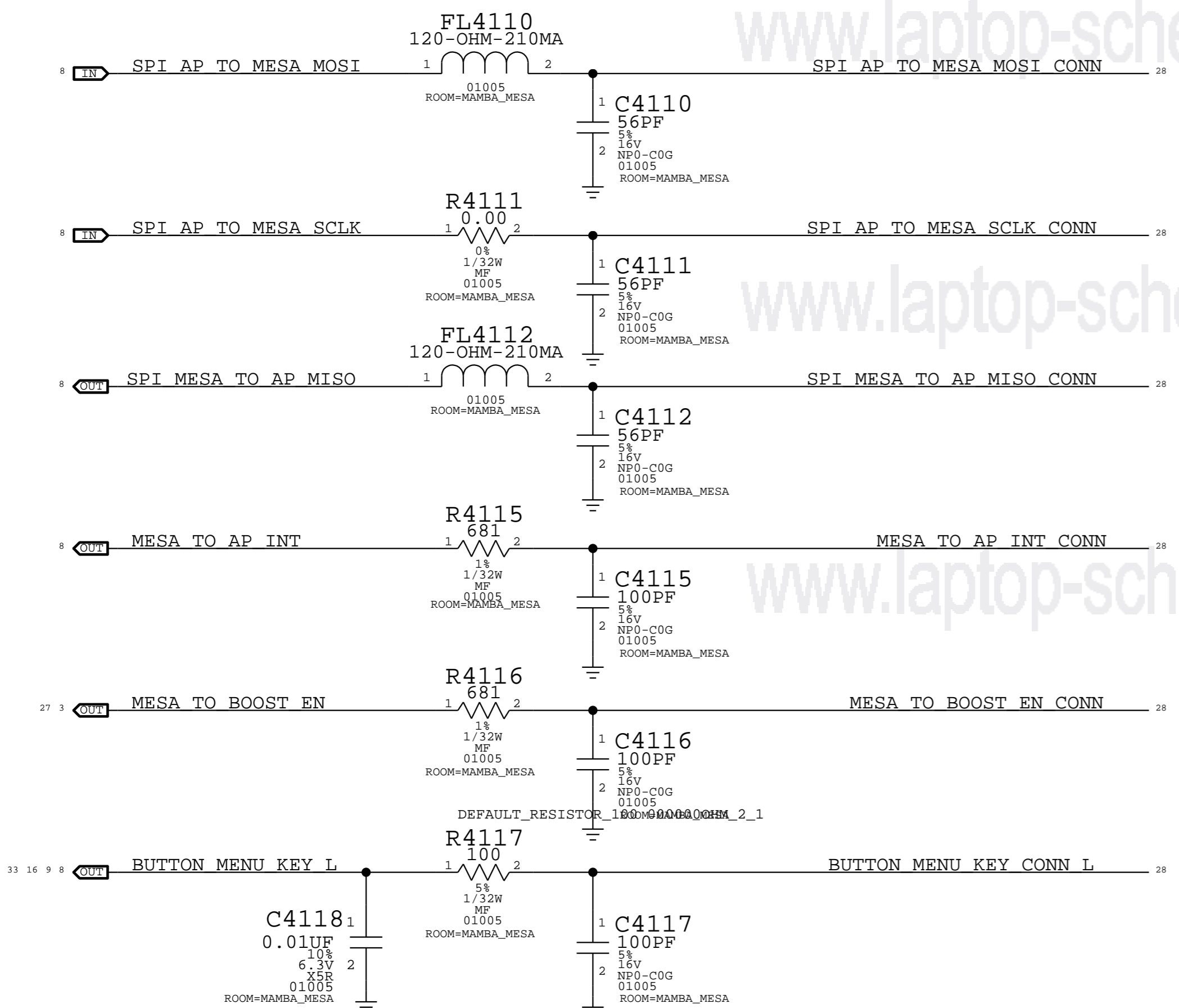
MAMBA & MESA (M&M) FLEX

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MESA POWER

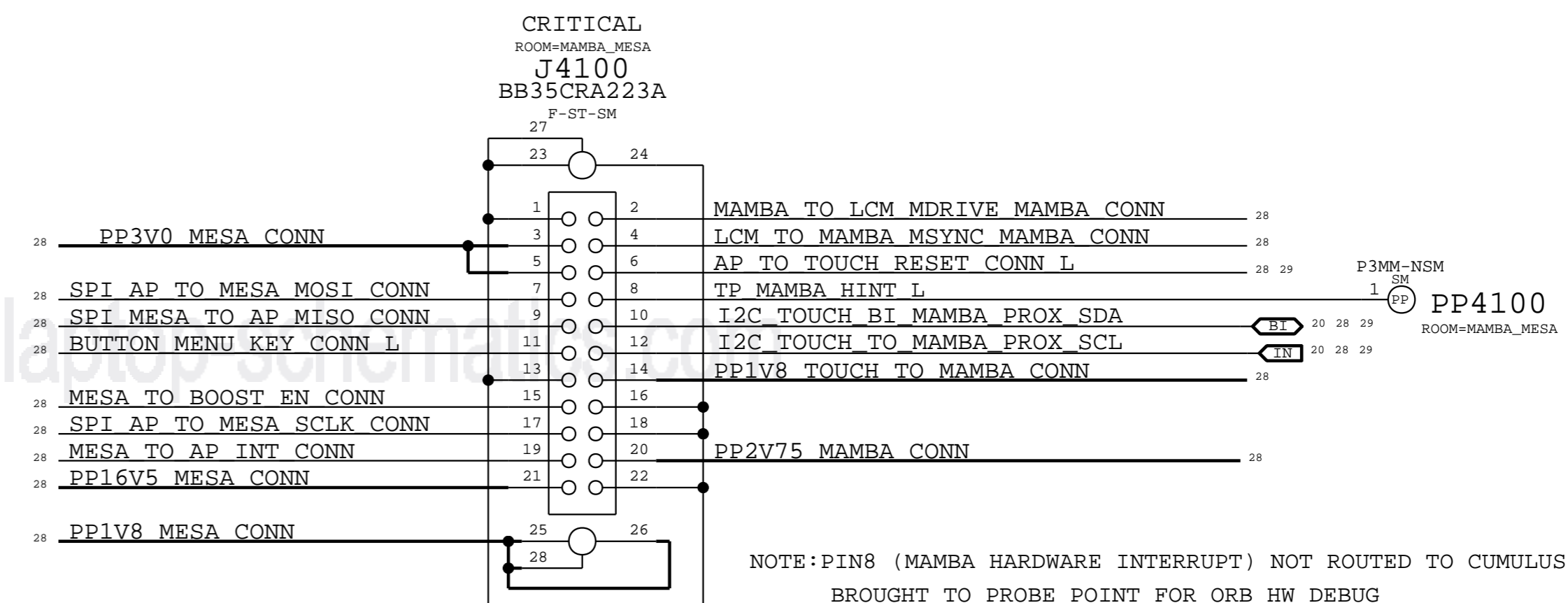


MESA DIGITAL I/O

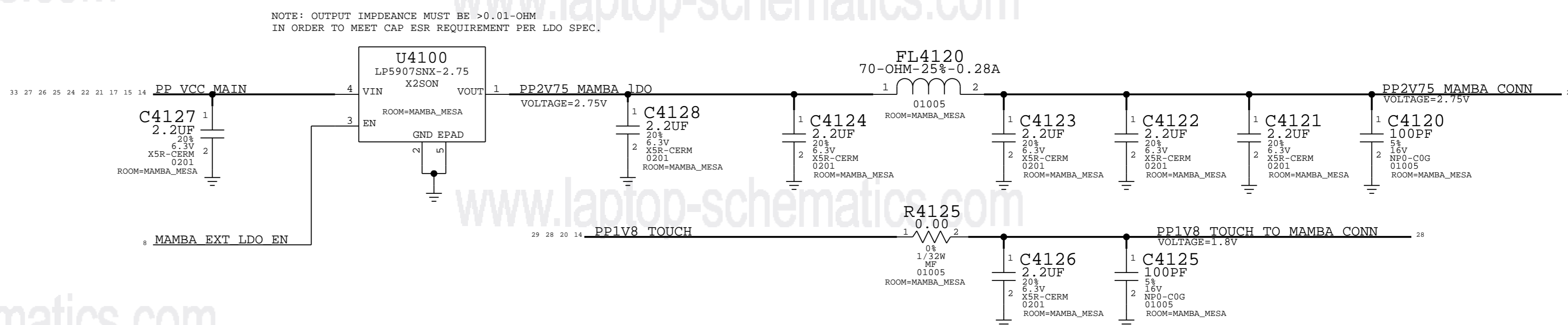


MAMBA & MESA CONNECTOR

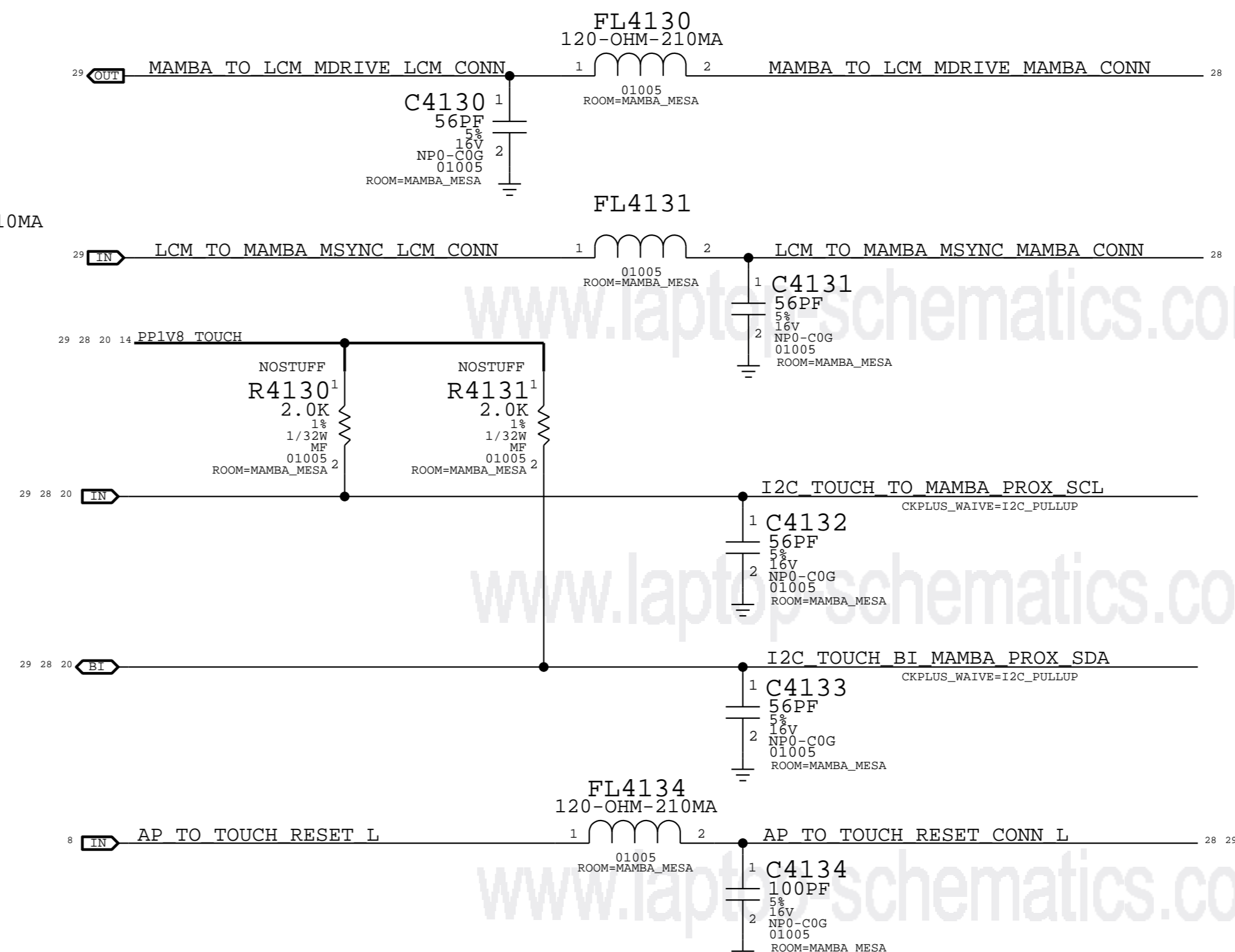
THIS ONE ON MLB ---> 516S00056 (RCPT)
516S00057 (PLUG)



MAMBA POWER



MAMBA DIGITAL I/O



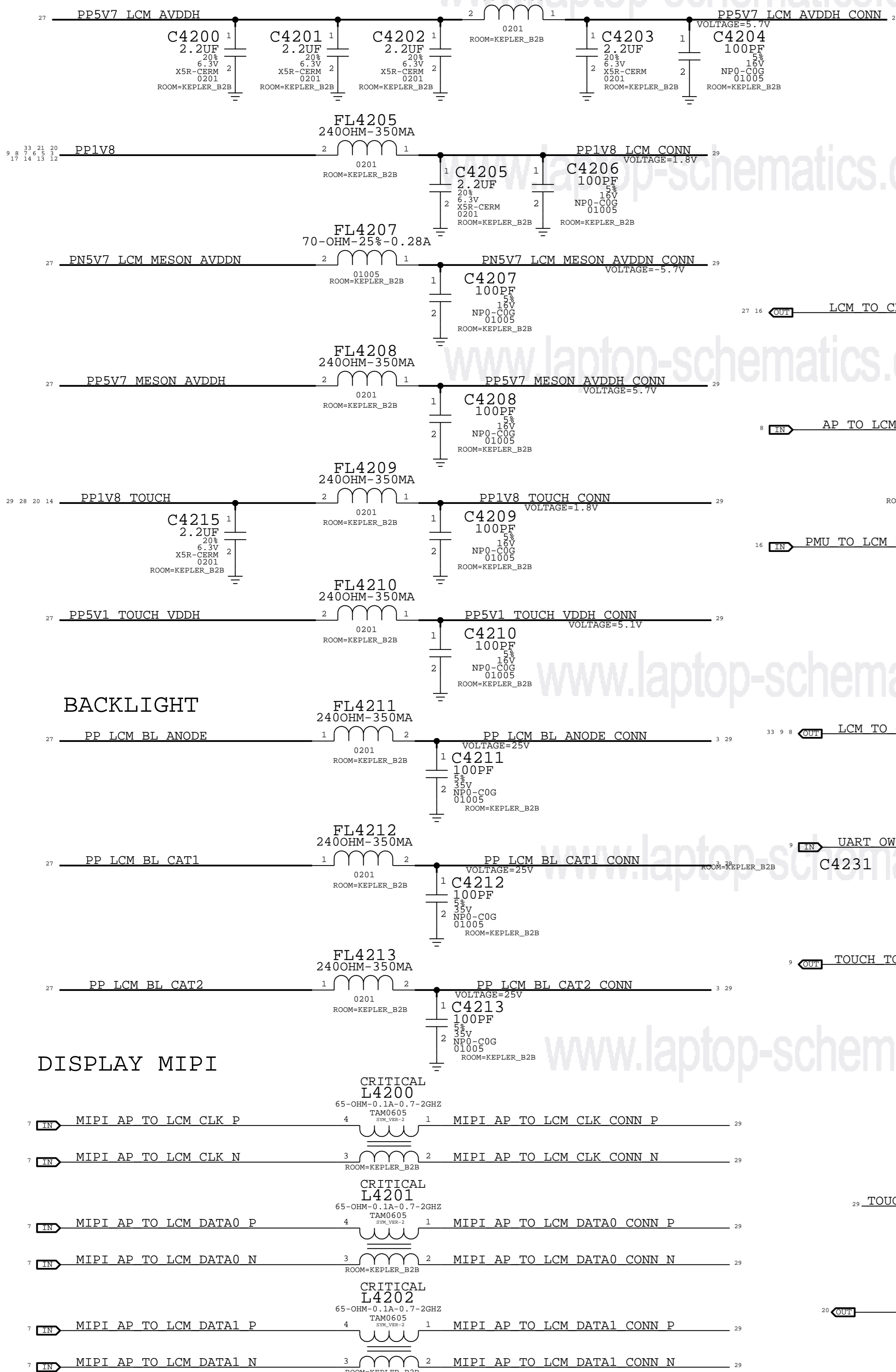
NOTE: TOUCH I2C PULL-UPS TO PP1V8_TOUCH INSIDE KEPLER ADDING R4130, R4131 AS OPTION FOR TWEAKING VALUES.

SYNC_MASTER=N/A	SYNC_DATE=N/A
PAGE TITLE TOUCH:ORB & MESA B2B	
Apple Inc.	DRAWING NUMBER 051-1902
	REVISION A.0.0
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	SHEET 28 OF 59

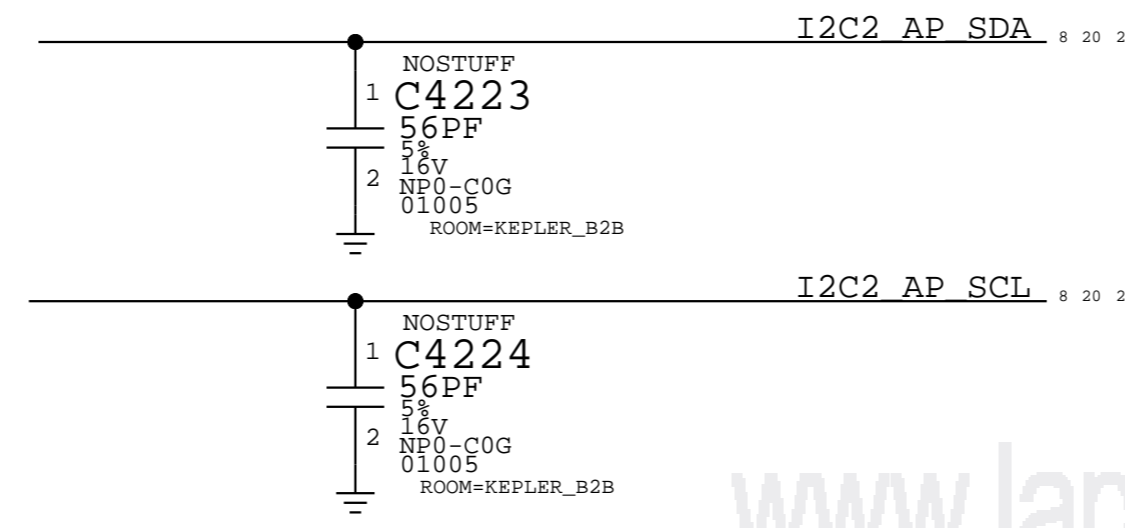
DISPLAY & TOUCH FLEX

DISPLAY CONNECTOR

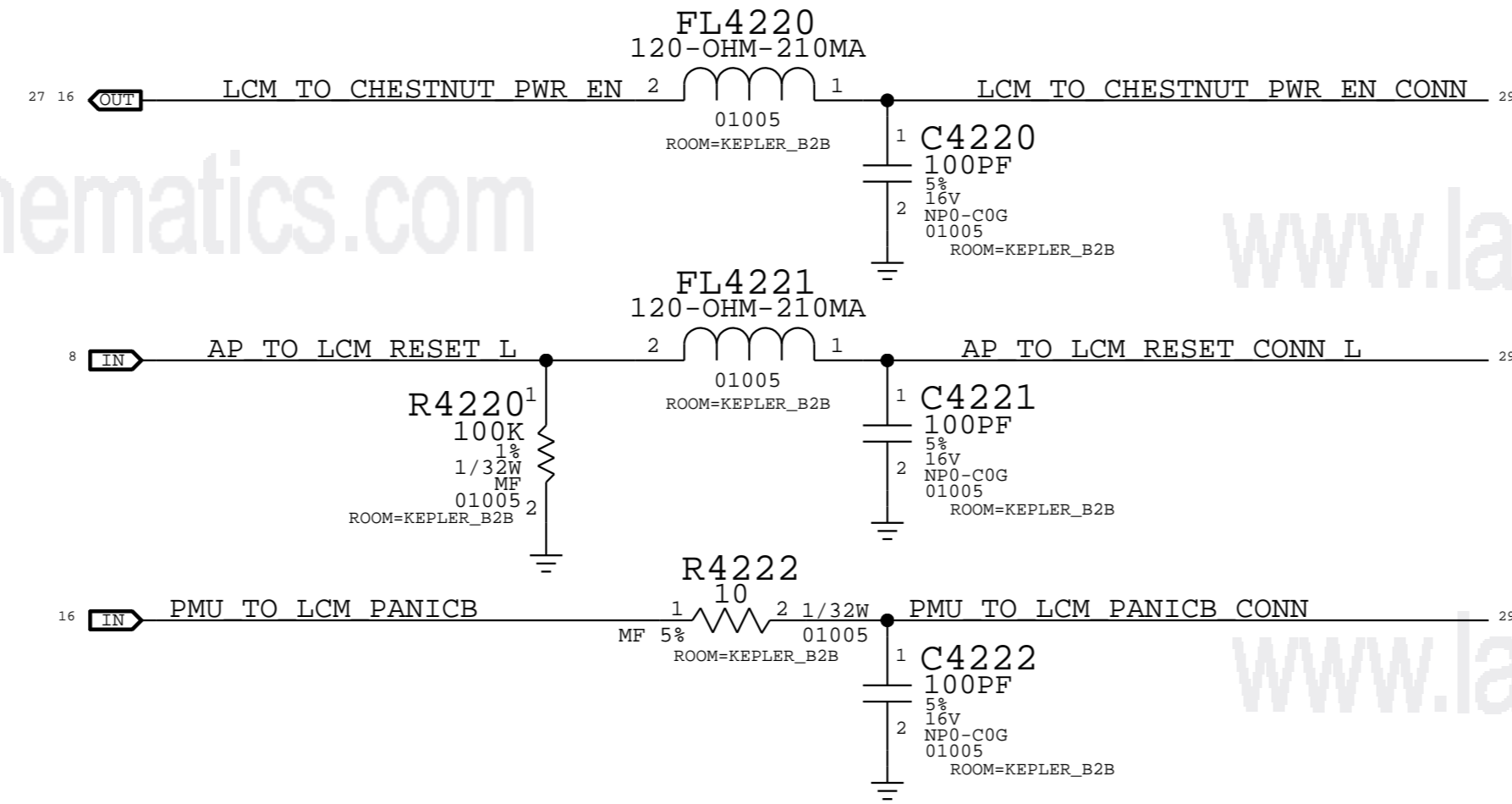
DISPLAY POWER



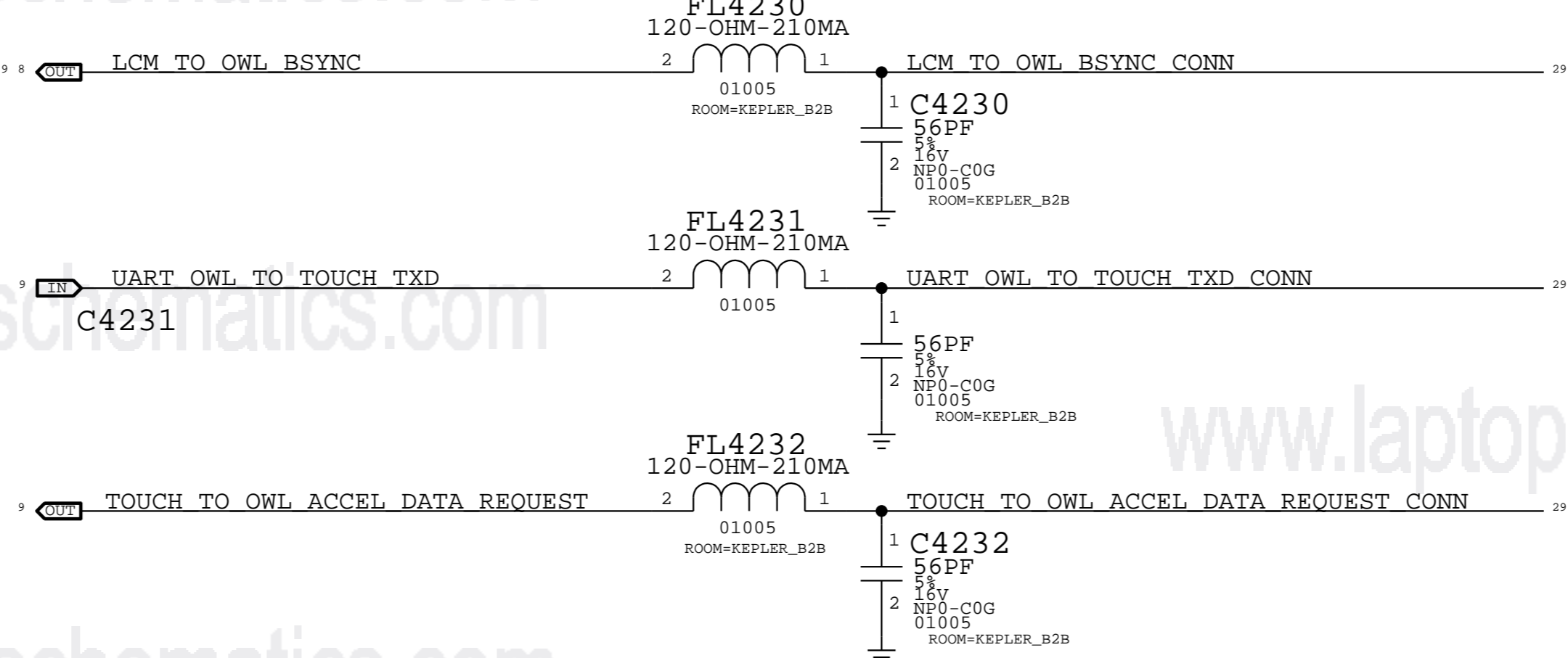
DISPLAY EEPROM I2C



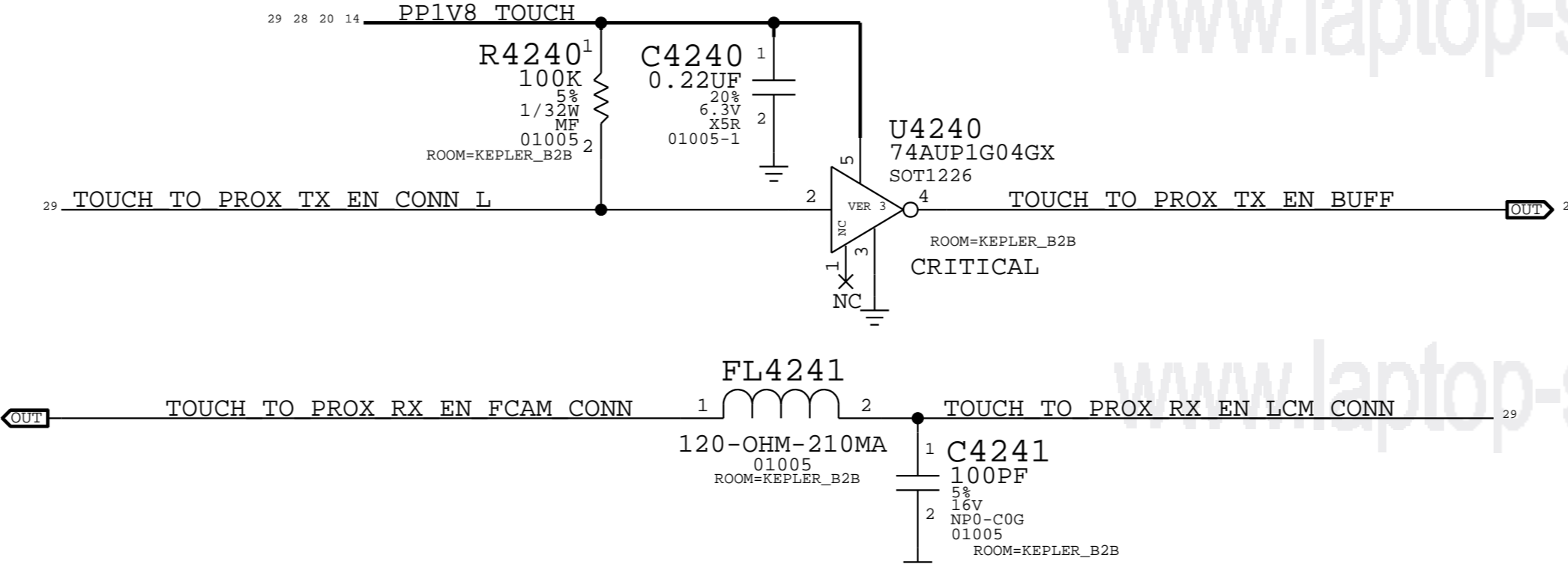
DISPLAY CONTROL SIGNALS



OWL TO TOUCH INTERFACE

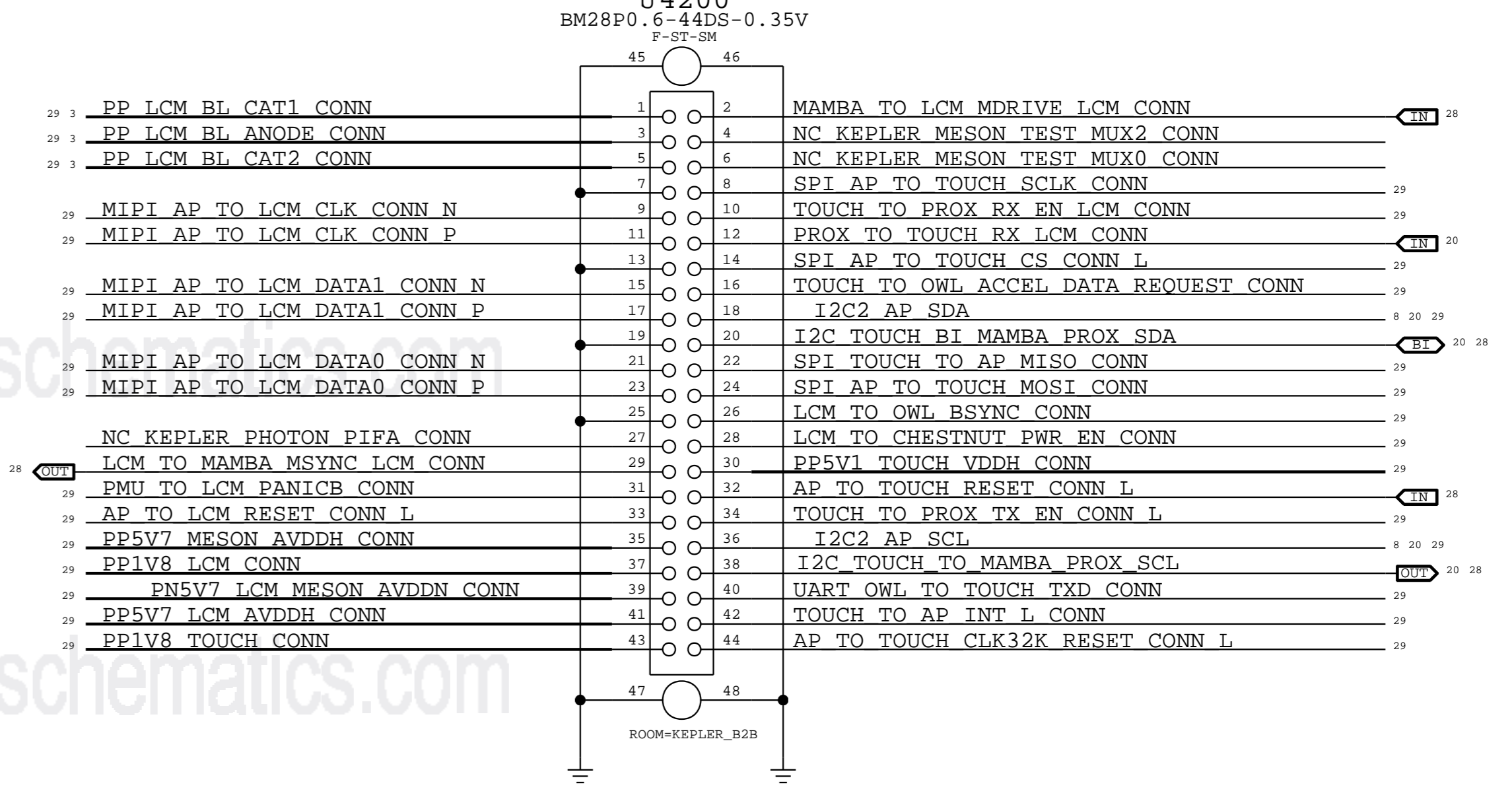


PROX TO TOUCH INTERFACE

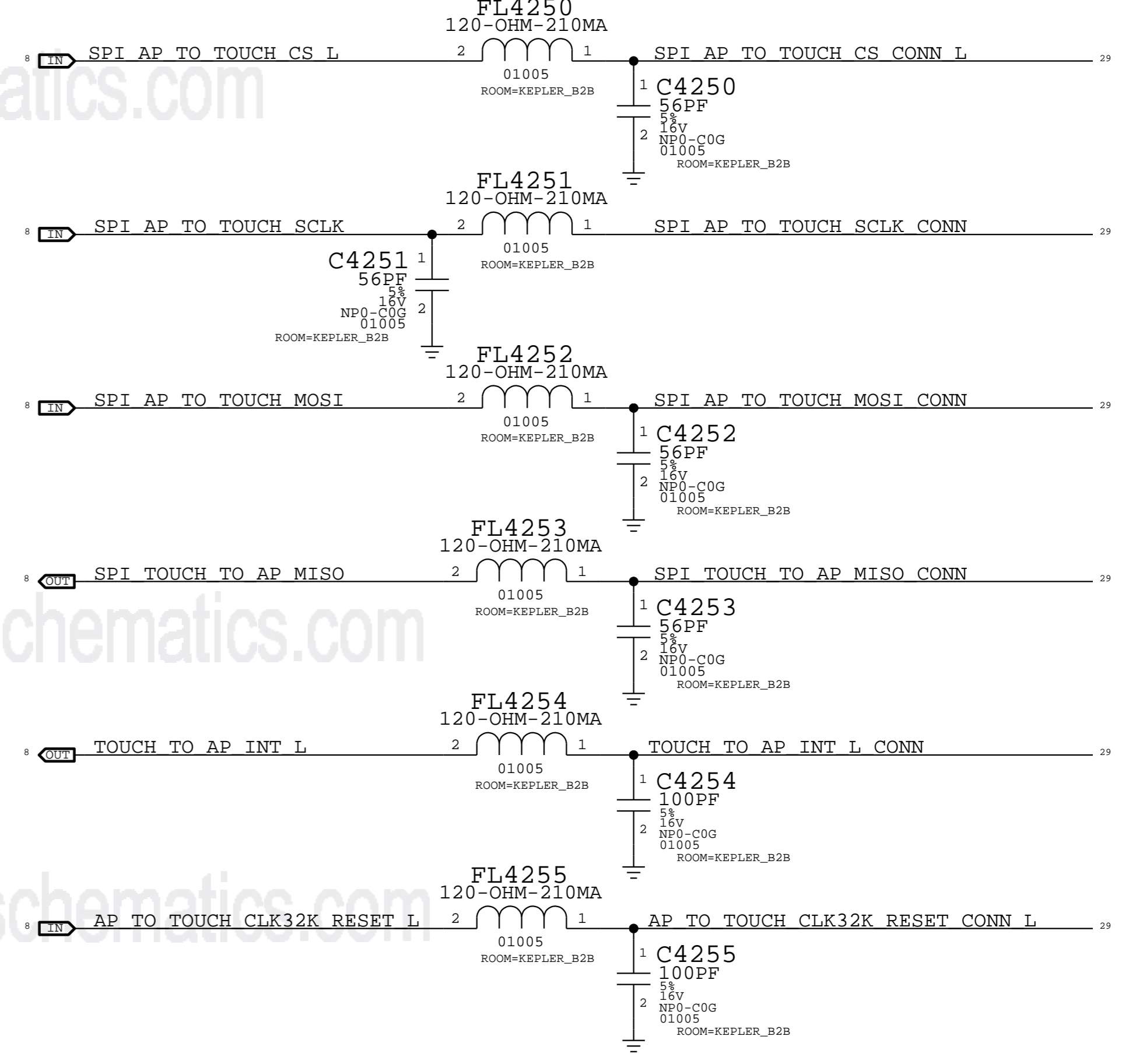


THIS ONE ON MLB ---> 516S00038 (RCPT)
516S00037 (PLUG)

CRITICAL



AP TO TOUCH INTERFACE



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PAGE TITLE			
DISPLAY:KEPLER B2B			
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		REVISION	A.0.0
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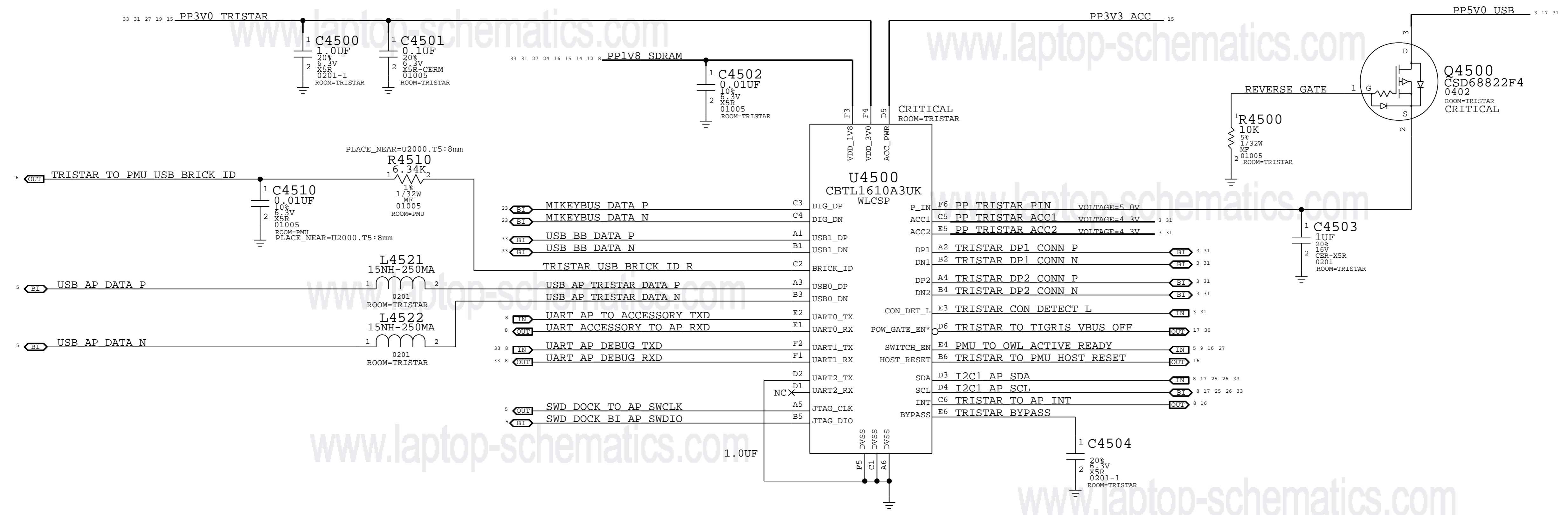
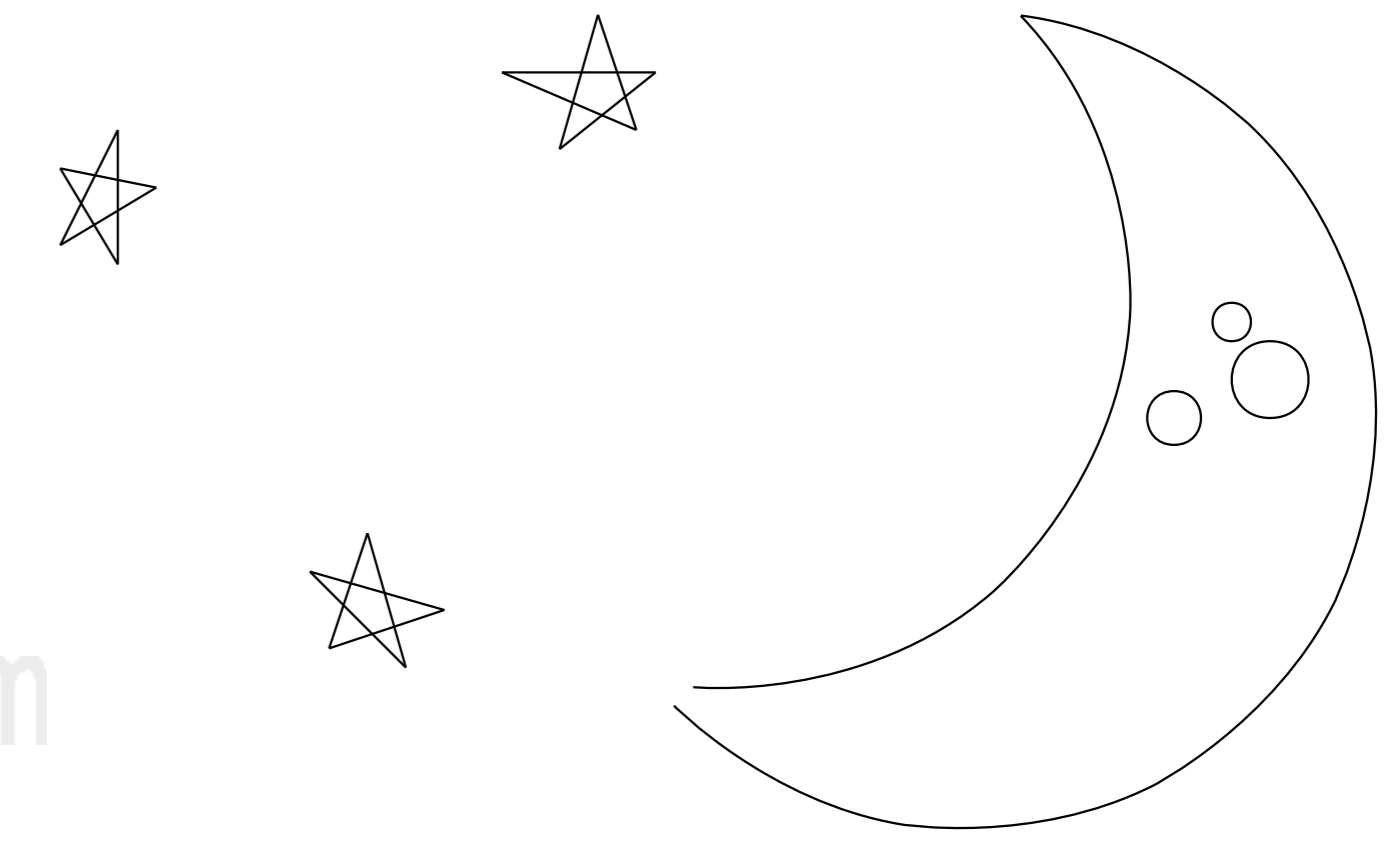
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TRISTAR 2 (A3)

APN: 343S0695

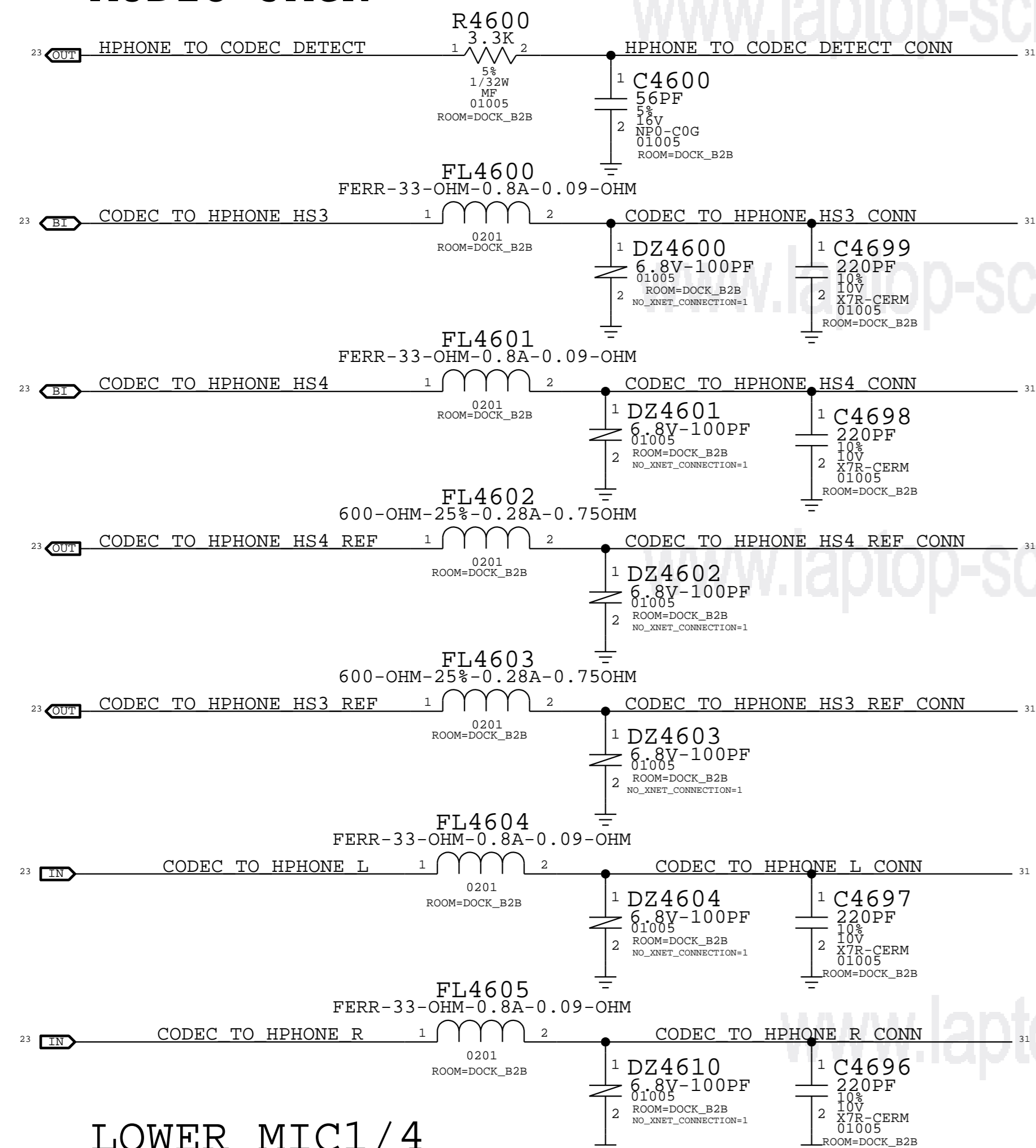


ROOM=TRISTAR
P3MM-NSM
33 17 TRISTAR TO TIGRIS VBUS OFF 1 PP4500

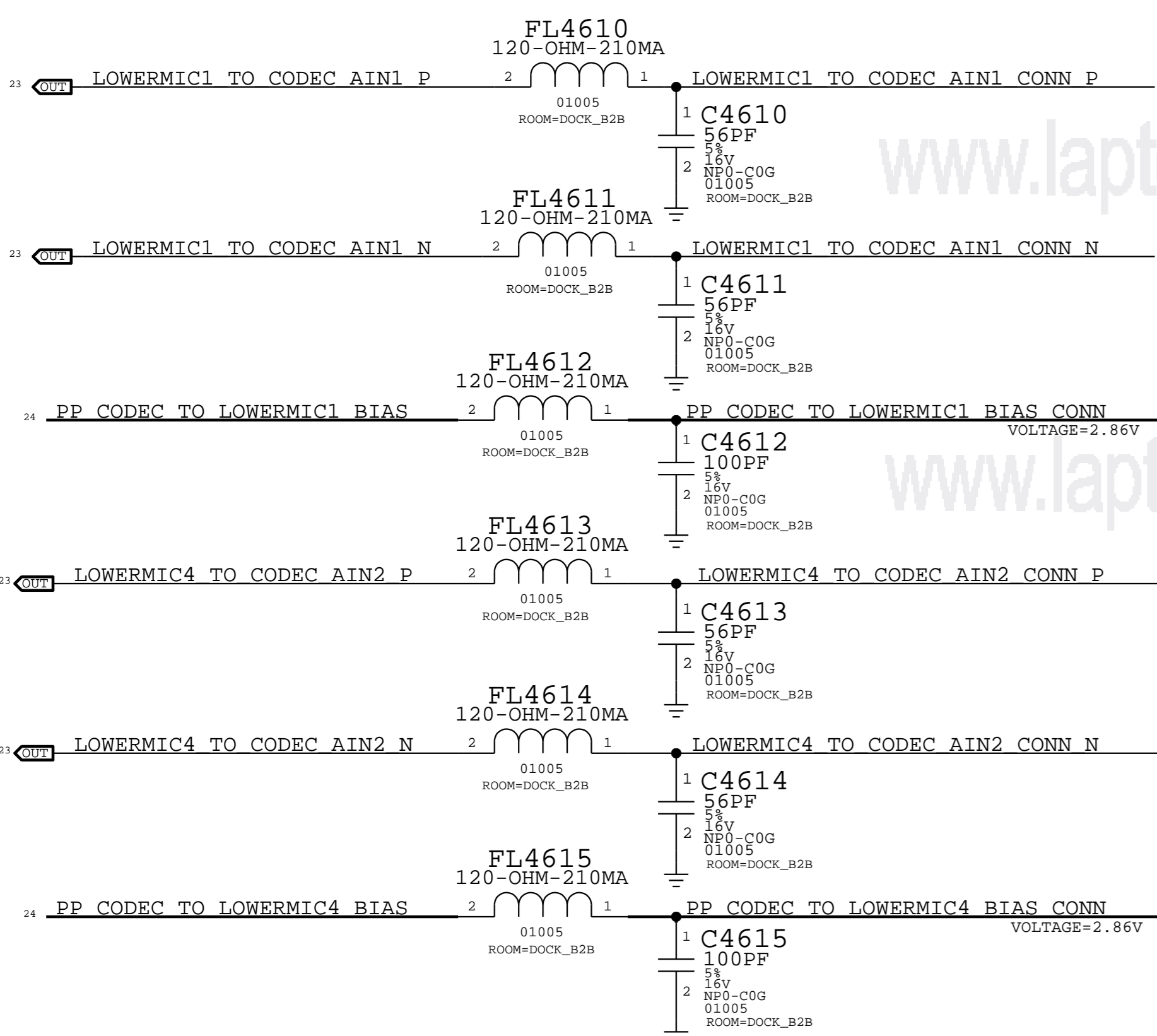
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DOCK FLEX CONNECTOR

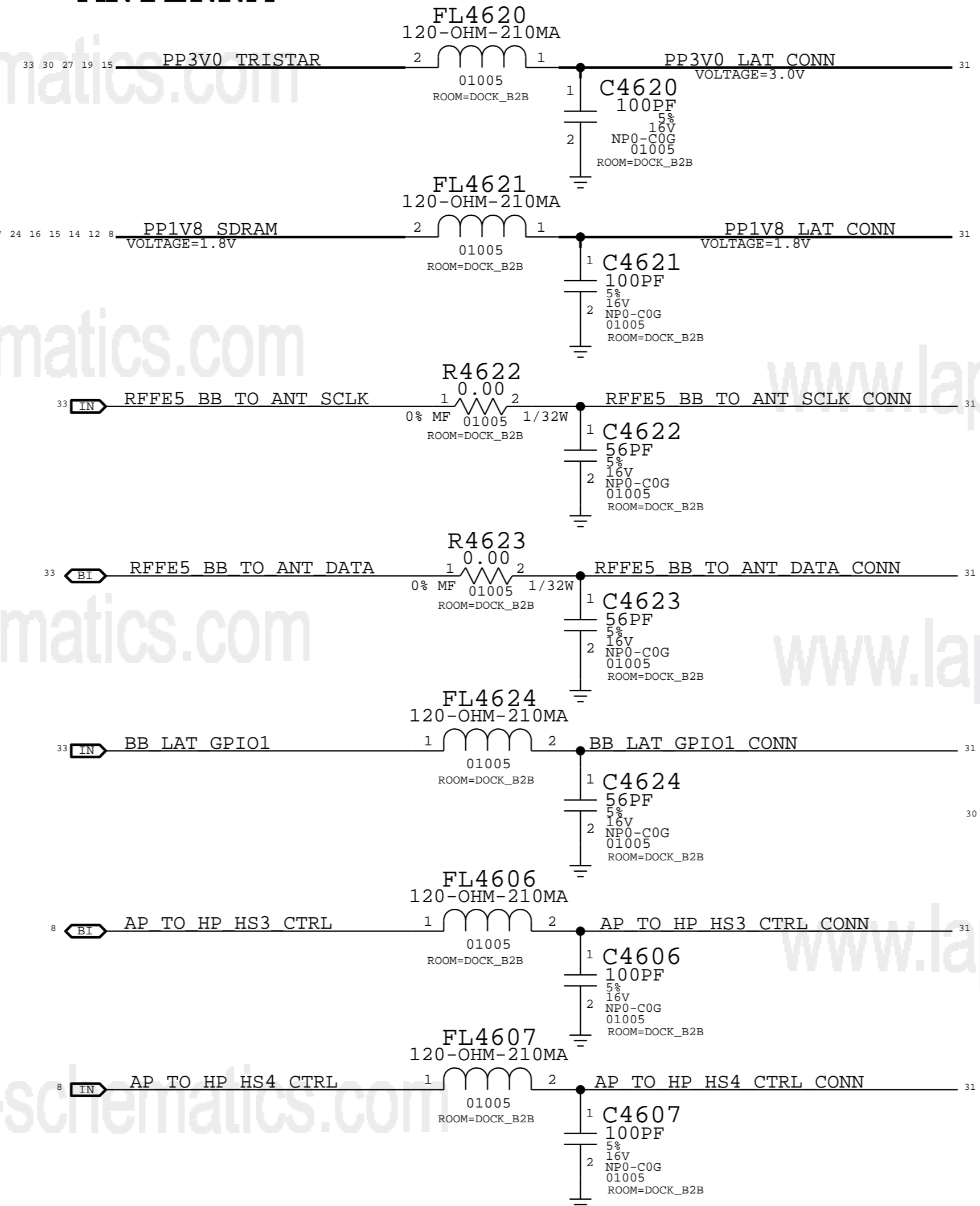
AUDIO JACK



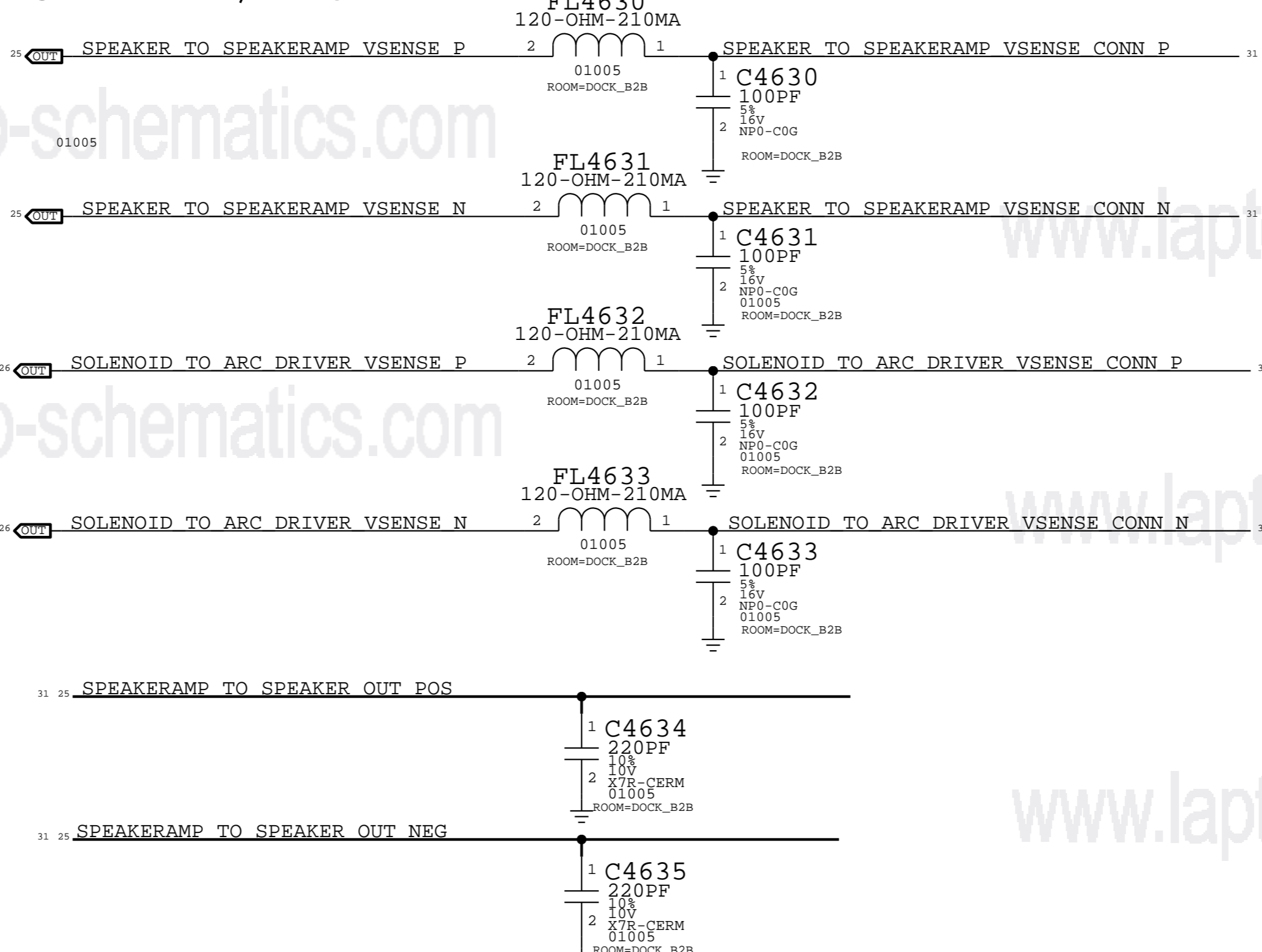
LOWER MIC1/4



ANTENNA

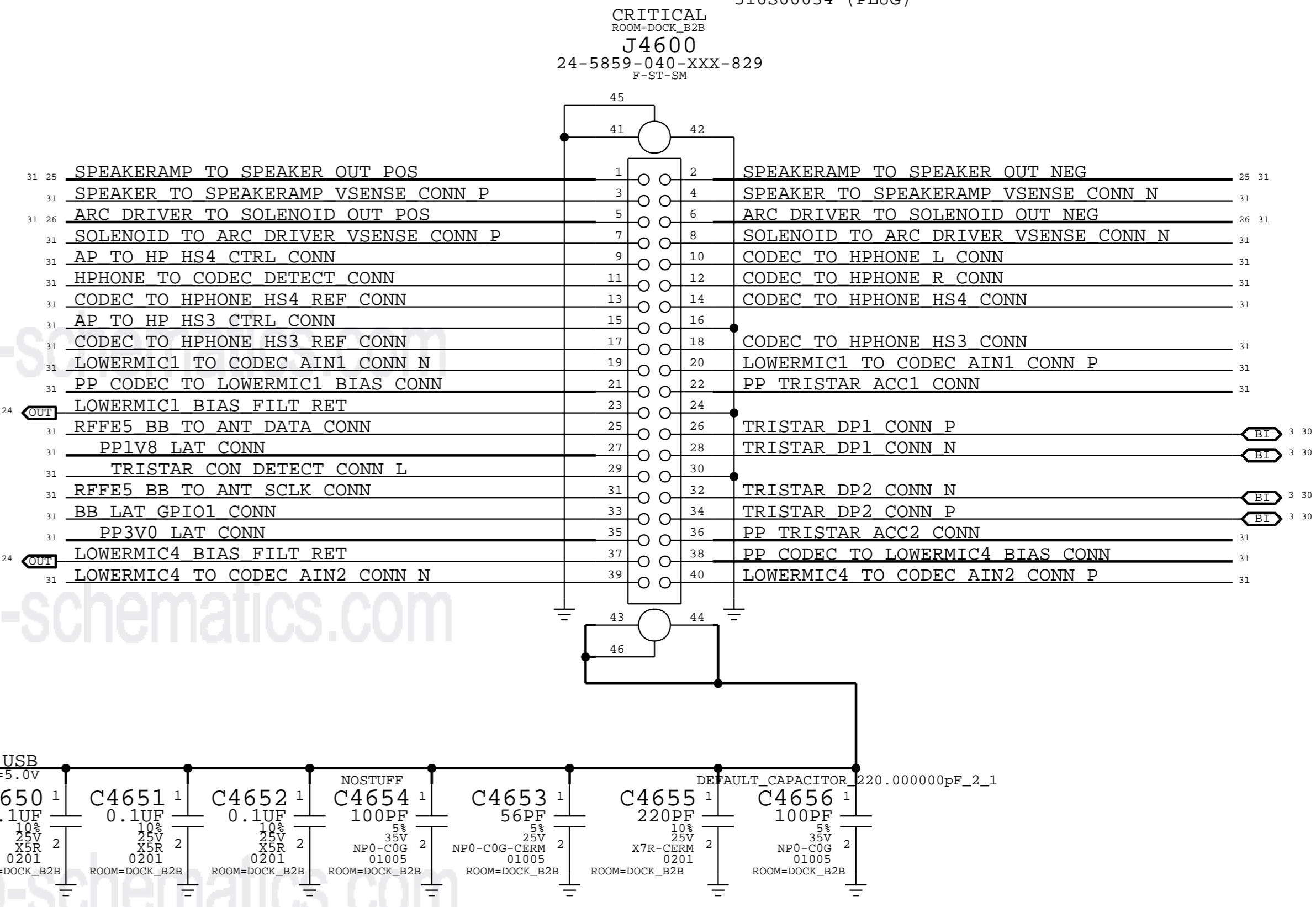


SPEAKER/ARC

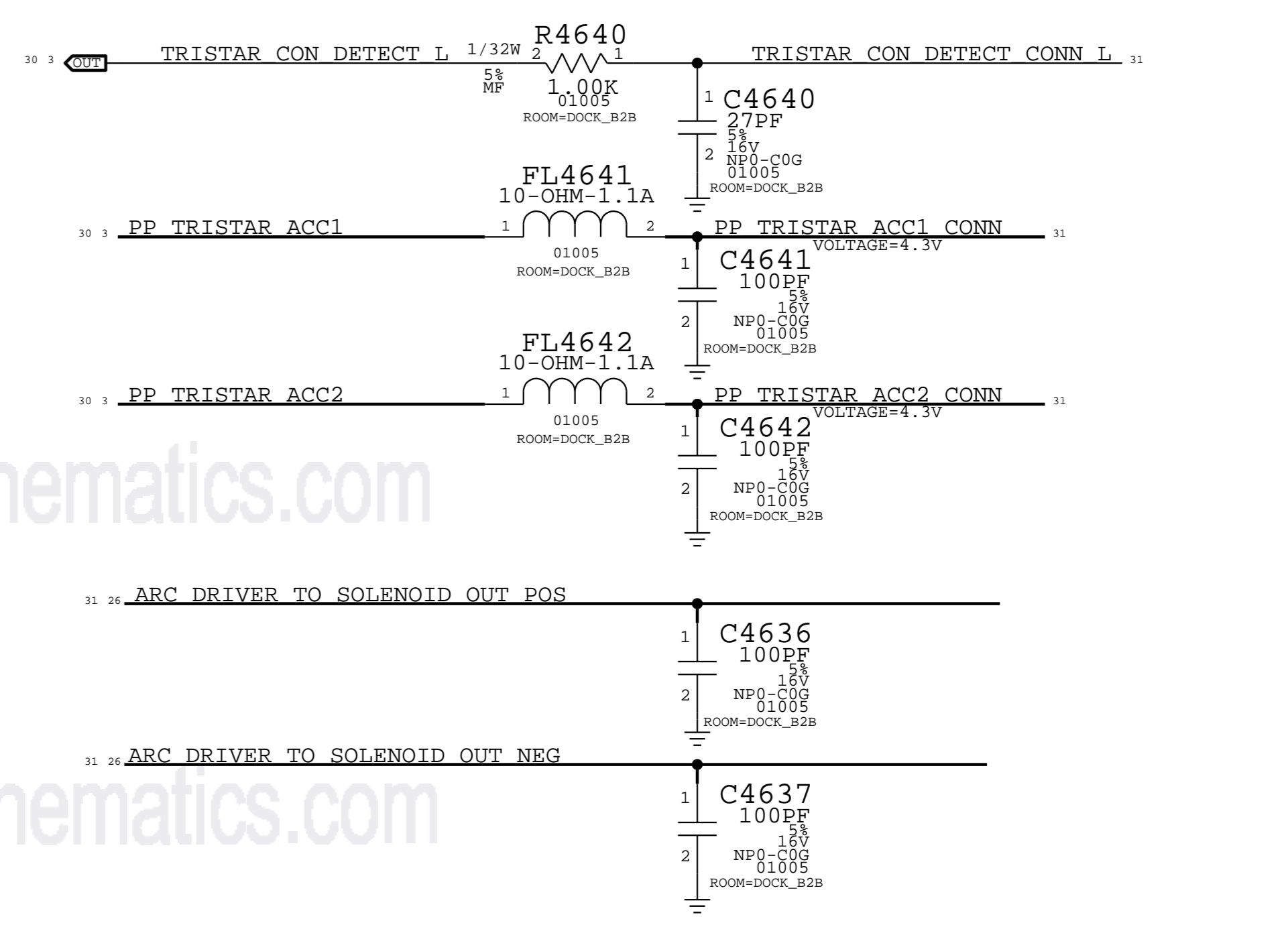


DOCK FLEX CONNECTOR

THIS ONE ON MLB ---> 516S00033 (RCPT)
516S00034 (PLUG)



TRISTAR



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
I/O: DOCK FLEX B2B			
Apple Inc.		DRAWING NUMBER	SIZE
		051-1902	D
		REVISION	
		A.0.0	
		BRANCH	
		PAGE	46 OF 49
		SHEET	31 OF 59
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BUTTON FLEX

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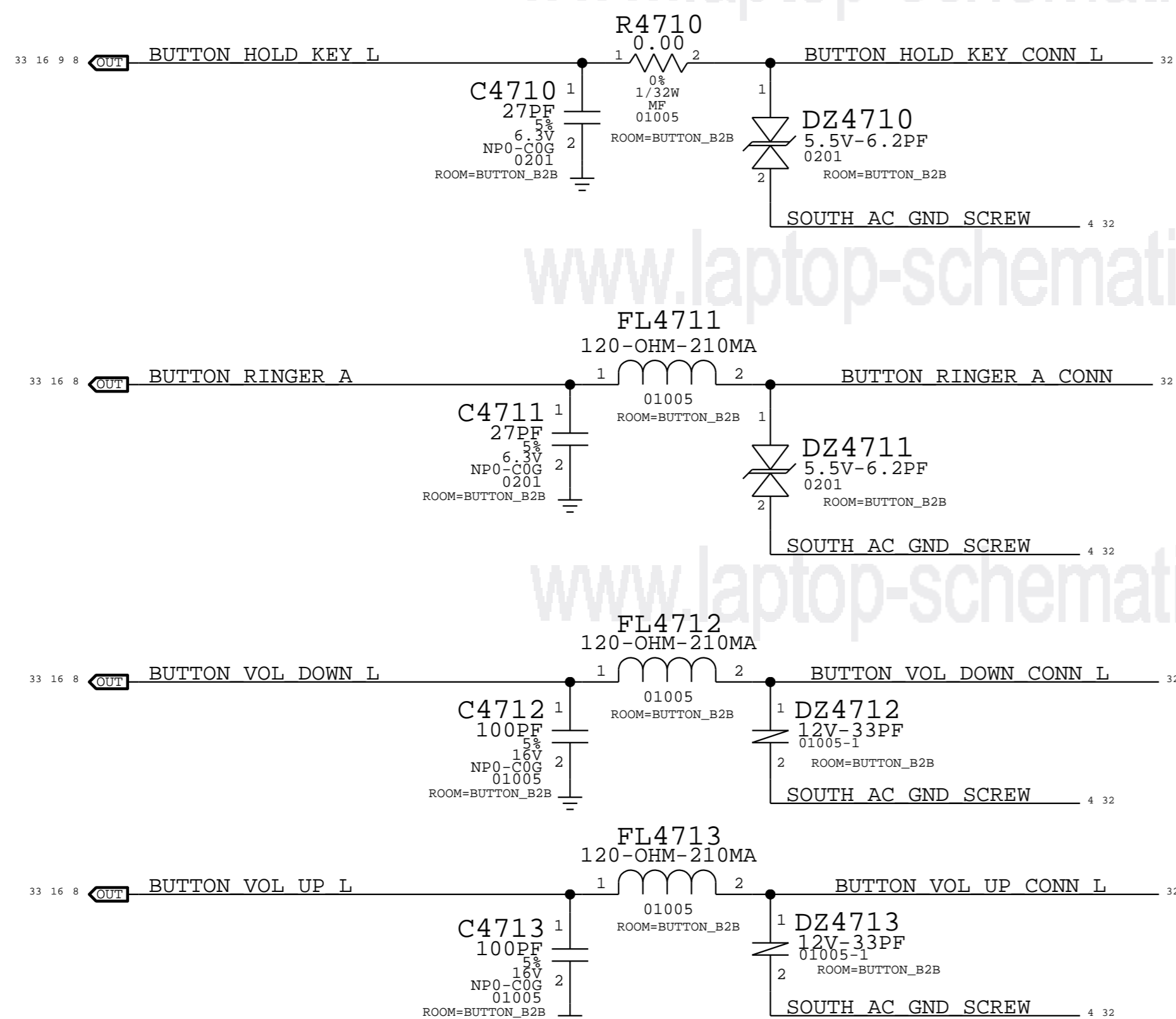
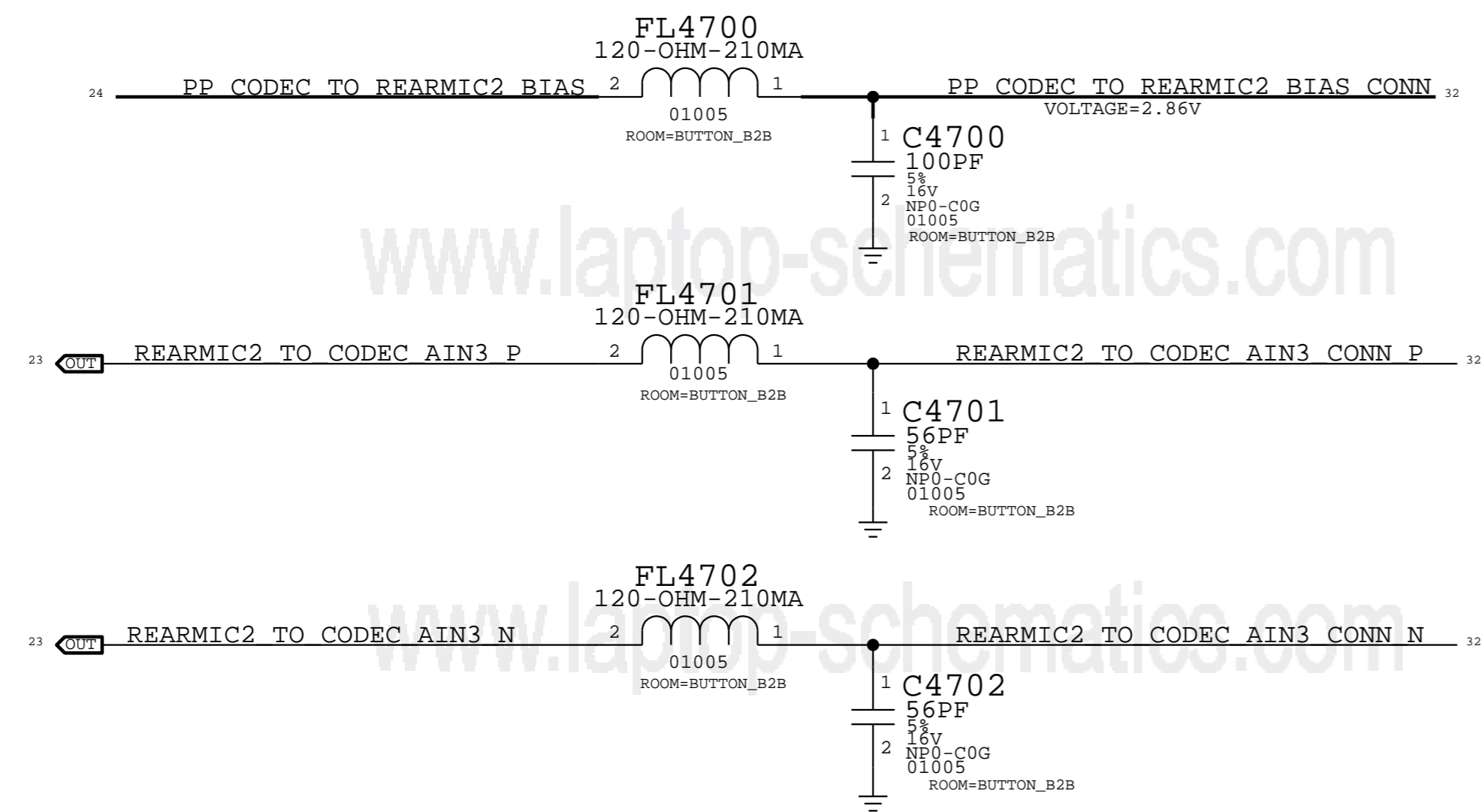
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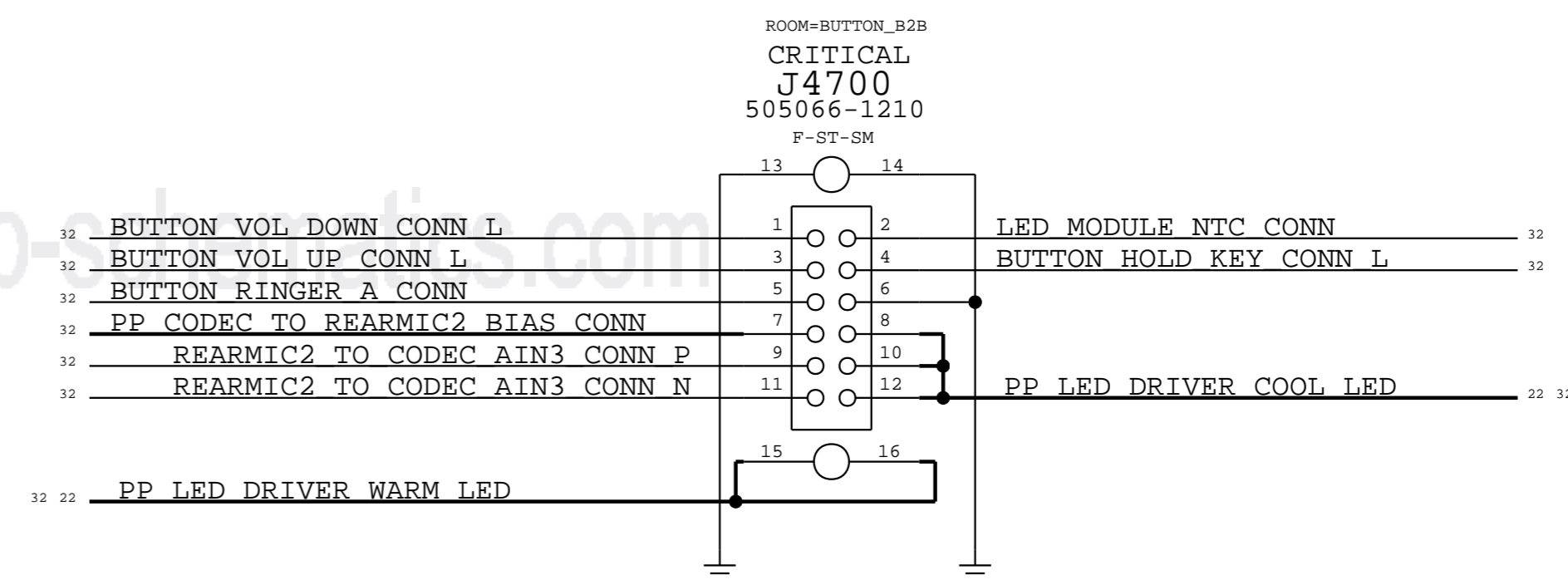
MIC2
ANC REF MIC



BUTTONS:
HOLD
RINGER
VOL UP/DOWN

BUTTON FLEX CONNECTOR

THIS ONE ON MLB ---> 516S00058 (RCPT)
516S00059 (PLUG)



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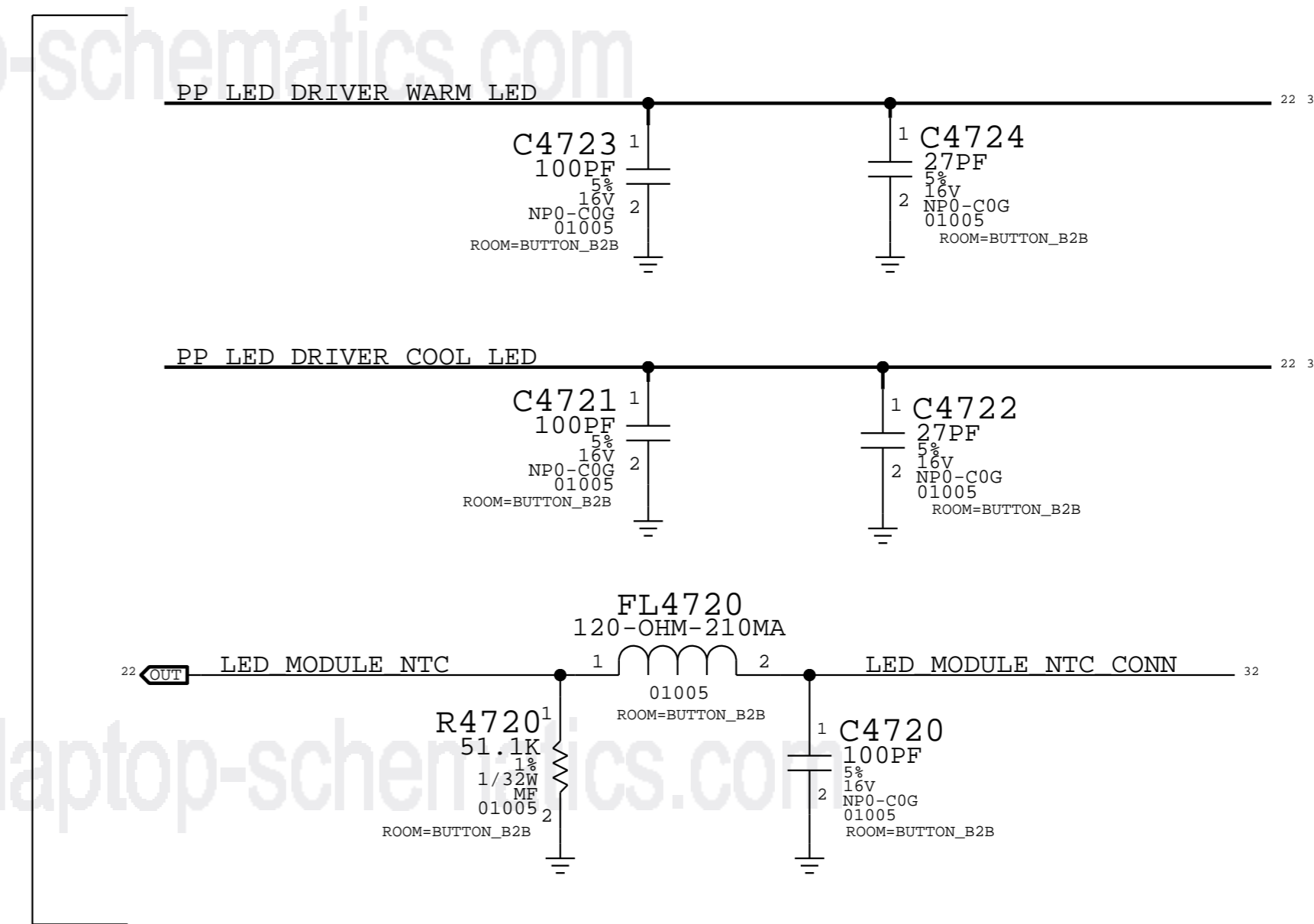
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STROBE:
WARM LED
COOL LED
MODULE NTC



SYNC_MASTER=N/A		SYNC_DATE=N/A	
PAGE TITLE			
I/O:BUTTON FLEX B2B			
	DRAWING NUMBER	051-1902	SIZE
	REVISION	A.0.0	D
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		SHEET	32 OF 59

BASEBAND, WLAN, BT & STOCKHOLM

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1456

MIS-NAMED NET. GPS_TIME_MARK ACTUALLY GOES FROM AP TO BB.

28	27	26	25	24	22	21	17	15	14	PP_VCC_MAIN						
31	30	27	19	15	PP3V0_TRISTAR											
31	30	27	24	16	15	14	12	8	PP1V8_SDRAM							
6	IN	PCIE AP TO BB TXD P														
6	IN	PCIE AP TO BB TXD N														
6	OUT	PCIE BB TO AP RXD P														
6	OUT	PCIE BB TO AP RXD N														
6	IN	PCIE AP TO BB REFCLK P														
6	IN	PCIE AP TO BB REFCLK N														
6	IN	PCIE AP TO BB RESET L														
6	BT	PCIE BB BI AP CLKREQ L														
16	OUT	BB TO PMU PCIE HOST WAKE L														
8	IN	AP TO BB PCIE DEV WAKE														
8	IN	I2S AP TO BB LRCLK														
8	IN	I2S AP TO BB BCLK														
8	IN	I2S AP TO BB DOUT														
8	OUT	I2S BB TO AP DIN														
8	IN	AP TO BB RADIO ON L														
16	IN	PMU TO BB PMIC RESET L														
8	IN	AP TO BB RESET L														
8	OUT	BB TO AP RESET DETECT L														
27	22	OUT	BB TO LED DRIVER GSM BURST IND													
8	BT	AP TO BB MESA UP L														
8	IN	BB TO AP GPS TIME MARK														
8	IN	AP TO BB COREDUMP TRIG														
8	BT	BB IPC GPIO														
29	9	IN	LCM TO OWL BSYNC													
9	IN	UART OWL TO BB TXD														
9	OUT	UART BB TO OWL RXD														
30	BT	USB BB DATA P														
30	BT	USB BB DATA N														
16	IN	PMU TO BB USB VBUS DETECT														
13	9	IN	SWD AP PERIPHERAL SWCLK													
9	BT	SWD AP BI BB SWDIO														
31	OUT	RFFE5 BB TO ANT SCLK														
31	BT	RFFE5 BB TO ANT DATA														
31	OUT	BB LAT GPIO1														
16	OUT	BB TO PMU AMUX LD011 SIM1														
16	OUT	BB TO PMU AMUX SMPS1														
16	OUT	BB TO PMU AMUX SMPS3														
16	OUT	BB TO PMU AMUX SMPS4														
4	OUT	50 AP UAT FEED														
4	OUT	50 AP WIFI 5G CONN ANT														
4	OUT	AP TO STOCKHOLM ANT														
21	20	17	14	13	12	9	8	7	6	5	3	2	1	PP1V8		
8	IN	DFU STATUS														
8	OUT	FORCE_DFU														
8	IN	NC_PMU_AMUX_AY														
8	IN	NC_PMU_AMUX_BY														
16	9	5	3	PMU TO SYSTEM COLD RESET L												
27	16	8	I2C0 AP_SCL													
27	16	8	I2C0 AP_SDA													
30	26	25	17	8	I2C1 AP_SCL											
30	26	25	17	8	I2C1 AP_SDA											
32	16	9	8	BUTTON_HOLD_KEY_L												
28	16	9	8	BUTTON_MENU_KEY_L												
32	16	8	BUTTON_RINGER_A													
32	16	8	BUTTON_VOL_DOWN_L													
32	16	8	BUTTON_VOL_UP_L													
32	16	8	NC_PMU_GPIO20													
32	16	8	NC_PMU_GPIO21													
30	8	IN	UART AP DEBUG TXD													
30	8	OUT	UART AP DEBUG RXD													
NC AP RESERVED2																

RADIO_MLB_MIMO	
SHARED POWER	
PP_VCC_MAIN	PP3V0_TRISTAR SUBDESIGN_SUFFIX=RF
PP1V8_SDRAM	
BASEBAND	
WLAN	
PCIE0_AP_TO_BB_TX_P	PCIE0_BB_TO_BB_TX_P
PCIE0_BB_TO_BB_TX_N	PCIE0_BB_TO_BB_TX_N
PCIE0_BB_TO_BB_TX_P	PCIE0_BB_TO_BB_TX_P
PCIE0_BB_TO_BB_TX_N	PCIE0_BB_TO_BB_TX_N
PCIE0_BB_TO_BB_REFCLK_P	PCIE0_BB_TO_BB_REFCLK_P
PCIE0_BB_TO_BB_REFCLK_N	PCIE0_BB_TO_BB_REFCLK_N
PCIE0_BB_TO_BB_PERST_L	PCIE0_BB_TO_BB_PERST_L
PCIE0_BB_TO_BB_CLKREQ_L	PCIE0_BB_TO_BB_CLKREQ_L
PCIE0_BB_TO_PMU_HOST_WAKE_L	PCIE0_BB_TO_PMU_HOST_WAKE_L
PCIE0_BB_TO_BB_DEV_WAKE	PCIE0_BB_TO_BB_DEV_WAKE
I2S_AP_TO_BB_WS	I2S_AP_TO_BB_WS
I2S_AP_TO_BB_CLK	I2S_AP_TO_BB_CLK
I2S_AP_TO_BB_DOUT	I2S_AP_TO_BB_DOUT
I2S_BB_TO_BB_TX	I2S_BB_TO_BB_TX
I2S_BB_TO_BB_TX	I2S_BB_TO_BB_TX
AP_TO_BBPMU_RADIO_ON_L	AP_TO_BBPMU_RADIO_ON_L
PMU_TO_BBPMU_RESET_L	PMU_TO_BBPMU_RESET_L
AP_TO_BB_RST_L	AP_TO_BB_RST_L
BB_TO_BB_RESET_DET_L	BB_TO_BB_RESET_DET_L
BB_TO_BB_GSM_TXBURST_IND	BB_TO_BB_GSM_TXBURST_IND
AP_TO_BB_MESA_ON_L	AP_TO_BB_MESA_ON_L
BB_TO_BB_GPS_TIME_MARK	BB_TO_BB_GPS_TIME_MARK
AP_TO_BB_COREDUMP_TRIG	AP_TO_BB_COREDUMP_TRIG
AP_TO_BB_IPC_GPIO	AP_TO_BB_IPC_GPIO
TOUCH_TO_BBPMU_FORCE_PWM	TOUCH_TO_BBPMU_FORCE_PWM
UART0_OWL_TO_BB_TX	UART0_OWL_TO_BB_TX
UART0_BB_TO_OWL_RXD	UART0_BB_TO_OWL_RXD
USB_BB_P	USB_BB_P
USB_BB_N	USB_BB_N
USB_BB_VBUS_DETECT	USB_BB_VBUS_DETECT
SWD_CLK_BB_JTAG_TCK	SWD_CLK_BB_JTAG_TCK
SWD_IO_BB_JTAG_TMS	SWD_IO_BB_JTAG_TMS
75_RFFE5_SCLK_BB	75_RFFE5_SCLK_BB
75_RFFE5_SDATA_BB	75_RFFE5_SDATA_BB
RFFE_BUFFER_LAT_GPIO1	RFFE_BUFFER_LAT_GPIO1
BB_TO_PMU_AMUX_LD011_SIM1	BB_TO_PMU_AMUX_LD011_SIM1
BB_TO_PMU_AMUX_SMPS1	BB_TO_PMU_AMUX_SMPS1
BB_TO_PMU_AMUX_SMPS3	BB_TO_PMU_AMUX_SMPS3
BB_TO_PMU_AMUX_SMPS4	BB_TO_PMU_AMUX_SMPS4
ANT	
50_UPPER_ANT_FEED	50_UPPER_ANT_FEED
50_WIFI_5G_CONN_ANT	50_WIFI_5G_CONN_ANT
STOCKHOLM_ANT	STOCKHOLM_ANT
AP DEBUG	
PP1V8	DFU_STATUS
FORCE_DFU	FORCE_DFU
PMU_AMUX_AY	PMU_AMUX_AY
PMU_AMUX_BY	PMU_AMUX_BY
PMU_TO_SYSTEM_COLD_RESET_L	PMU_TO_SYSTEM_COLD_RESET_L
I2C0_AP_SCL	I2C0_AP_SCL
I2C0_AP_SDA	I2C0_AP_SDA
I2C1_AP_SCL	I2C1_AP_SCL
I2C1_AP_SDA	I2C1_AP_SDA
BUTTON_HOLD_KEY_L	BUTTON_HOLD_KEY_L
BUTTON_MENU_KEY_L	BUTTON_MENU_KEY_L
BUTTON_RINGER_A	BUTTON_RINGER_A
BUTTON_VOL_DOWN_L	BUTTON_VOL_DOWN_L
BUTTON_VOL_UP_L	BUTTON_VOL_UP_L
PMU_GPIO20	PMU_GPIO20
PMU_GPIO21	PMU_GPIO21
AP_RESERVED0	AP_RESERVED0
AP_RESERVED1	AP_RESERVED1
AP_RESERVED2	AP_RESERVED2

WLAN	
PCIE0_BB_TO_BB_TX_P	PCIE0_BB_TO_BB_TX_P
PCIE0_BB_TO_BB_TX_N	PCIE0_BB_TO_BB_TX_N
PCIE0_BB_TO_BB_REFCLK_P	PCIE0_BB_TO_BB_REFCLK_P
PCIE0_BB_TO_BB_REFCLK_N	PCIE0_BB_TO_BB_REFCLK_N
PCIE0_BB_TO_BB_PERST_L	PCIE0_BB_TO_BB_PERST_L
PCIE0_BB_TO_BB_CLKREQ_L	PCIE0_BB_TO_BB_CLKREQ_L
PCIE0_BB_TO_PMU_HOST_WAKE_L	PCIE0_BB_TO_PMU_HOST_WAKE_L
PCIE0_BB_TO_BB_DEV_WAKE	PCIE0_BB_TO_BB_DEV_WAKE
PCIE0_BB_TO_BB_WS	PCIE0_BB_TO_BB_WS
PCIE0_BB_TO_BB_CLK	PCIE0_BB_TO_BB_CLK
PCIE0_BB_TO_BB_DOUT	PCIE0_BB_TO_BB_DOUT
PCIE0_BB_TO_BB_TX	PCIE0_BB_TO_BB_TX
PCIE0_BB_TO_BB_TX	PCIE0_BB_TO_BB_TX
AP_TO_BBPMU_RADIO_ON_L	AP_TO_BBPMU_RADIO_ON_L
PMU_TO_BBPMU_RESET_L	PMU_TO_BBPMU_RESET_L
AP_TO_BB_RST_L	AP_TO_BB_RST_L
BB_TO_BB_RESET_DET_L	BB_TO_BB_RESET_DET_L
BB_TO_BB_GSM_TXBURST_IND	BB_TO_BB_GSM_TXBURST_IND
AP_TO_BB_MESA_ON_L	AP_TO_BB_MESA_ON_L
BB_TO_BB_GPS_TIME_MARK	BB_TO_BB_GPS_TIME_MARK
AP_TO_BB_COREDUMP_TRIG	AP_TO_BB_COREDUMP_TRIG
AP_TO_BB_IPC_GPIO	AP_TO_BB_IPC_GPIO
TOUCH_TO_BBPMU_FORCE_PWM	TOUCH_TO_BBPMU_FORCE_PWM
UART0_OWL_TO_BB_TX	UART0_OWL_TO_BB_TX
UART0_BB_TO_OWL_RXD	UART0_BB_TO_OWL_RXD
USB_BB_P	USB_BB_P
USB_BB_N	USB_BB_N
USB_BB_VBUS_DETECT	USB_BB_VBUS_DETECT
SWD_CLK_BB_JTAG_TCK	SWD_CLK_BB_JTAG_TCK
SWD_IO_BB_JTAG_TMS	SWD_IO_BB_JTAG_TMS
75_RFFE5_SCLK_BB	75_RFFE5_SCLK_BB
75_RFFE5_SDATA_BB	75_RFFE5_SDATA_BB
RFFE_BUFFER_LAT_GPIO1	RFFE_BUFFER_LAT_GPIO1
BB_TO_PMU_AMUX_LD011_SIM1	BB_TO_PMU_AMUX_LD011_SIM1
BB_TO_PMU_AMUX_SMPS1	BB_TO_PMU_AMUX_SMPS1
BB_TO_PMU_AMUX_SMPS3	BB_TO_PMU_AMUX_SMPS3
BB_TO_PMU_AMUX_SMPS4	BB_TO_PMU_AMUX_SMPS4
ANT	
50_UPPER_ANT_FEED	50_UPPER_ANT_FEED
50_WIFI_5G_CONN_ANT	50_WIFI_5G_CONN_ANT
STOCKHOLM_ANT	STOCKHOLM_ANT
AP DEBUG	
PP1V8	DFU_STATUS
FORCE_DFU	FORCE_DFU
PMU_AMUX_AY	PMU_AMUX_AY
PMU_AMUX_BY	PMU_AMUX_BY
PMU_TO_SYSTEM_COLD_RESET_L	PMU_TO_SYSTEM_COLD_RESET_L
I2C0_AP_SCL	I2C0_AP_SCL
I2C0_AP_SDA	I2C0_AP_SDA
I2C1_AP_SCL	I2C1_AP_SCL
I2C1_AP_SDA	I2C1_AP_SDA
BUTTON_HOLD_KEY_L	BUTTON_HOLD_KEY_L
BUTTON_MENU_KEY_L	BUTTON_MENU_KEY_L
BUTTON_RINGER_A	BUTTON_RINGER_A
BUTTON_VOL_DOWN_L	BUTTON_VOL_DOWN_L
BUTTON_VOL_UP_L	BUTTON_VOL_UP_L
PMU_GPIO20	PMU_GPIO20
PMU_GPIO21	PMU_GPIO21
AP_RESERVED0	AP_RESERVED0
AP_RESERVED1	AP_RESERVED1
AP_RESERVED2	AP_RESERVED2

PCIE AP TO WLAN TXD P	
PCIE AP TO WLAN TXD N	
PCIE WLAN TO AP RXD P	
PCIE WLAN TO AP RXD N	
PCIE AP TO WLAN REFCLK P	
PCIE AP TO WLAN REFCLK N	
PCIE AP TO WLAN RESET L	
PCIE AP TO WLAN DEV WAKE	
PCIE WLAN TO AP CLKREQ L	
UART AP TO WLAN TXD	
UART AP TO WLAN RTS L	
UART WLAN TO AP RXD	
UART WLAN TO AP CTS L	
PMU TO WLAN CLK32K	
PMU TO WLAN REG_ON	
WLAN TO PMU HOST WAKE	
OWL TO WLAN CONTEXT A	
OWL TO WLAN CONTEXT B	
I2S AP TO BT LRCLK	
I2S AP TO BT BCLK	
I2S AP TO BT DOUT	
I2S BT TO AP DIN	
UART AP TO BT TXD	
UART AP TO BT RTS L	
UART BT TO AP RXD	
UART BT TO AP CTS L	
PMU TO BT REG_ON	
BT TO PMU HOST WAKE	
AP TO BT WAKE	
UART AP TO STOCKHOLM TXD	
UART AP TO STOCKHOLM RTS L	
UART STOCKHOLM TO AP RXD	
UART STOCKHOLM TO AP CTS L	
PMU TO STOCKHOLM_EN	
STOCKHOLM TO PMU HOST WAKE	
AP TO STOCKHOLM DEV WAKE	
AP TO STOCKHOLM DWLD REQUEST	

PAGE TITLE			
BASEBAND:RADIO SYMBOL			
DRAWING NUMBER		SIZE	
051-1902		D	
REVISION		A.0.0	
BRANCH			
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- 1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
- 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
- 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD	DATE
A	0004536627	PRODUCTION RELEASED		2015-07-21

N71 RADIO_MLB_MIMO - PVT

JULY 07, 2015

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3	3	FE: ANT CONNECTORS AND UAT TUNER
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6	32	CELLULAR BASEBAND: POWER2
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14	40	CELLULAR TRANSCEIVER: DRX/GPS PORTS
15	41	CELLULAR TRANSCEIVER: TX PORTS
16	42	CELLULAR FRONT END: LB PAD
17	43	CELLULAR FRONT END: MB PAD
18	44	CELLULAR FRONT END: HB PAD
19	45	CELLULAR FRONT END: 2G PA
20	46	CELLULAR FRONT END: LB ASM
21	47	CELLULAR FRONT END: MB-HB ASM
22	48	CELLULAR FRONT END: DIVERSITY
23	49	SIM
24	50	WIFI/BT: WIFI/BT MODULE
25	51	STOCKHOLM

ROW/RF2 HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	ROW
152S1990	1	3.0NH, INDUCTOR	L4105_RF	RF2
131S0377	1	1.2PF, CAPACITOR	C4108_RF	RF2
152S2007	1	8.2NH, INDUCTOR	L4401_RF	ROW
131S0426	1	22PF, CAPACITOR	C4405_RF	ROW
152S2001	1	2.4NH, INDUCTOR	C4405_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4406_RF	RF2
152S2044	1	2.2NH, INDUCTOR	C4406_RF	ROW
152S2021	1	1.5NH, INDUCTOR	C4406_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4407_RF	ROW
152S2056	1	5.6NH, INDUCTOR	L4403_RF	ROW
131S0429	1	8.2PF, CAPACITOR	C4407_RF	ROW
152S2036	1	2.5NH, INDUCTOR	C4407_RF	RF2
131S0631	1	0.3PF, CAPACITOR	L4408_RF	RF2
152S00143	1	15NH, INDUCTOR	L4404_RF	ROW
131S0823	1	33PF, CAPACITOR	C4408_RF	ROW
152S2051	1	1.3NH, INDUCTOR	C4408_RF	RF2
152S2042	1	1.8NH, INDUCTOR	C4409_RF	RF2
117S0108	1	51 OHM, RESISTOR	L4410_RF	ROW
131S0363	1	0.6PF, CAPACITOR	L4410_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3910_RF	ROW
152S00026	1	3.5NH, INDUCTOR	L3910_RF	RF2
152S2039	1	3.8NH, INDUCTOR	L3911_RF	ROW
117S0201	1	0 OHM, RESISTOR	L3911_RF	RF2
131S0279	1	1.3PF, CAPACITOR	L3919_RF	ROW
152S2045	1	3.0NH, INDUCTOR	L3919_RF	RF2
152S00052	1	3.4NH, INDUCTOR	L3912_RF	RF2
131S0599	1	1.5PF, CAPACITOR	C3922_RF	RF2
131S0630	1	27PF, CAPACITOR	C3911_RF	RF2

LB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	ROW
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	RF2
353S00461	1	IC, PWR AMP, LB_PAD, SKWS	ULBPA_RF	RFC
353S00541	1	IC, PWR AMP, LB_PAD, PT	ULBPA_RF	DARWIN

MB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	ROW
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	RF2
353S4495	1	IC, PWR AMP, MB_PAD	UMBPA_RF	RFC
353S00477	1	IC, PWR AMP, MB_PAD, PT	UMBPA_RF	DARWIN

RFC HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	RFC
152S2007	1	8.2NH, INDUCTOR	L4401_RF	RFC
131S0426	1	22PF, CAPACITOR	C4405_RF	RFC
152S2044	1	2.2NH, INDUCTOR	C4406_RF	RFC
131S0631	1	0.3PF, CAPACITOR	L4407_RF	RFC
152S2056	1	5.6NH, INDUCTOR	L4403_RF	RFC
131S0429	1	8.2PF, CAPACITOR	C4407_RF	RFC
152S00143	1	15NH, INDUCTOR	L4404_RF	RFC
131S0823	1	33PF, CAPACITOR	C4408_RF	RFC
117S0108	1	51 OHM, RESISTOR	L4410_RF	RFC
152S00052	1	3.4NH, INDUCTOR	L3910_RF	RFC
152S2039	1	3.8NH, INDUCTOR	L3911_RF	RFC
131S0279	1	1.3PF, CAPACITOR	L3919_RF	RFC

DARWIN HB PAD MATCHING BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
152S1907	1	3.3NH, INDUCTOR	L4105_RF	DARWIN
152S2007	1	8.2NH, INDUCTOR	L4401_RF	DARWIN
131S0426	1	22PF, CAPACITOR	C4405_RF	DARWIN
152S2044	1	2.2NH, INDUCTOR	C4406_RF	DARWIN
131S0631	1	0.3PF, CAPACITOR	L4407_RF	DARWIN
152S2056	1	5.6NH, INDUCTOR	L4403_RF	DARWIN
131S0429	1	8.2PF, CAPACITOR	C4407_RF	DARWIN
152S00143	1	15NH, INDUCTOR	L4404_RF	DARWIN
131S0823	1	33PF, CAPACITOR	C4408_RF	DARWIN
117S0108	1	51 OHM, RESISTOR	L4410_RF	DARWIN
152S00052	1	3.4NH, INDUCTOR	L3910_RF	DARWIN
152S2039	1	3.8NH, INDUCTOR	L3911_RF	DARWIN
131S0279	1	1.3PF, CAPACITOR	L3919_RF	DARWIN

LAT DIPLEXER1

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	ROW
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	RF2
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	RFC
155S0971	1	LAT CELL DIPLEXER1,TDK	FLDIP_RF	DARWIN

HB PAD

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
353S00376	1	IC, PWR AMP, HB_PAD, TQS	UHBPA_RF	ROW
353S4494	1	IC, PWR AMP, HB_PAD, AVAGO	UHBPA_RF	RF2
353S00376	1	IC, PWR AMP, HB_PAD, TQS	UHBPA_RF	RFC
353S00478	1	IC, PWR AMP, HB_PAD, PT	UHBPA_RF	DARWIN

19.2MHZ XTAL ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y_XO_RF	XTAL, 19.2MHZ
197S0598	197S0593	ALTERNATE	Y_XO_RF	XTAL, 19.2MHZ

VINYL

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
337S00176	1	IC, VINYL	U5101_RF	ROW
337S00176	1	IC, VINYL	U5101_RF	RF2

VINYL RESISTOR

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
117S0161	1	0 OHM, RESISTOR	R3402_RF	RFC
117S0161	1	0 OHM, RESISTOR	R3402_RF	DARWIN

HW_REV1_ID RESISTOR

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	BOM OPTION
118S0646	1	51.1 KOHM, RESISTOR	R3503_RF	DARWIN

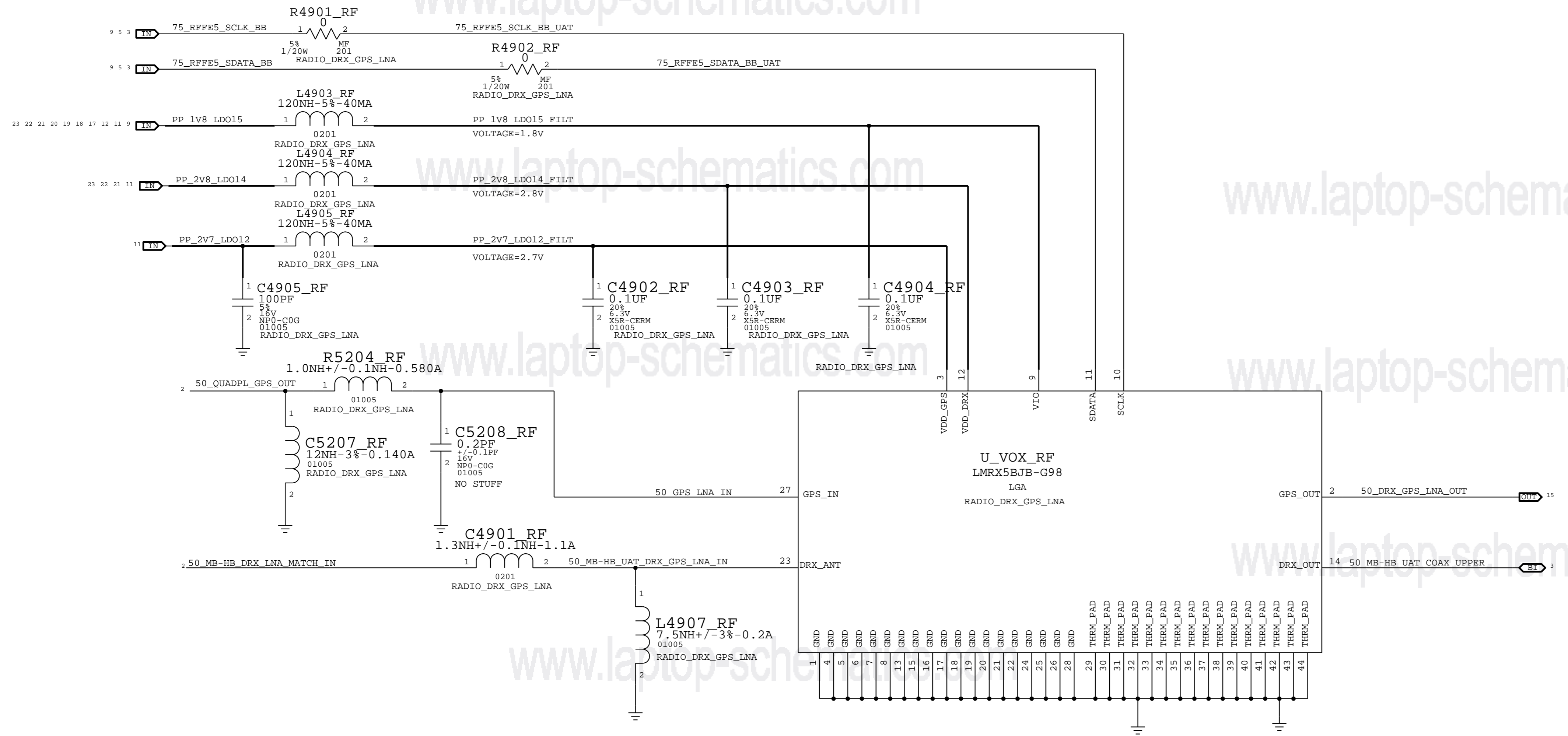
SIM ESD DIODE ALTERNATE

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
377S00042	377S0163	ALTERNATE	VR301_RF	ON SEMI ESD DIODE

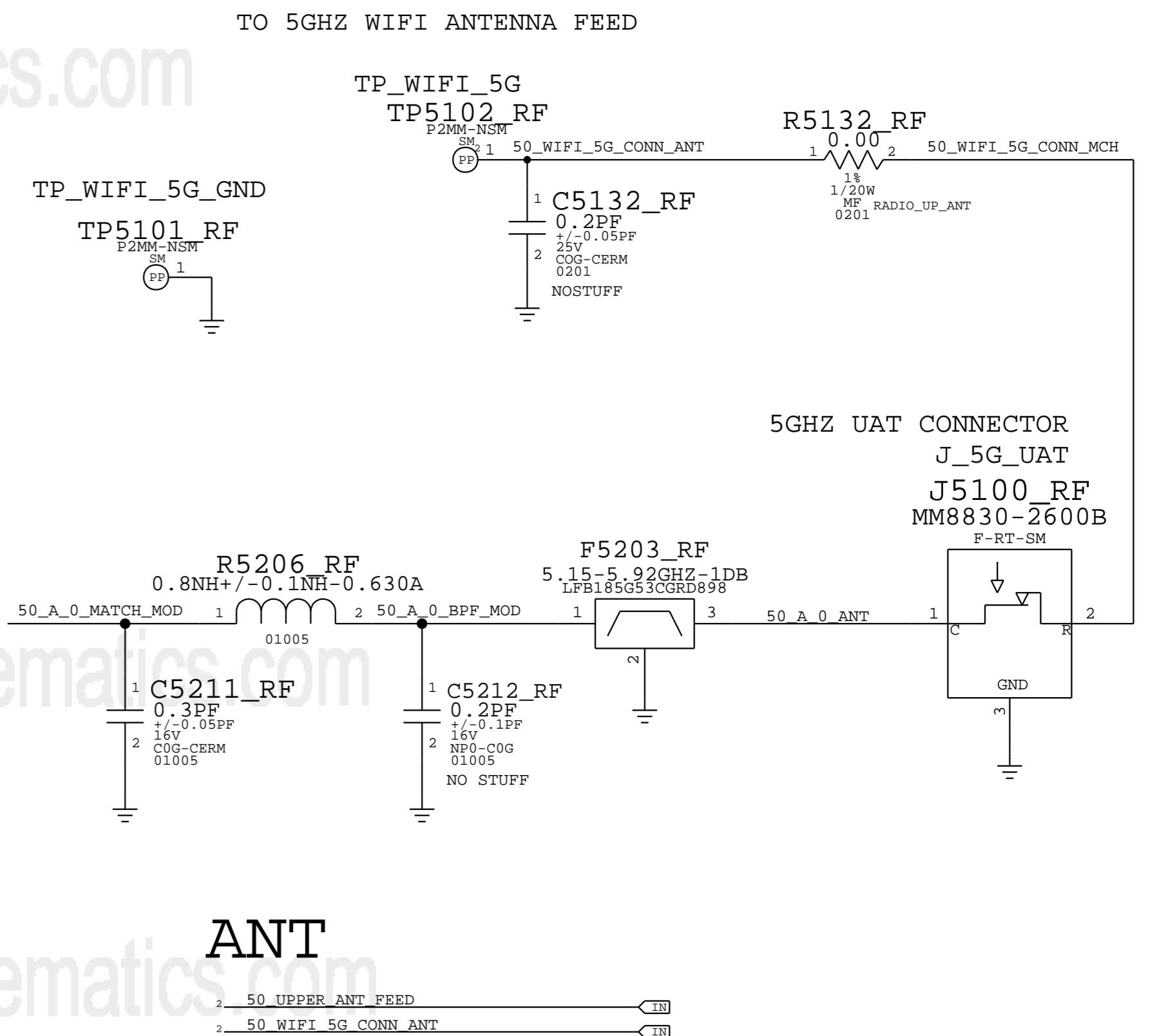
DRAWING TITLE SCHEM, SINGLE, BRD, N71		DRAWING NUMBER 051-1902	SIZE D
Apple Inc.		REVISION A.0.0	BRANCH
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N71-SPECIFIC RADIO PAGE 2

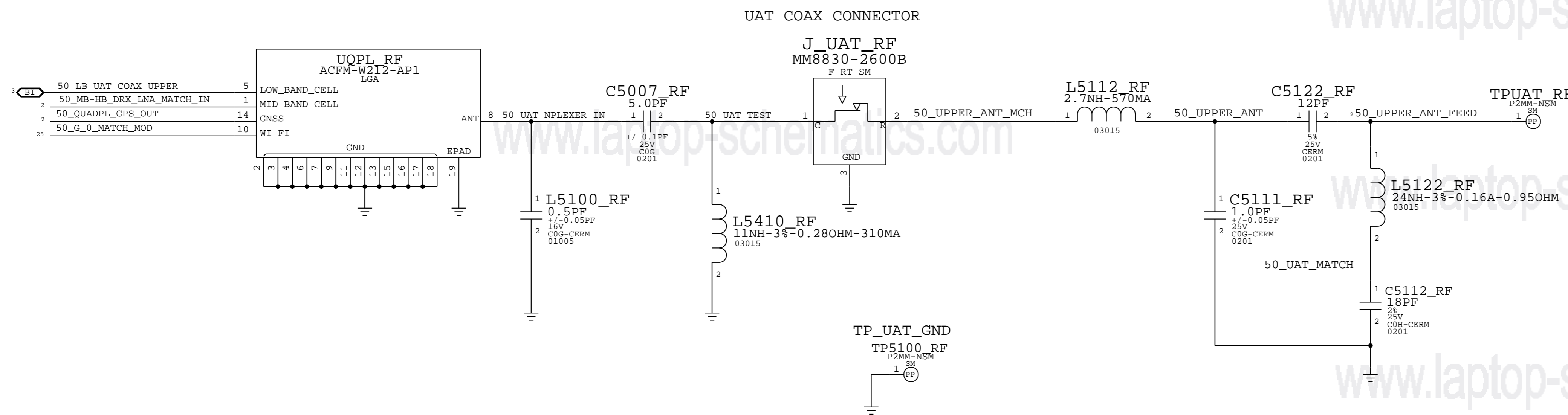
DIVERSITY LNA



WIFI ANT FEED



UAT ANT FEED



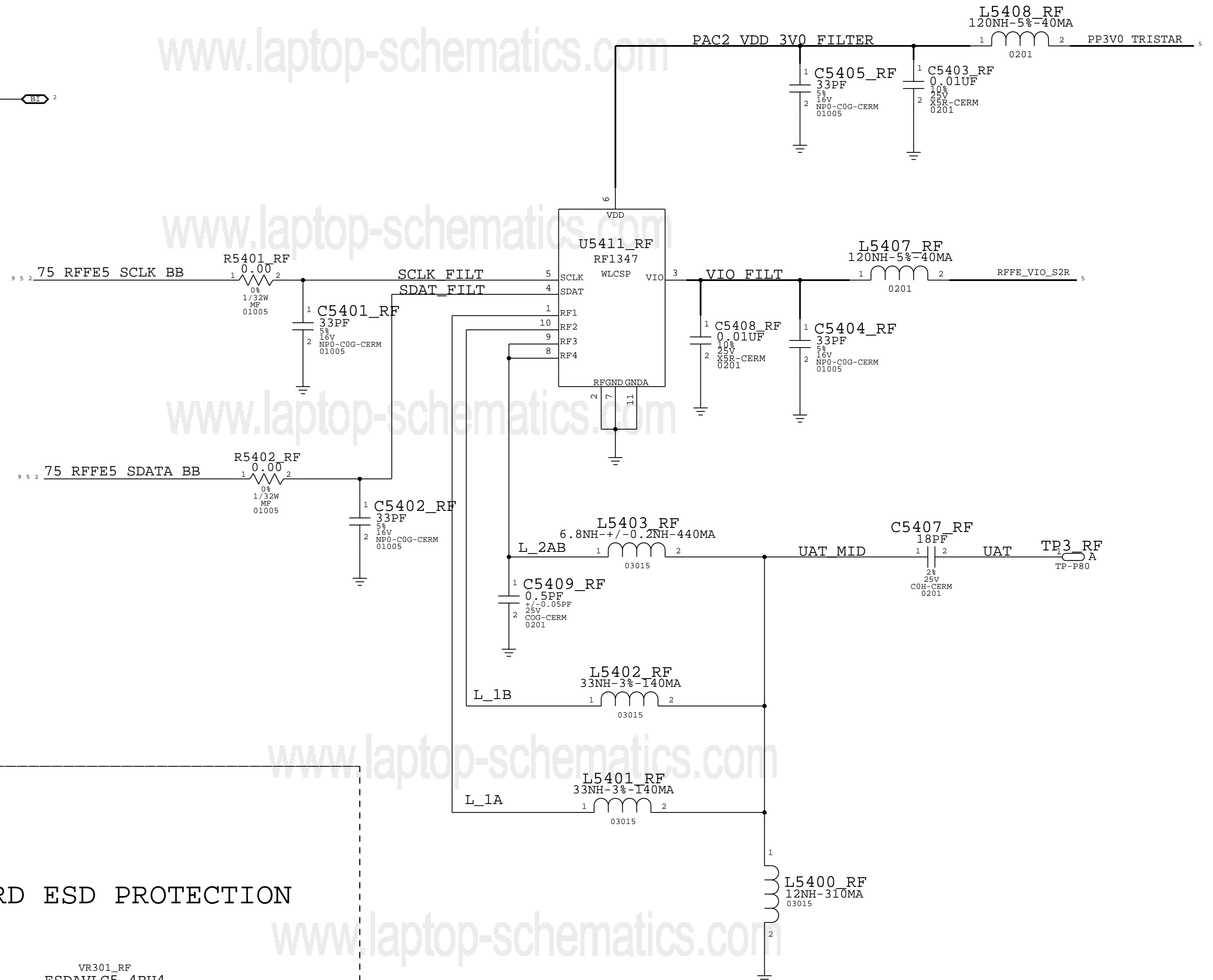
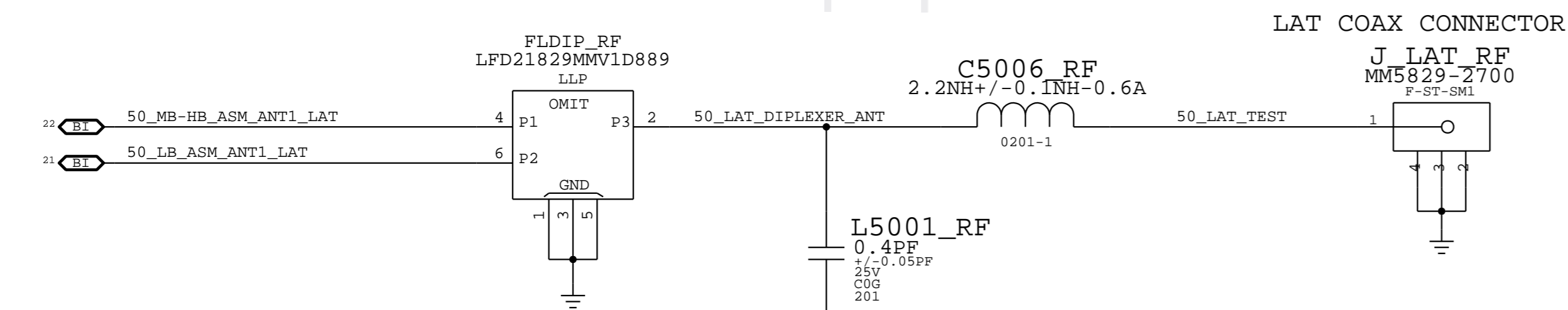
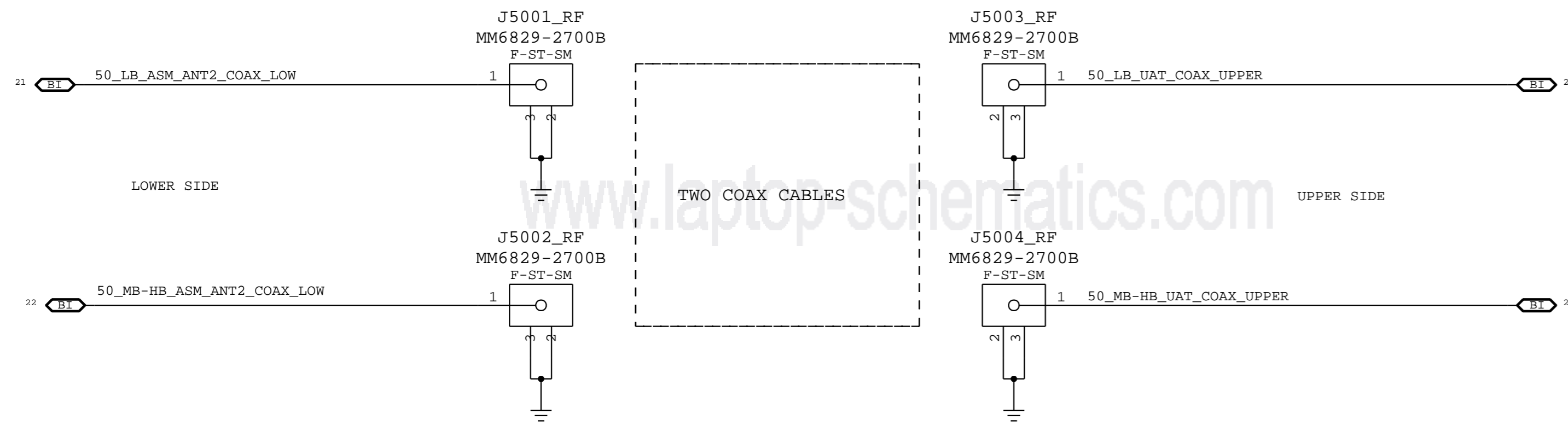
ANT

PAGE TITLE		ELNA & UAT ANT FEED	
Apple Inc.		DRAWING NUMBER	051-1902
		REVISION	A.0.0
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II NOT TO REPRODUCE OR COPY IT			
III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART			
IV ALL RIGHTS RESERVED			

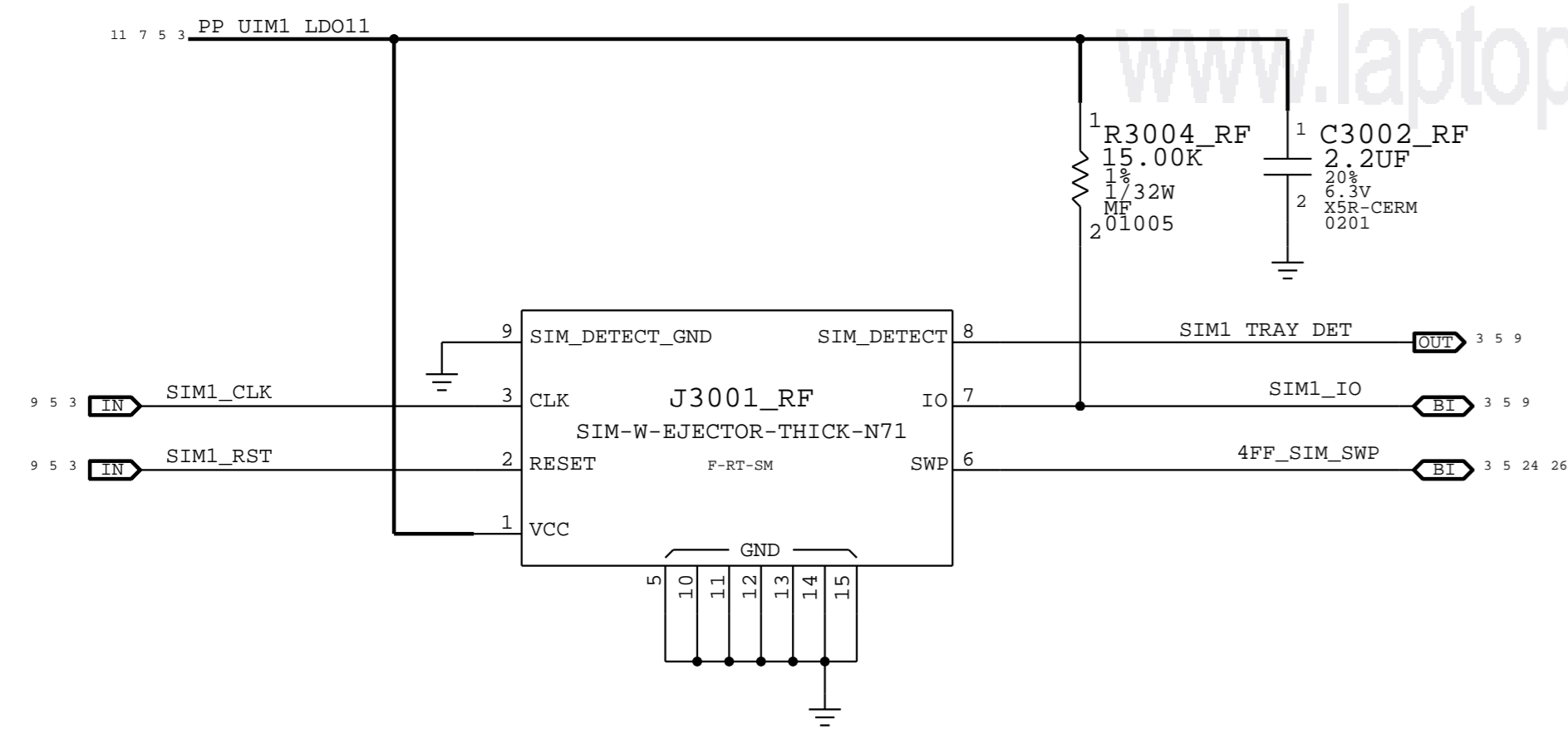
N71-SPECIFIC RADIO PAGE 3

ANTENNA FEEDS AND CONNECTORS

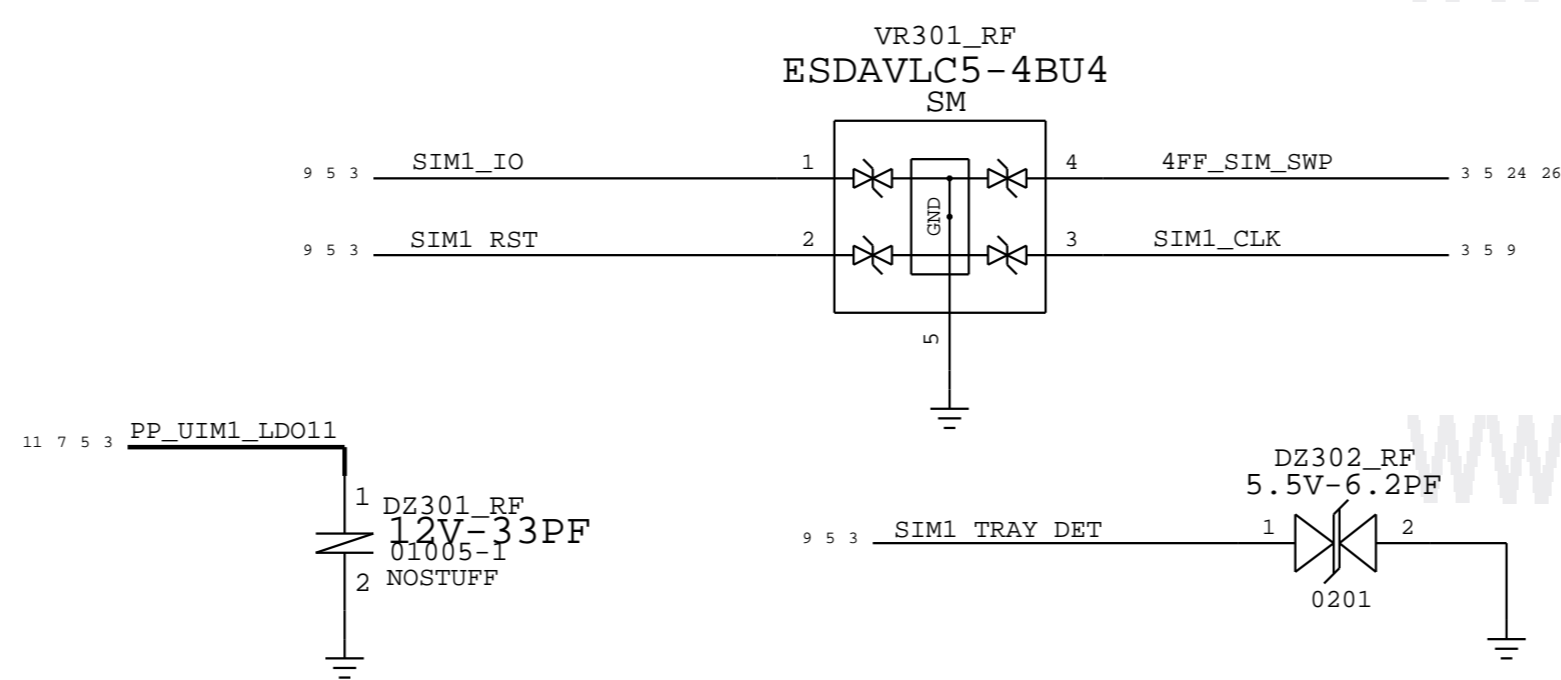
UAT TUNER



SIM CARD CONNECTOR



SIM CARD ESD PROTECTION



PAGE TITLE FE: ANT CONNECTORS AND UAT TUNER		
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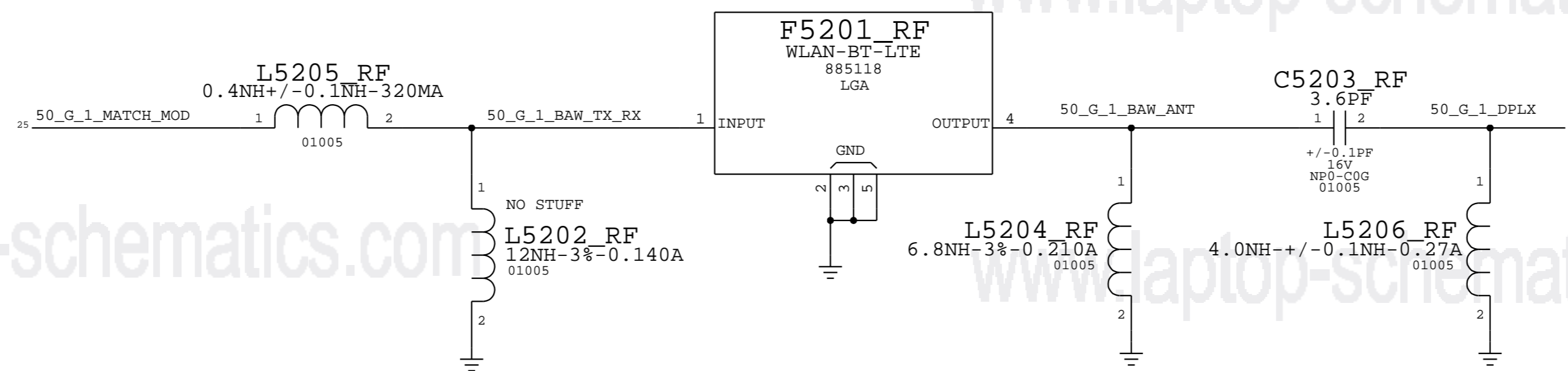
N71-SPECIFIC RADIO PAGE 4

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WLAN LAT 2.4GHZ BAW BPF

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AP TO BB/WLAN/BT/SH CONNECTIONS

MLB PROBE POINTS

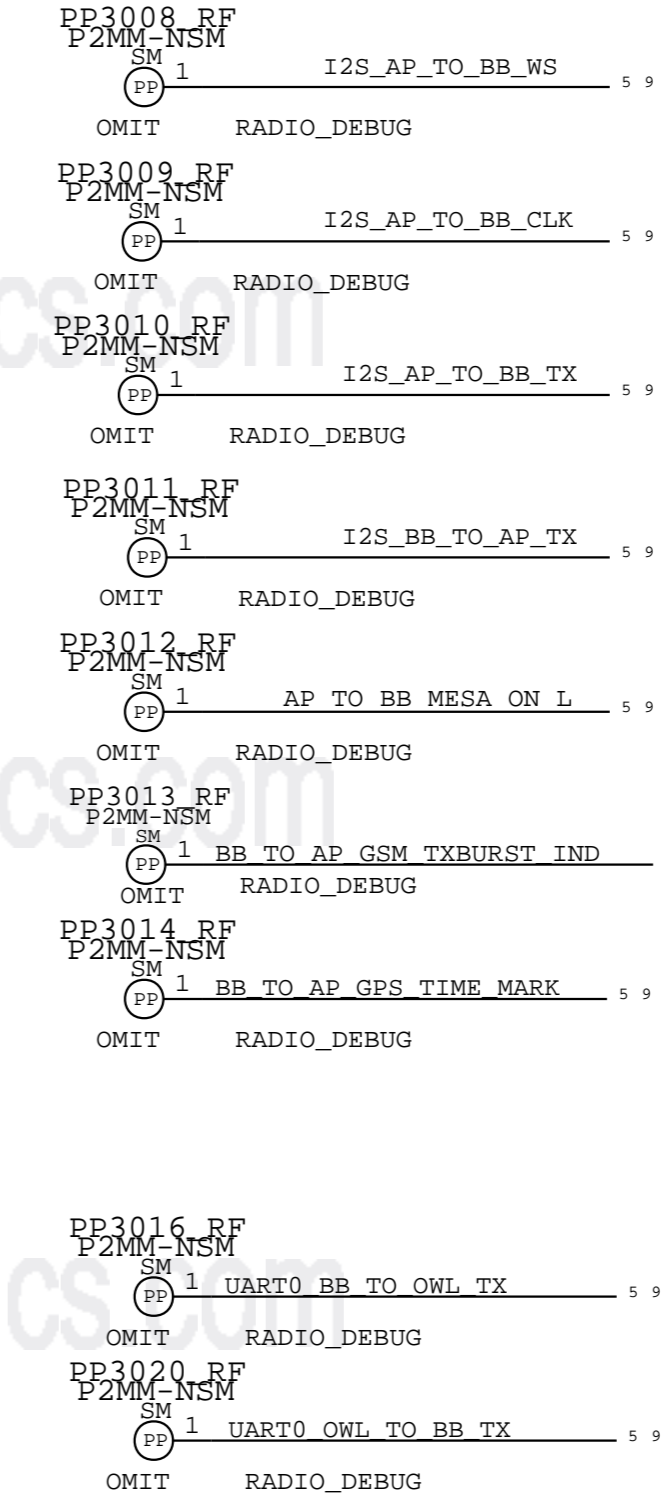
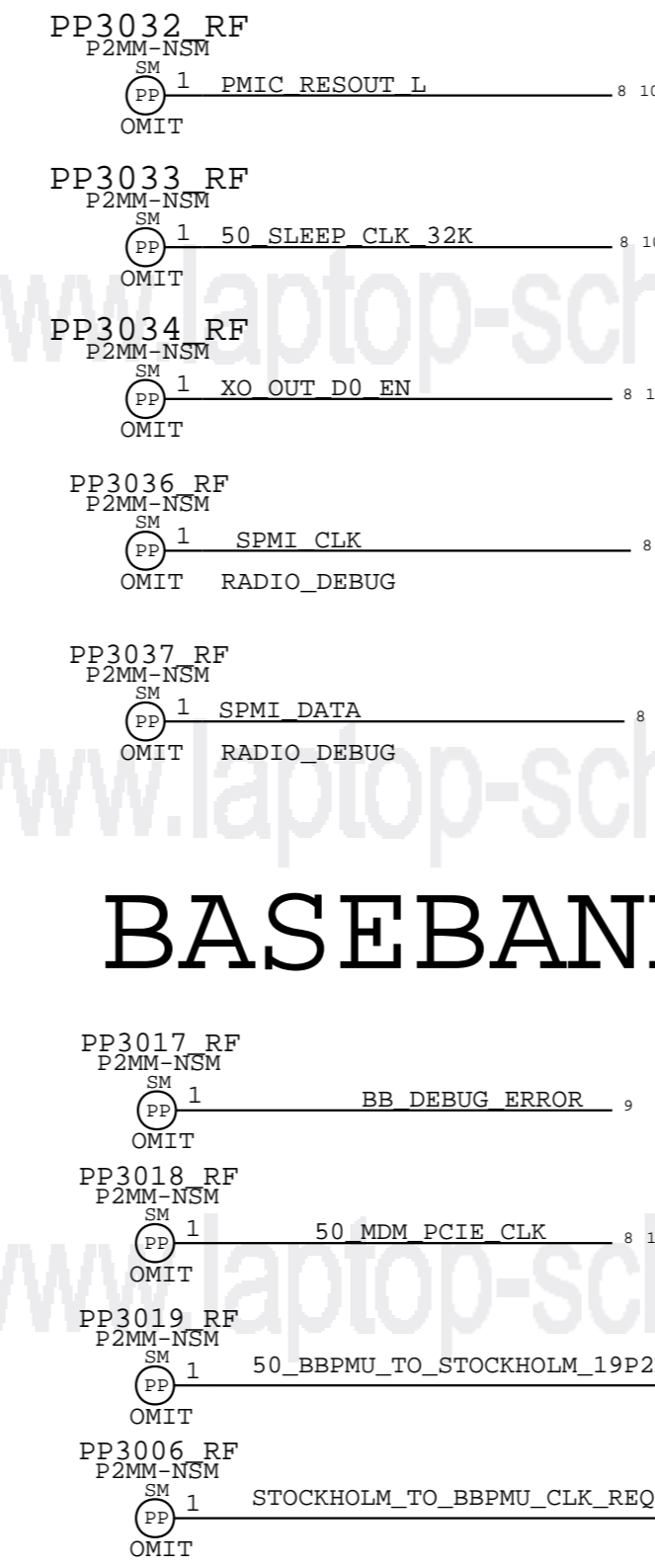
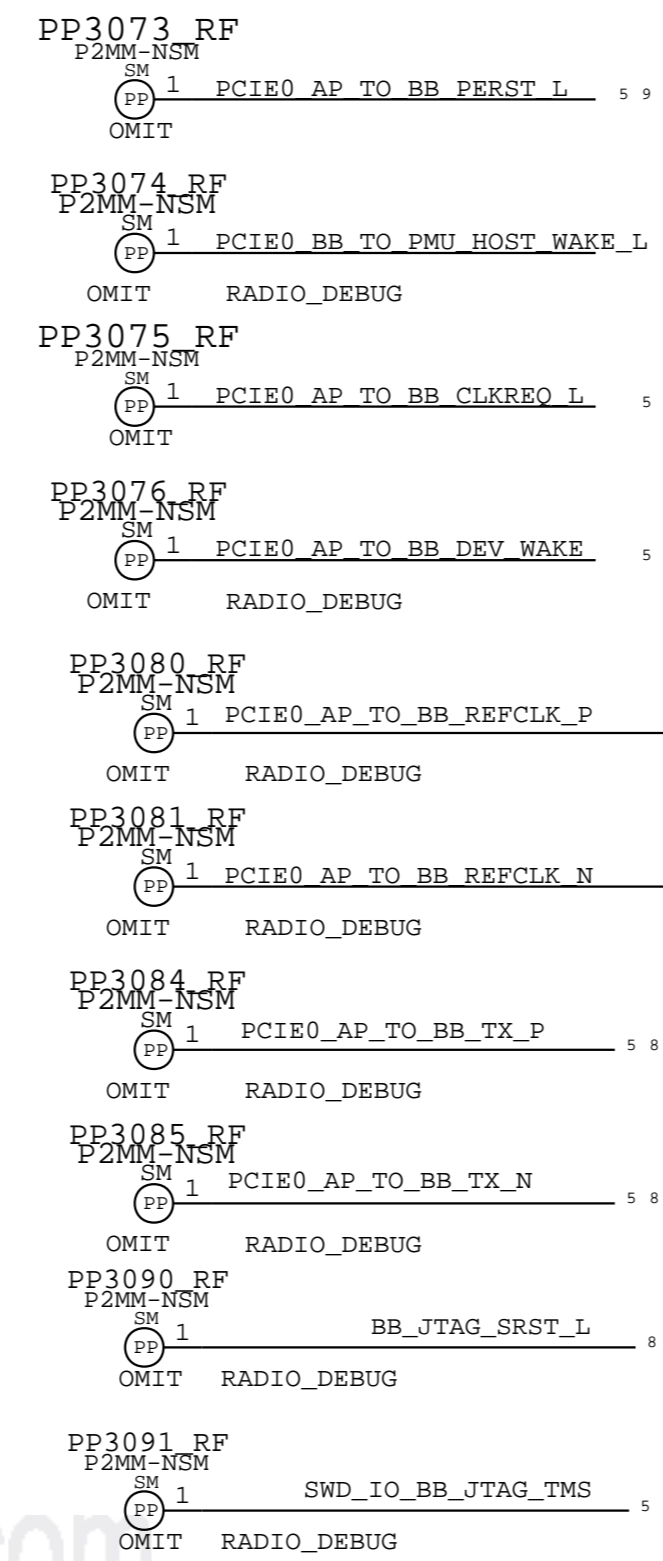
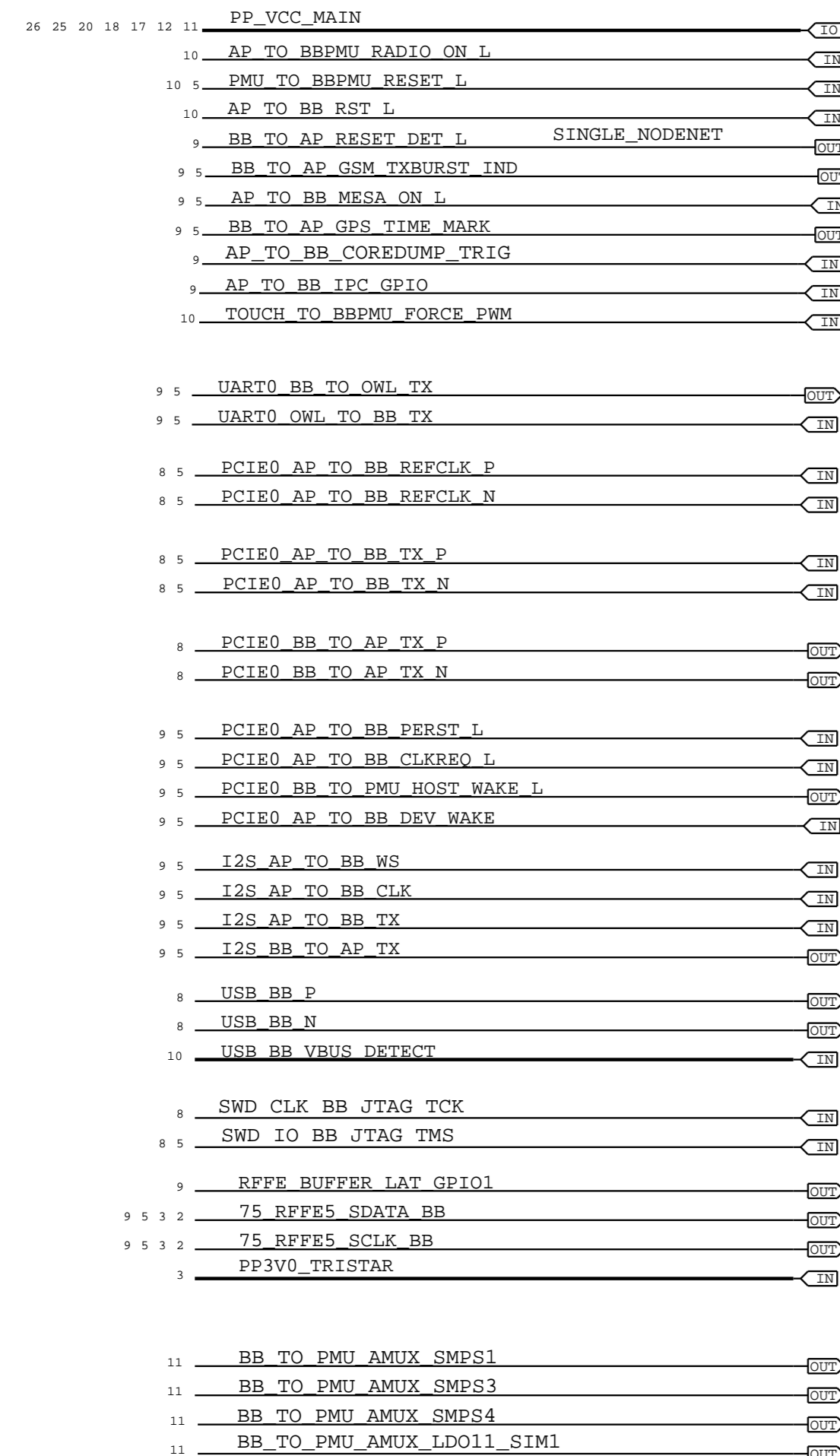
BASEBAND

WLAN/BT

PCIE

PMU

ANT TUNER

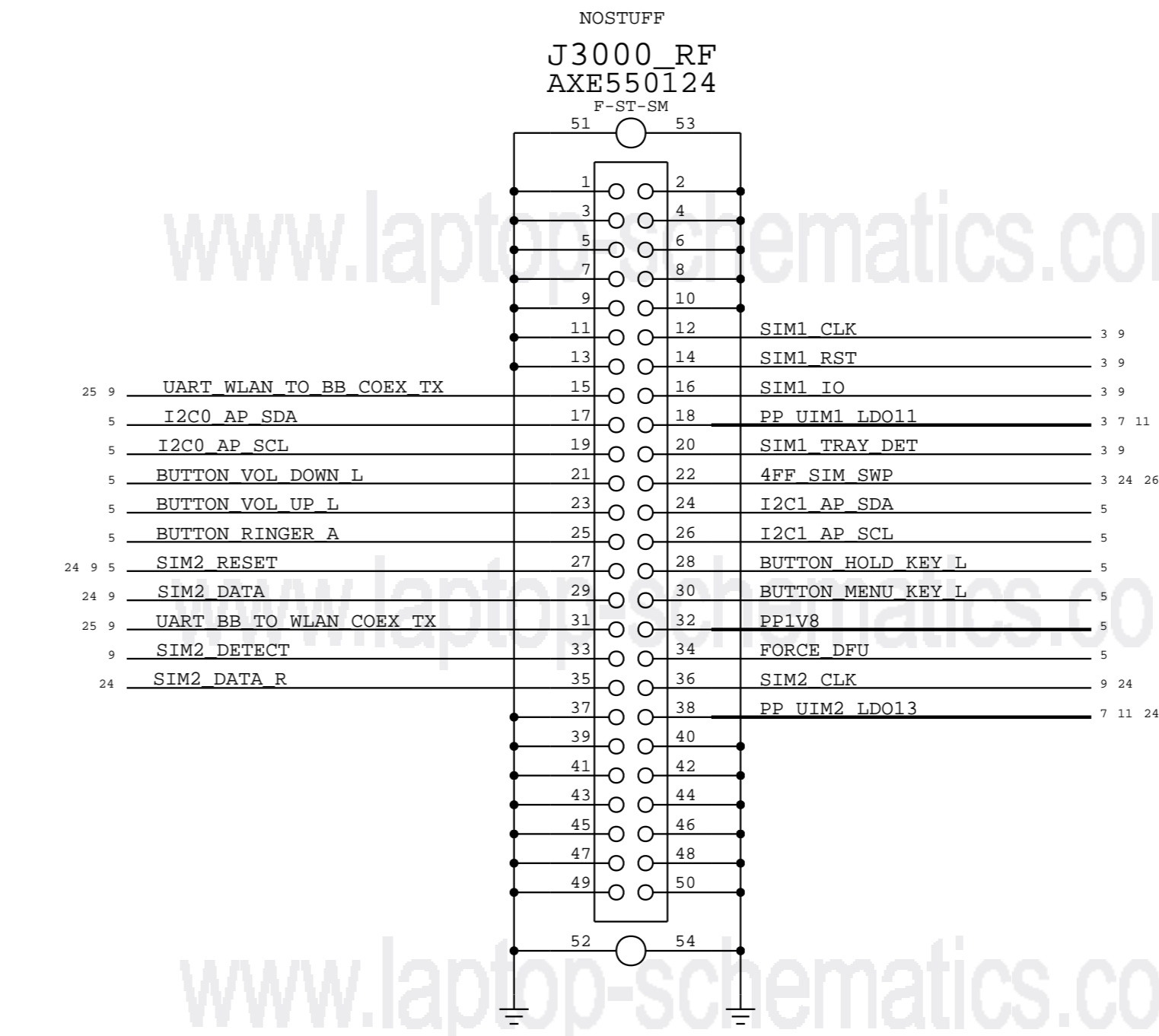
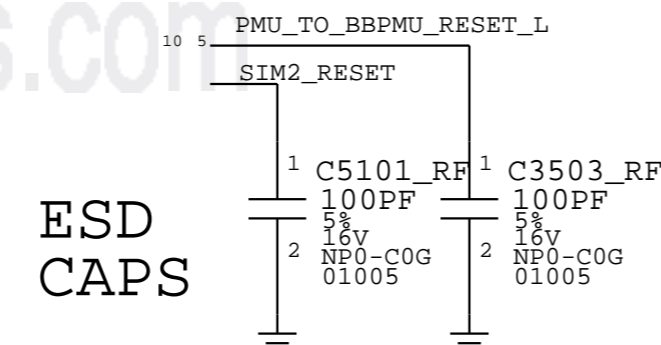
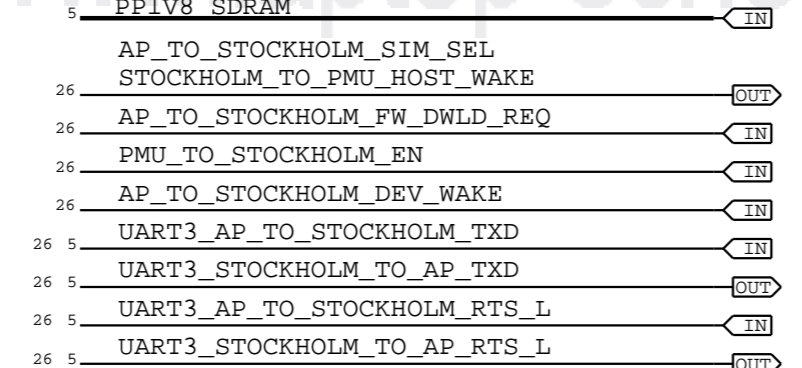
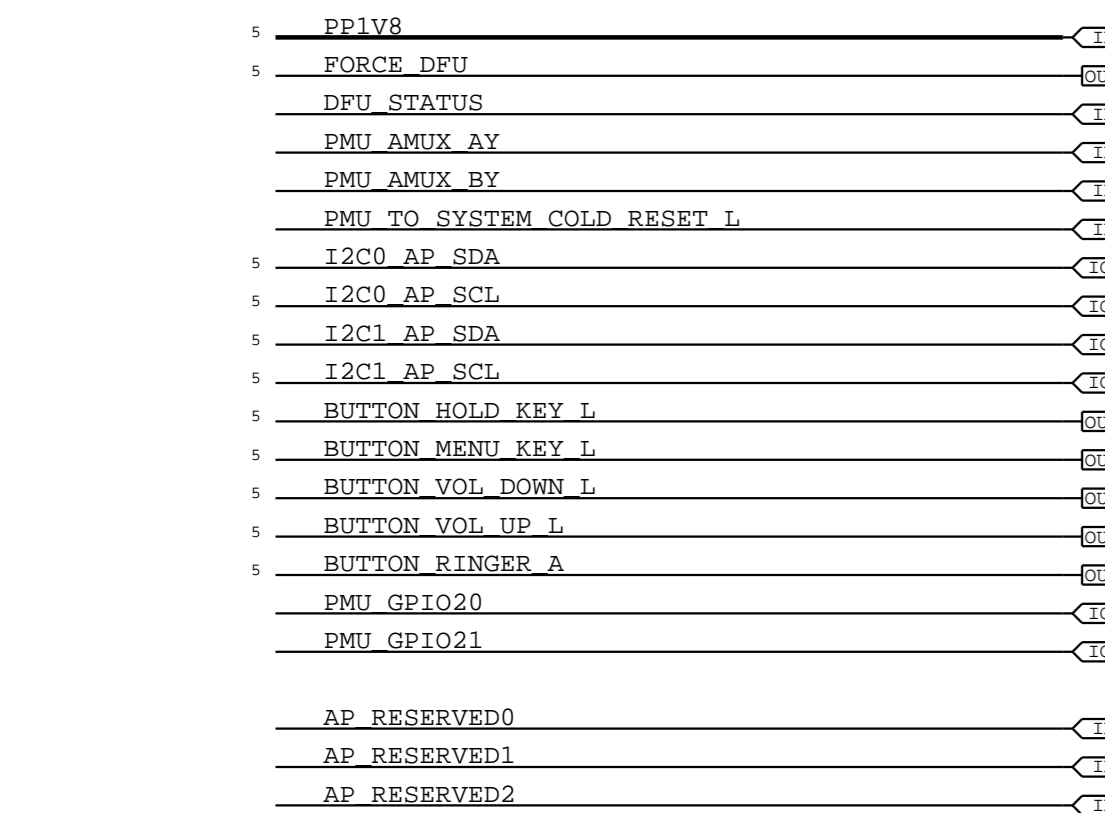


AP DEBUG

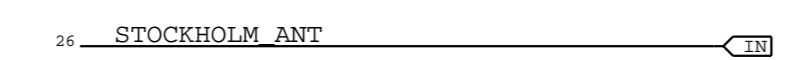
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DEBUG CONNECTOR

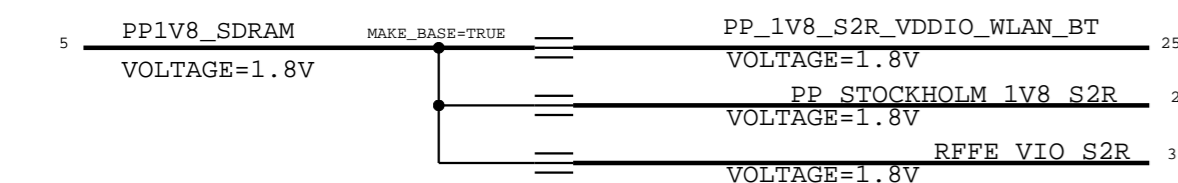
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ANT



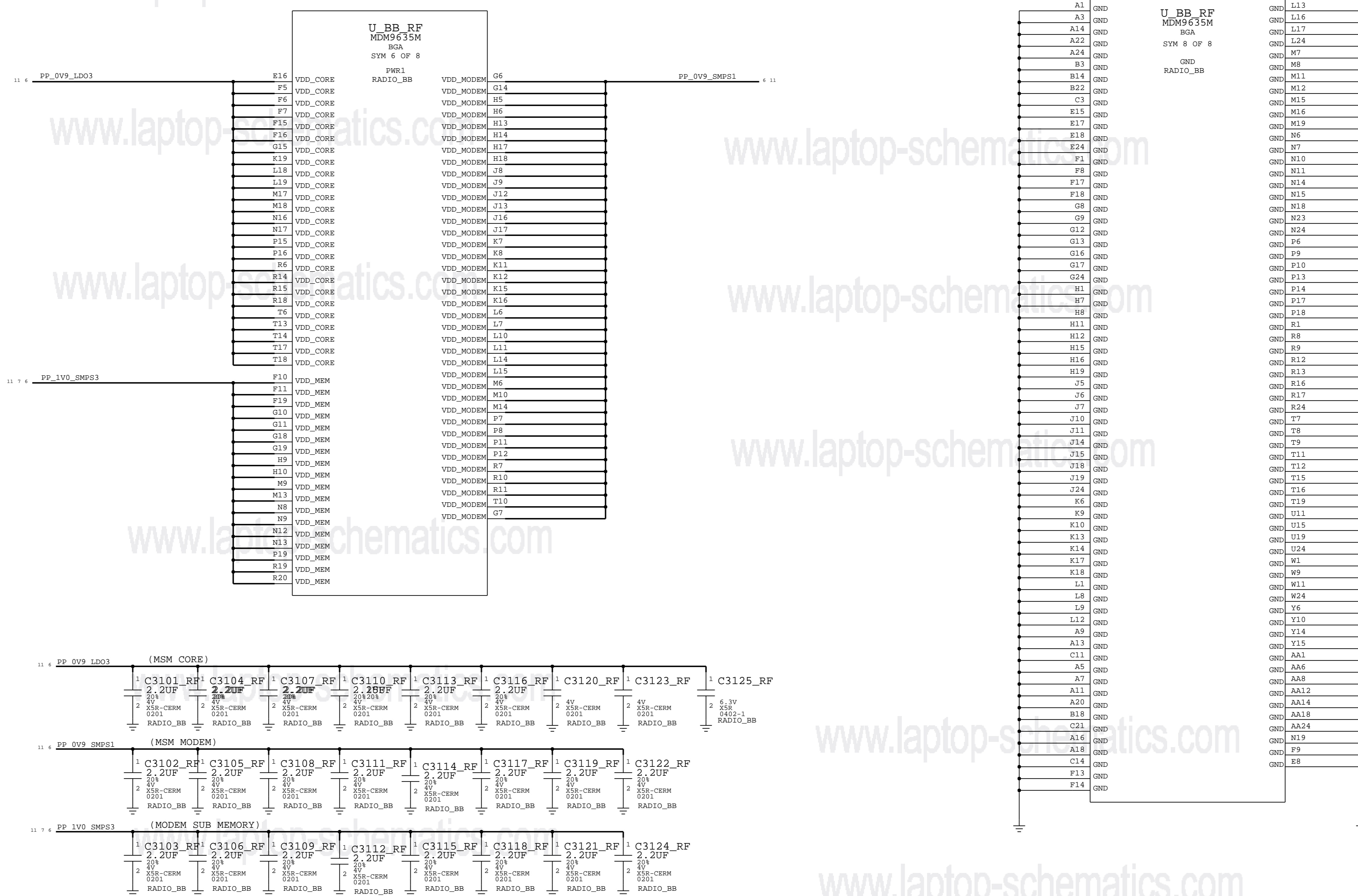
POWER



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BASEBAND: POWER 1

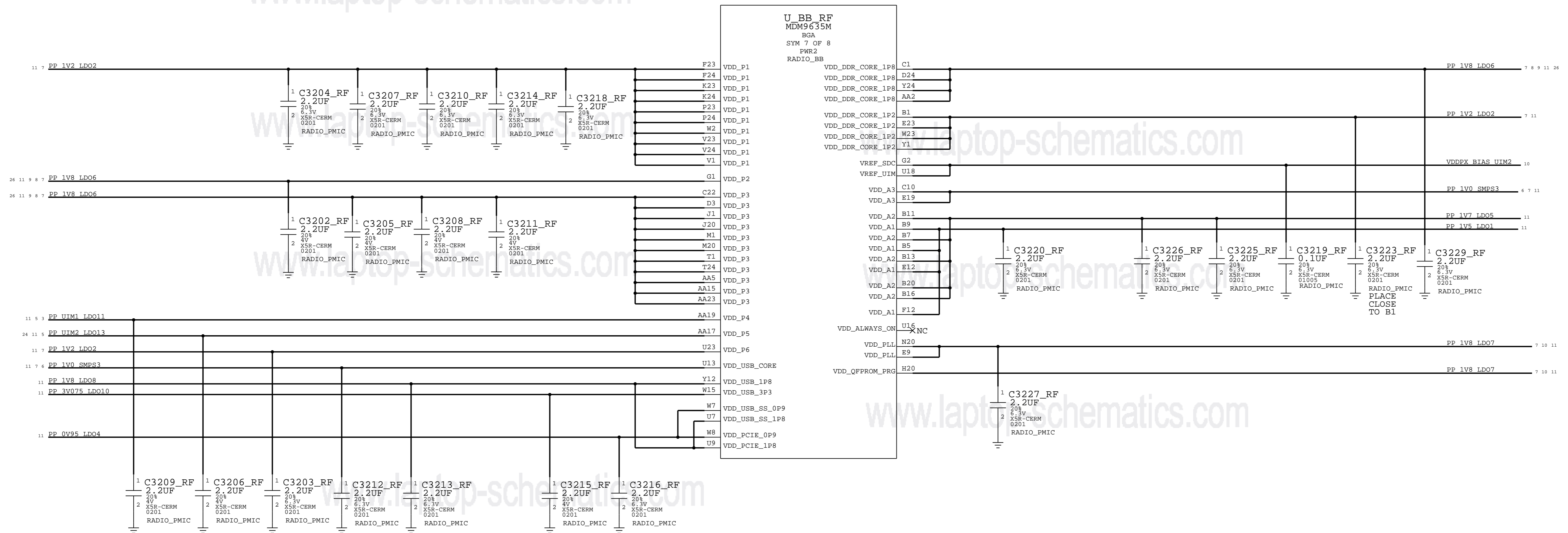
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BASEBAND: POWER 2

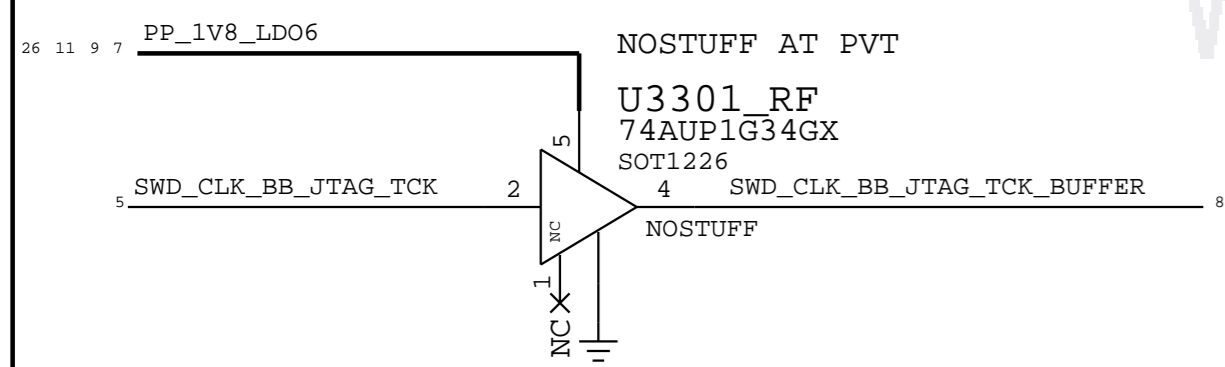
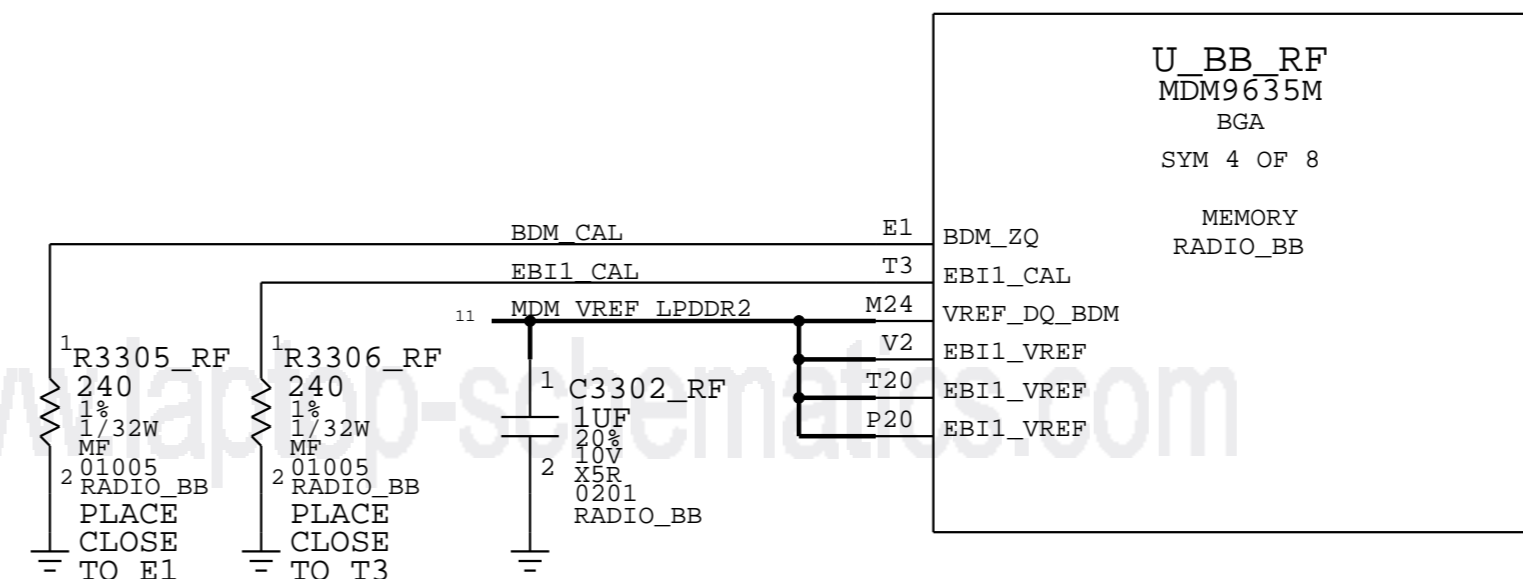
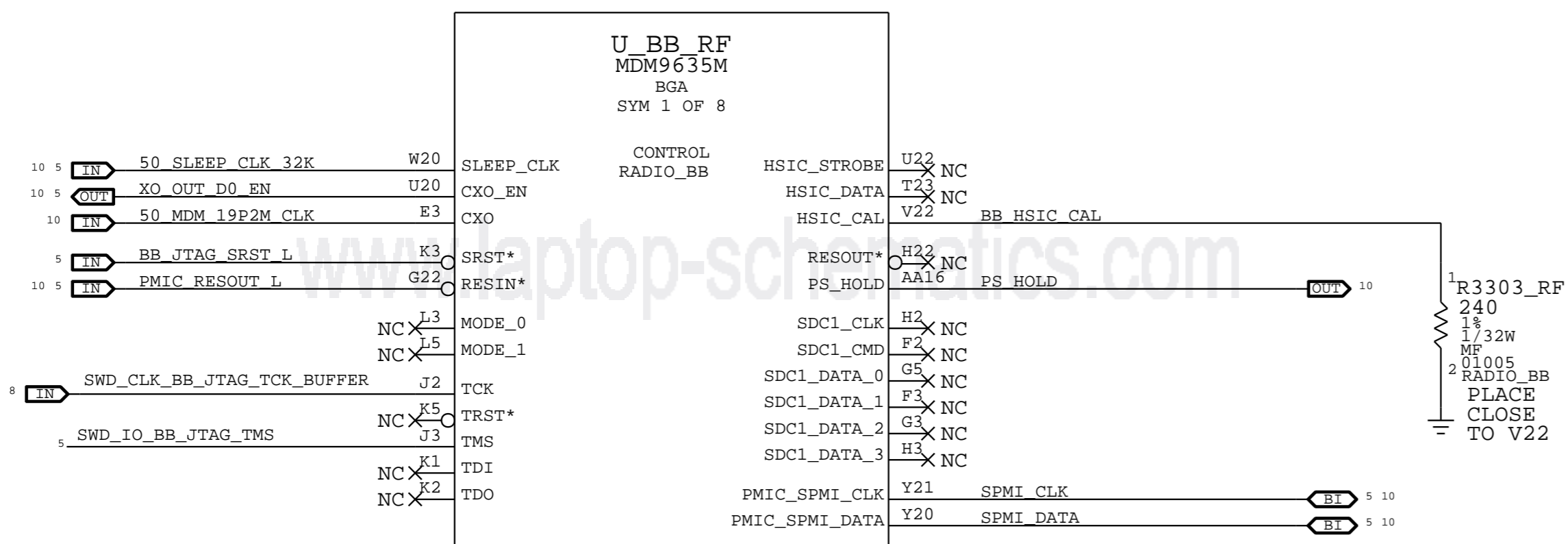
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BASEBAND: CONTROL AND INTERFACES

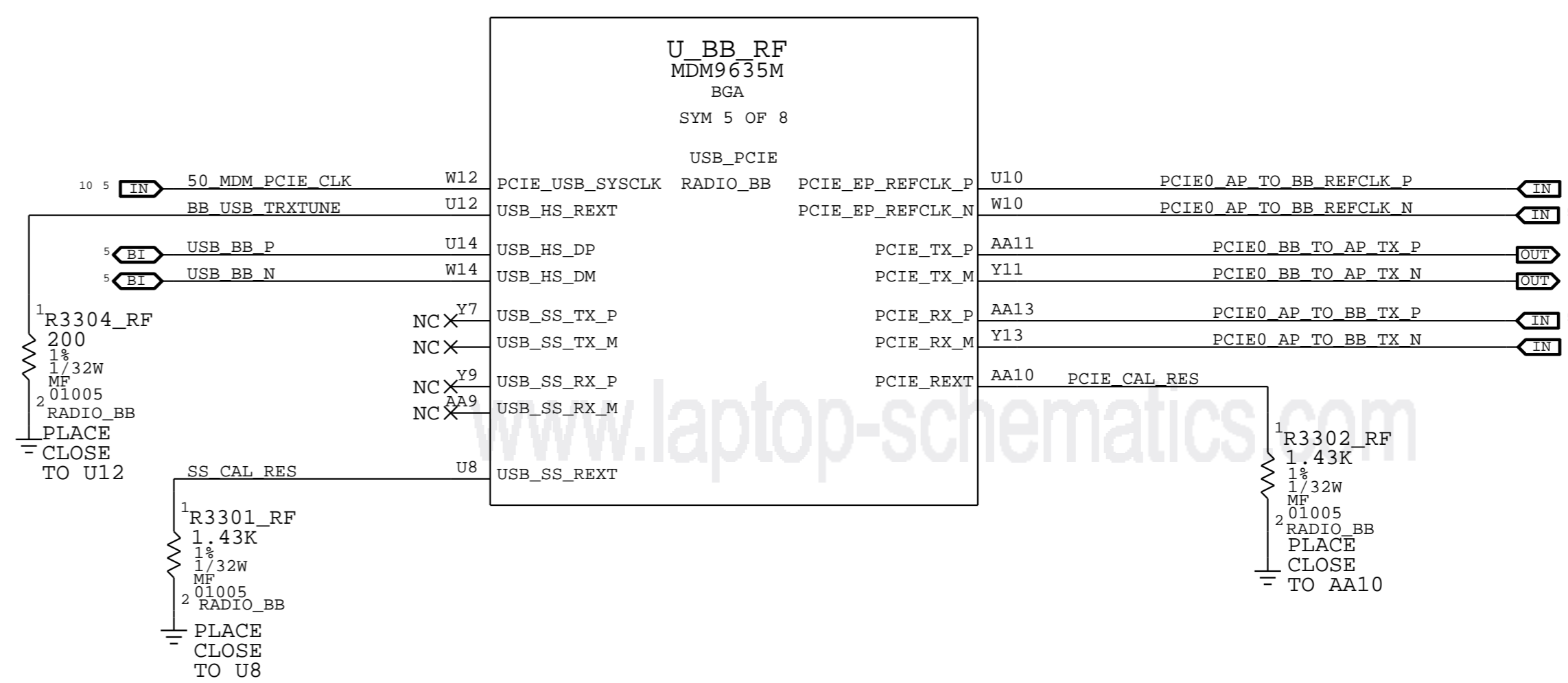
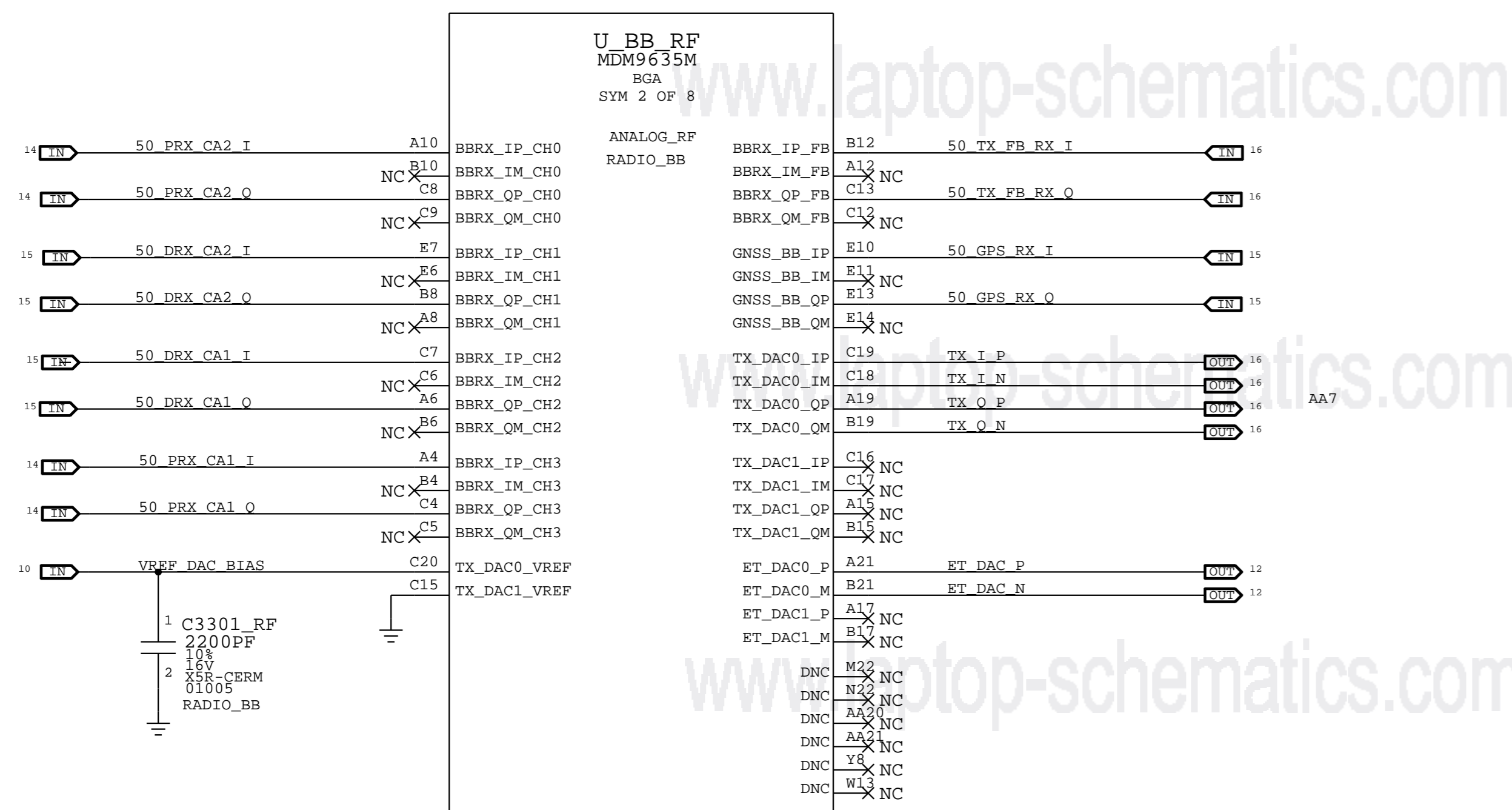
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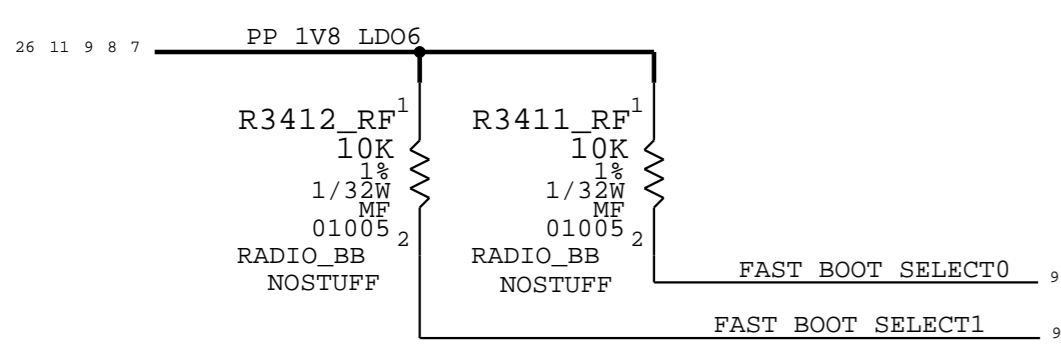
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BASEBAND: GPIOS

GPIO	Signal	Pin	Component
GPIO_0	SIM2_DATA	Y17	GPIO_0
GPIO_1	SIM2_DETECT	Y16	GPIO_1
GPIO_2	SIM2_RESET	W16	GPIO_2
GPIO_3	SIM2_CLK	W17	GPIO_3
GPIO_4	NC	L22	GPIO_4
GPIO_5	NC	L23	GPIO_5
GPIO_6	NC	L20	GPIO_6
GPIO_7	NC	L23	GPIO_7
GPIO_8	UART0_BB_TO_OWL_TX	W6	GPIO_8
GPIO_9	UART0_OWL_TO_BB_TX	Y5	GPIO_9
GPIO_10	NC	W5	GPIO_10
GPIO_11	NC	W4	GPIO_11
GPIO_12	I2S_AP_TO_BB_HS	Y23	GPIO_12
GPIO_13	I2S_AP_TO_BB_TX	W21	GPIO_13
GPIO_14	I2S_BB_TO_AP_TX	Y22	GPIO_14
GPIO_15	I2S_AP_TO_BB_CLK	Y22	GPIO_15
GPIO_16	NC	P22	GPIO_16
GPIO_17	NC	P23	GPIO_17
GPIO_18	NC	P22	GPIO_18
GPIO_19	NC	P22	GPIO_19
GPIO_20	NC	P24	GPIO_20
GPIO_21	NC	P23	GPIO_21
GPIO_22	BB_EEPROM_I2C_SDA	J23	GPIO_22
GPIO_23	BB_EEPROM_I2C_SCL	J22	GPIO_23
GPIO_24	NC	P22	GPIO_24
GPIO_25	NC	P24	GPIO_25
GPIO_26	NC	P5	GPIO_26
GPIO_27	NC	P3	GPIO_27
GPIO_28	NC	P2	GPIO_28
GPIO_29	NC	P1	GPIO_29
GPIO_30	NC	P5	GPIO_30
GPIO_31	NC	P3	GPIO_31
GPIO_32	FAST_BOOT_SELECT0	N2	GPIO_32
GPIO_33	FAST_BOOT_SELECT1	N1	GPIO_33
GPIO_34	NC	M5	GPIO_34
GPIO_35	NC	M2	GPIO_35
GPIO_36	BB_TO_AP_GSM_TXBURST_IND	L2	GPIO_36
GPIO_37	NC	L2	GPIO_37
GPIO_38	NC	L2	GPIO_38
GPIO_39	NC	L2	GPIO_39

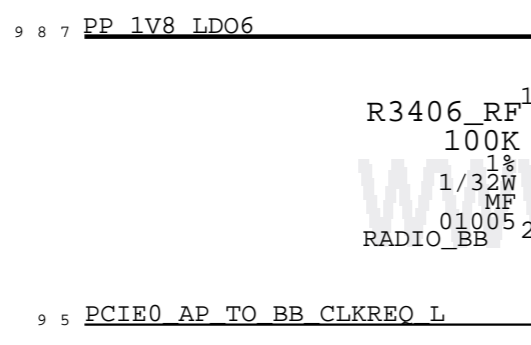
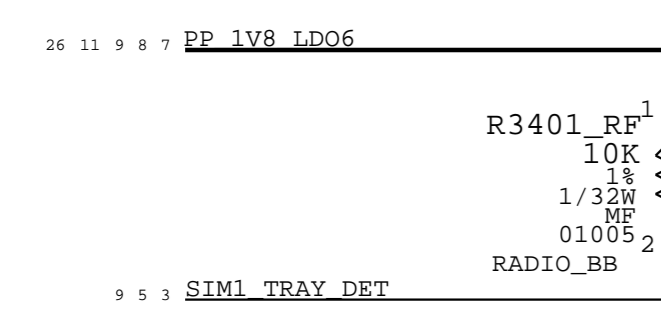
GPIO	Signal	Pin	Component
GPIO_40	D1	BB_TO_AP_GPS_TIME_MARK	GPIO_40
GPIO_41	F22	50_GSM_TX_PHASE	GPIO_41
GPIO_42	G20	NC	GPIO_42
GPIO_43	G23	NC	GPIO_43
GPIO_44	C23	75_RFFE3_SDATA_BB	GPIO_44
GPIO_45	C24	75_RFFE3_SCLK_BB	GPIO_45
GPIO_46	D22	75_RFFE4_SDATA_BB	GPIO_46
GPIO_47	D23	75_RFFE4_SCLK_BB	GPIO_47
GPIO_48	E22	75_RFFE5_SDATA_BB_BUFFER	GPIO_48
GPIO_49	F20	75_RFFE5_SCLK_BB_BUFFER	GPIO_49
GPIO_50	E2	NC	GPIO_50
GPIO_51	W4	NC	GPIO_51
GPIO_52	R5	NC	GPIO_52
GPIO_53	R2	UART_BB_TO_WLAN_COEX_TX	GPIO_53
GPIO_54	W3	UART_WLAN_TO_BB_COEX_TX	GPIO_54
GPIO_55	W5	PCIED_AP_TO_BB_DEV_WAKE	GPIO_55
GPIO_56	T5	BB_TO_AP_RESET_DET_L	GPIO_56
GPIO_57	A2	AP_TO_BB_COREDUMP_TRIG	GPIO_57
GPIO_58	B2	BB_DEBUG_ERROR	GPIO_58
GPIO_59	U3	NC	GPIO_59
GPIO_60	R3	AP_TO_BB_IPC_GPIO	GPIO_60
GPIO_61	T2	NC	GPIO_61
GPIO_62	Y2	NC	GPIO_62
GPIO_63	AA3	PCIED_BB_TO_PMU_HOST_WAKE_L	GPIO_63
GPIO_64	K20	NC	GPIO_64
GPIO_65	U6	PCIED_AP_TO_BB_CLKREQ_L	GPIO_65
GPIO_66	U5	PCIED_AP_TO_BB_PERST_L	GPIO_66
GPIO_67	U5	AP_TO_BB_MESA_ON_L	GPIO_67
GPIO_68	Y3	NC	GPIO_68
GPIO_69	U1	NC	GPIO_69
GPIO_70	U1	NC	GPIO_70
GPIO_71	V3	SIM1_REMOVAL_ALARM	GPIO_71
GPIO_72	W2	NC	GPIO_72
GPIO_73	E20	RF_SOC2BB_I2S_MCLK	GPIO_73
GPIO_74	A23	75_RFFE2_SDATA_BB	GPIO_74
GPIO_75	B23	75_RFFE2_SCLK_BB	GPIO_75
GPIO_76	B24	75_RFFE1_SDATA_BB	GPIO_76
GPIO_77	B24	75_RFFE1_SCLK_BB	GPIO_77
GPIO_78	Y19	SIM1_IO	GPIO_78
GPIO_79	W19	SIM1_TRAY_DET	GPIO_79
GPIO_80	W18	SIM1_RST	GPIO_80
GPIO_81	W18	SIM1_CLK	GPIO_81



STUFF R3411 FOR PCIE BOOT (UNFUSED BB)
STUFF R3412 FOR USB BOOT (UNFUSED BB)

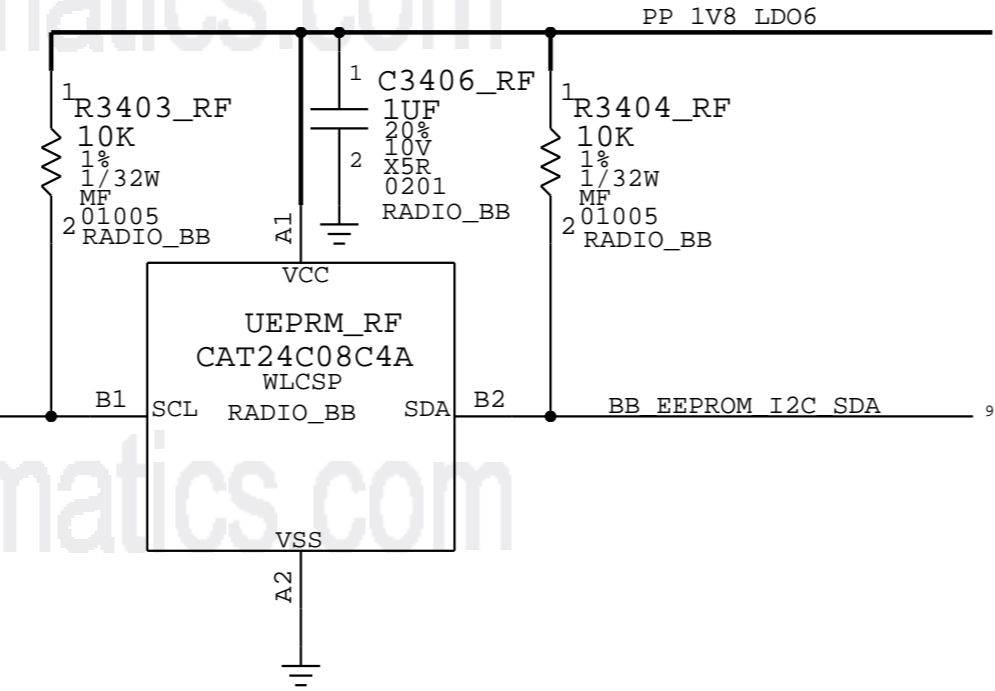
OPTION SEL2	SEL1	SELO
GPIO	35	34 32
PCIE	0	0 1
HSUSB	0	1 0
HSIC	0	1 1

NOSTUFF R3402 WHEN VINYL PRESENT
STUFF R3402 WHEN VINYL NOT PRESENT

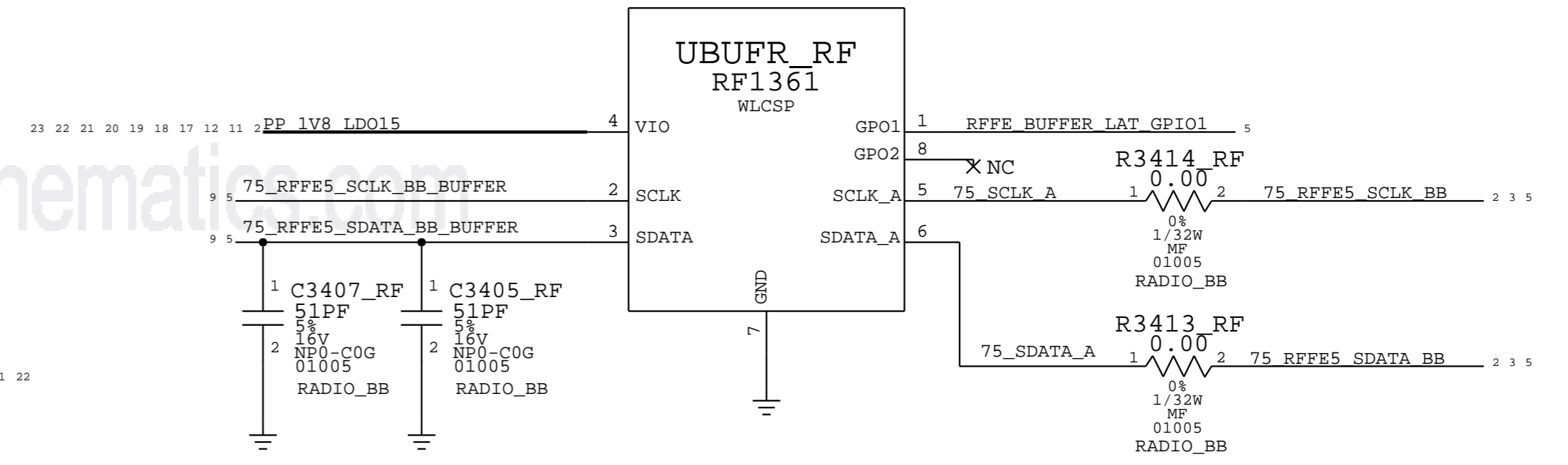


PCIE PULL-UPS TO BB RAIL

BB EEPROM

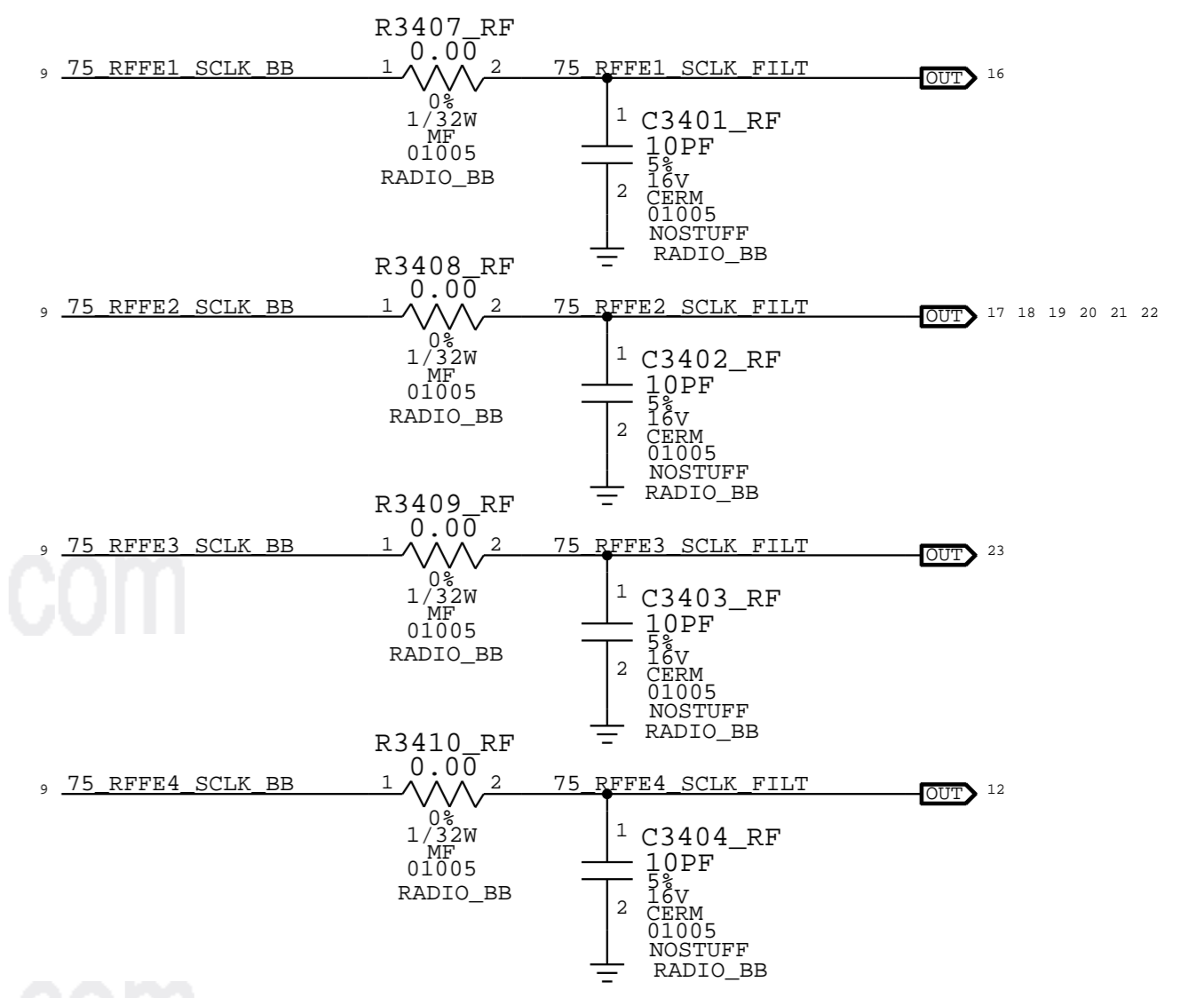


BUFFER ON RFFE5
SCLK/SDATA_A IS OUTPUT



PLACE C3405_RF CLOSE TO BUFFER
PLACE C3407_RF CLOSE TO MDM

RFFE CLOCK FILTERS



RFFE USAGE TABLE

- RFFE1 WTR
- RFFE2 LB/MB/HB PAD, 2G PA, LB/MB/HB ASM
- RFFE3 DIV ASM
- RFFE4 QPOET
- RFFE5 DIV LNA, ANT TUNERS

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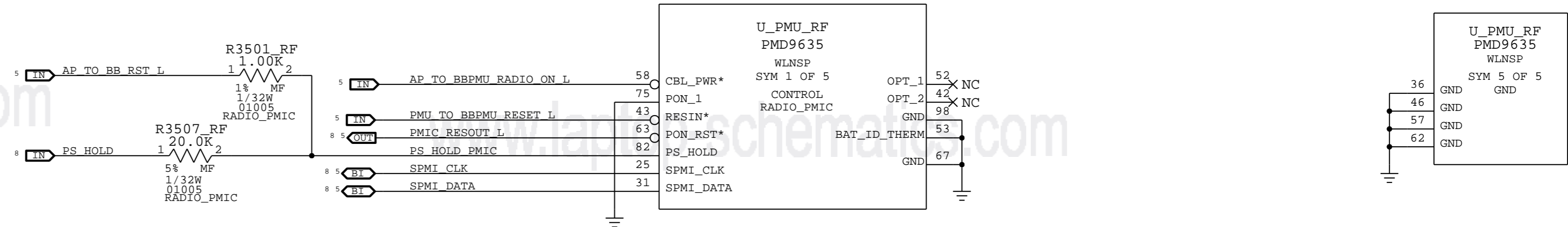
PMU: CONTROL AND CLOCKS

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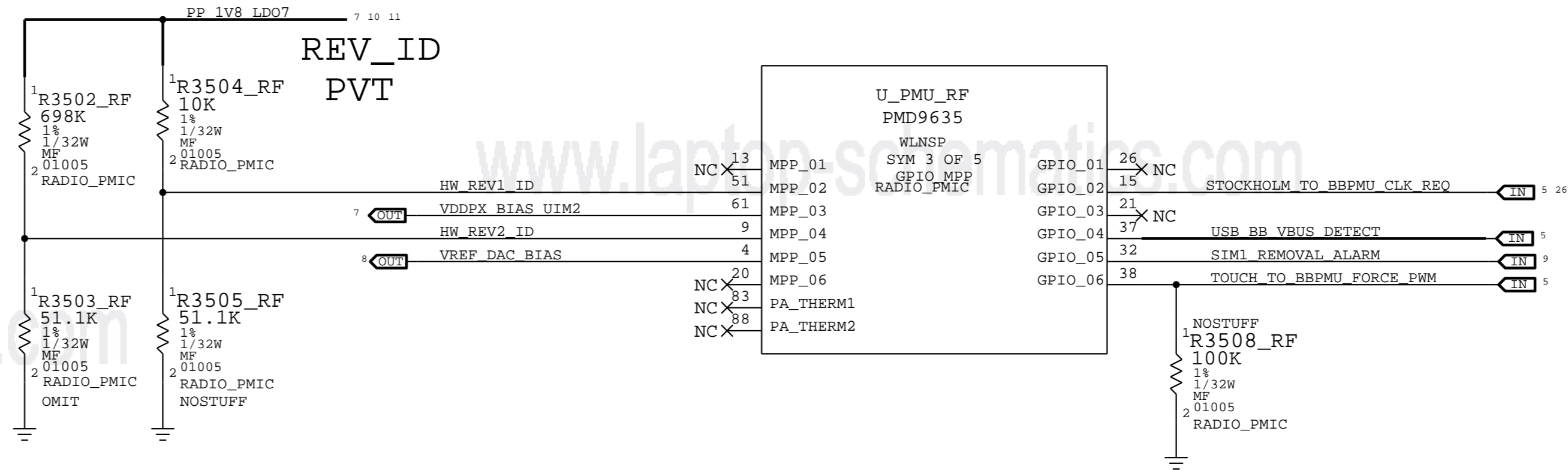
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1.80V	698K	-	MLB
0.12V	698K	51.1K	SELF GEN

HW_REV_ID	R3504	R3505	REVISION
0.10V	887K	51.1K	DEV1
0.30V	255K	51.1K	DEV2
0.50V	124K	51.1K	DEV3
0.70V	82.5K	51.1K	DEV4/PROTOMLB1
0.90V	51.1K	51.1K	PROTOMLB2
1.10V	31.6K	51.1K	DEV5/PROTO1
1.20V	50K	100K	PROTO2
1.31V	39K	105K	EVT
1.43V	13.3K	51.1K	EVT ALT
1.55V	8.25K	51.1K	CARRIER BUILD
1.67V	3.92K	51.1K	DVT
1.80V	10K	-	PVT

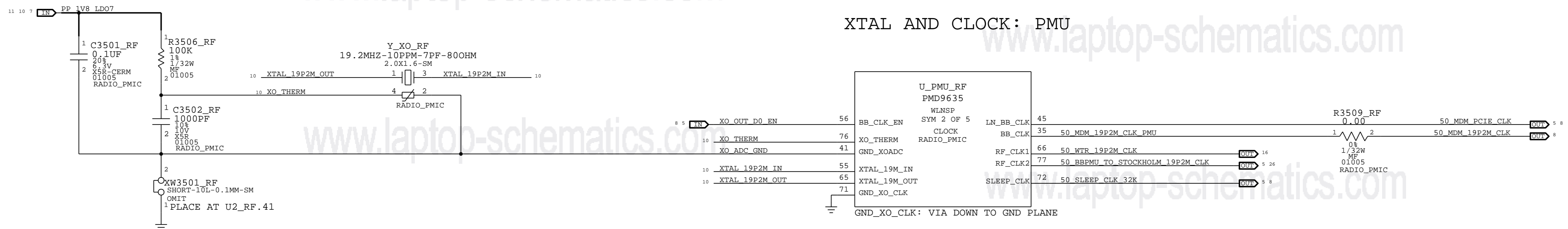
RESET AND CONTROL: PMU



MPPS AND GPIOs: PMU



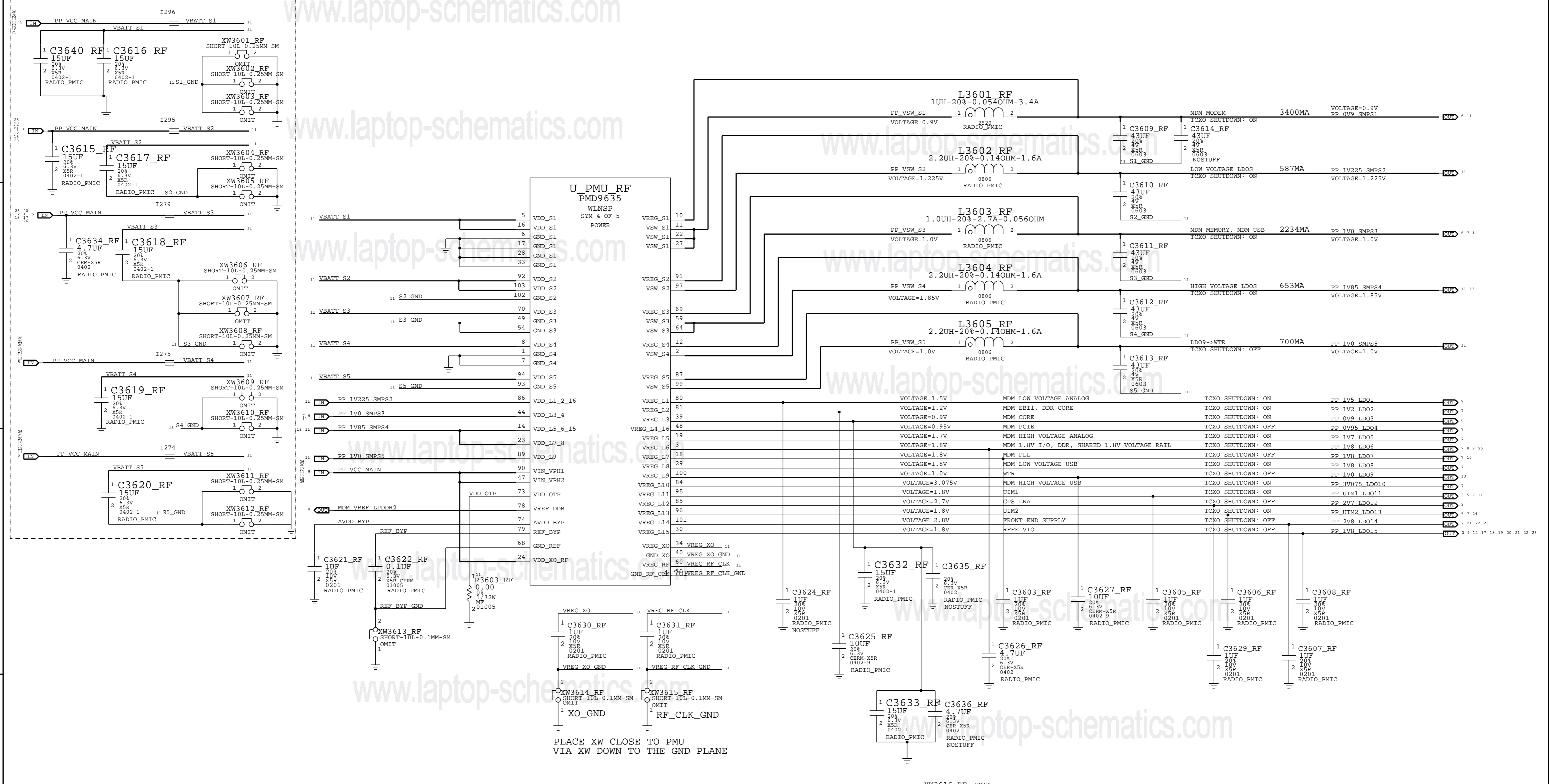
XTAL AND CLOCK: PMU



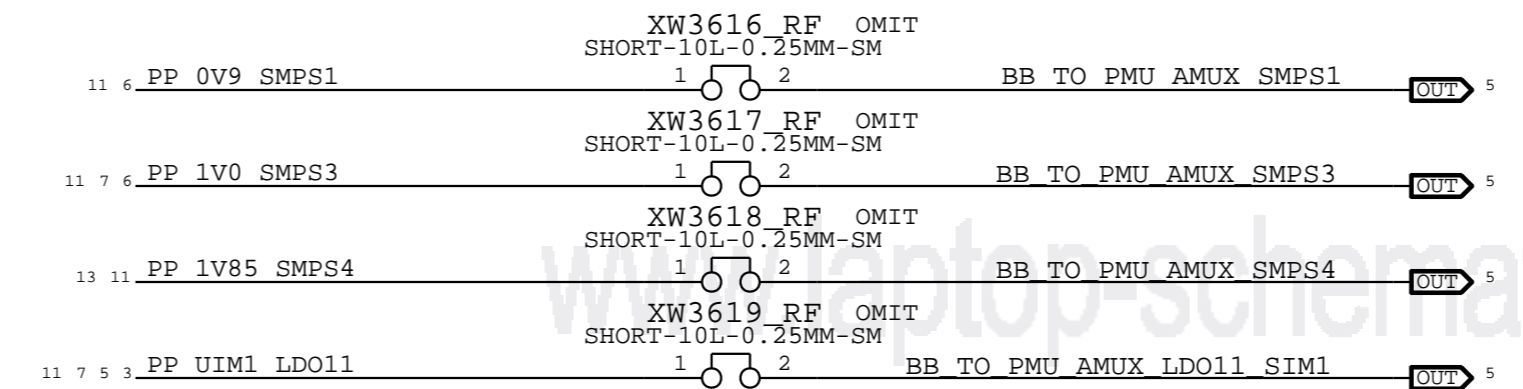
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PMU: SWITCHERS AND LDOS

SWITCHERS BULK CAPS



PLACE XW CLOSE TO PMU
VIA XW DOWN TO THE GND PLANE



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PMU: ET MODULATOR

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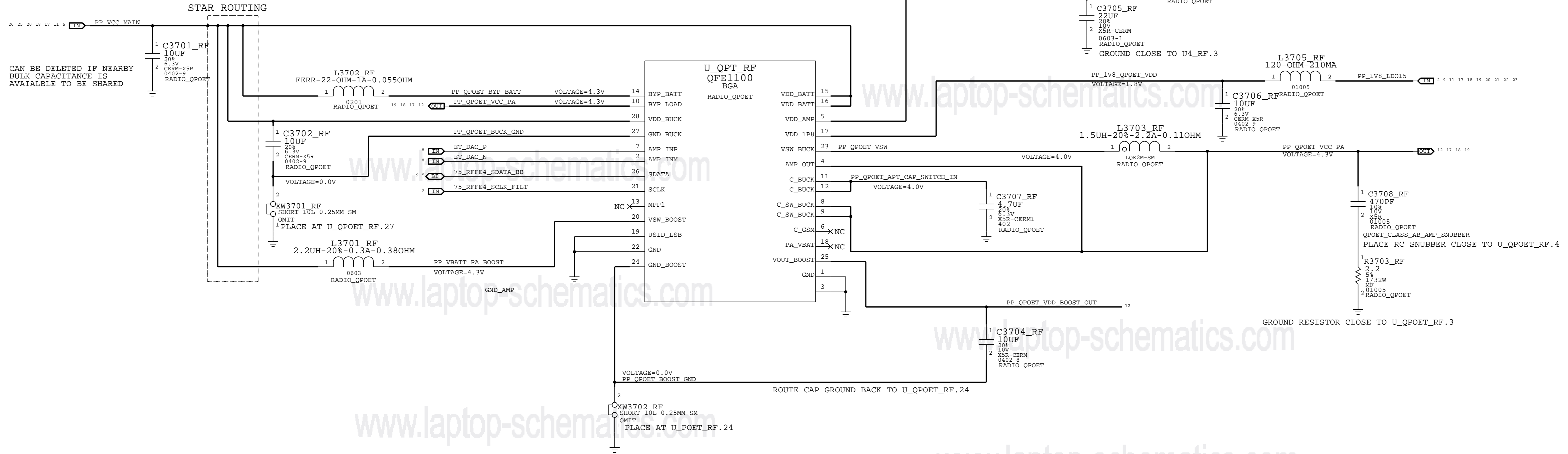
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QFE1100
BGA

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TRANSCEIVER: POWER

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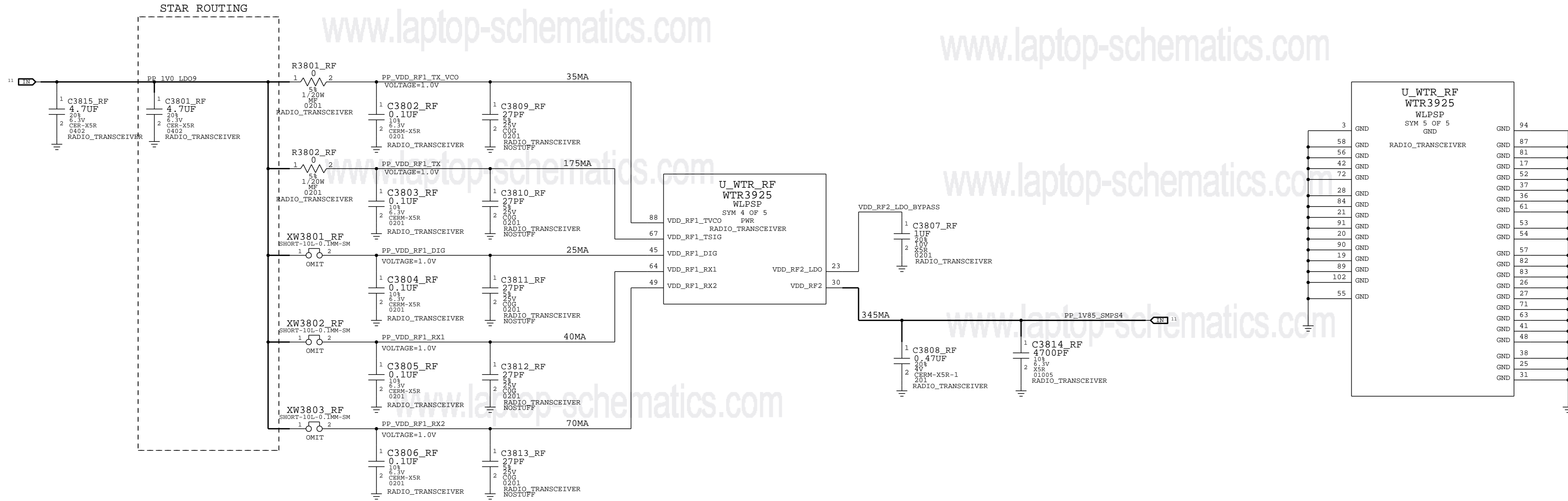
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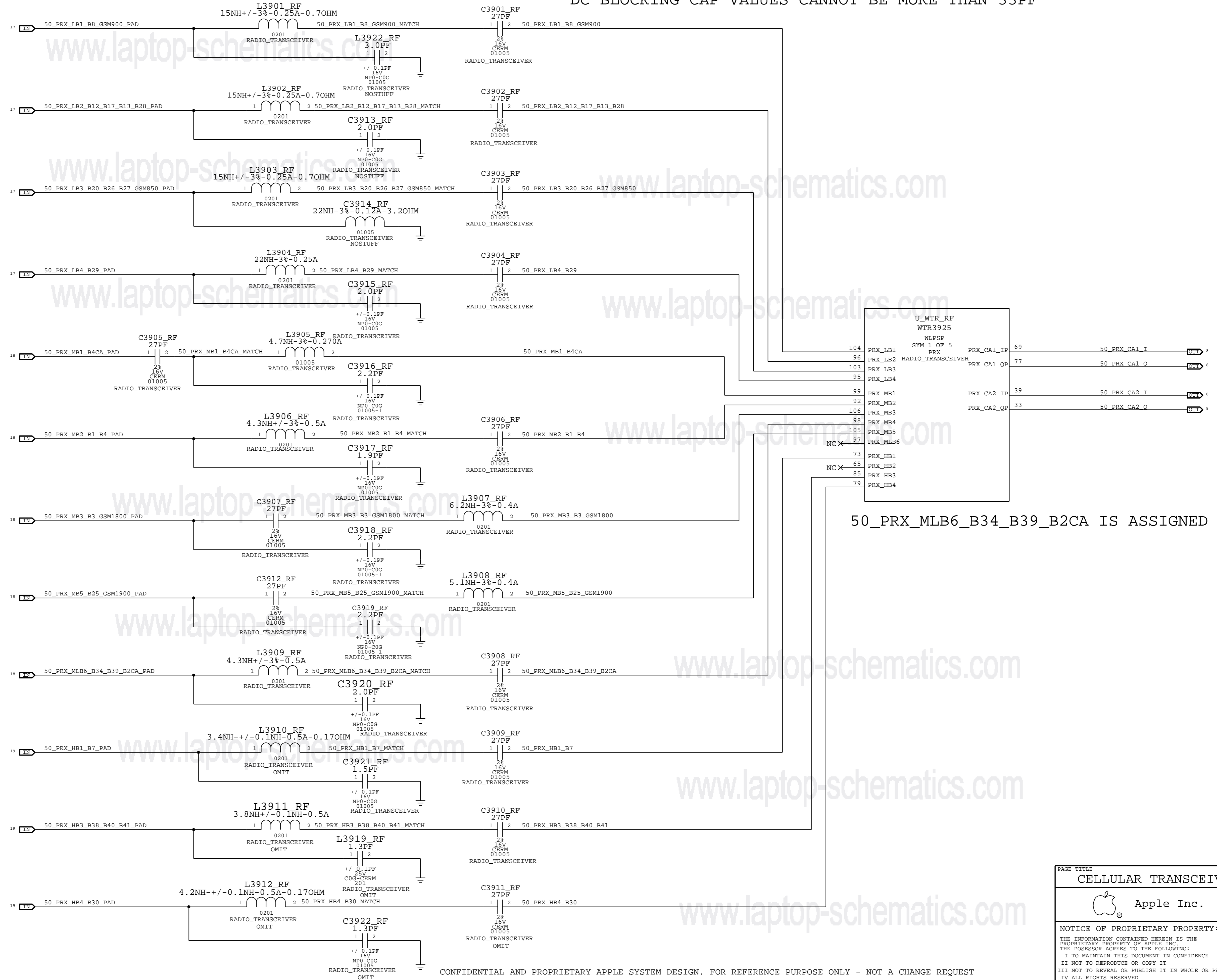
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TRANSCEIVER: PRX PORTS

DC BLOCKING CAP VALUES CANNOT BE MORE THAN 33PF



50_PRX_MLB6_B34_B39_B2CA IS ASSIGNED TO MB4

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TRANSCEIVER: DRX/GPS PORTS

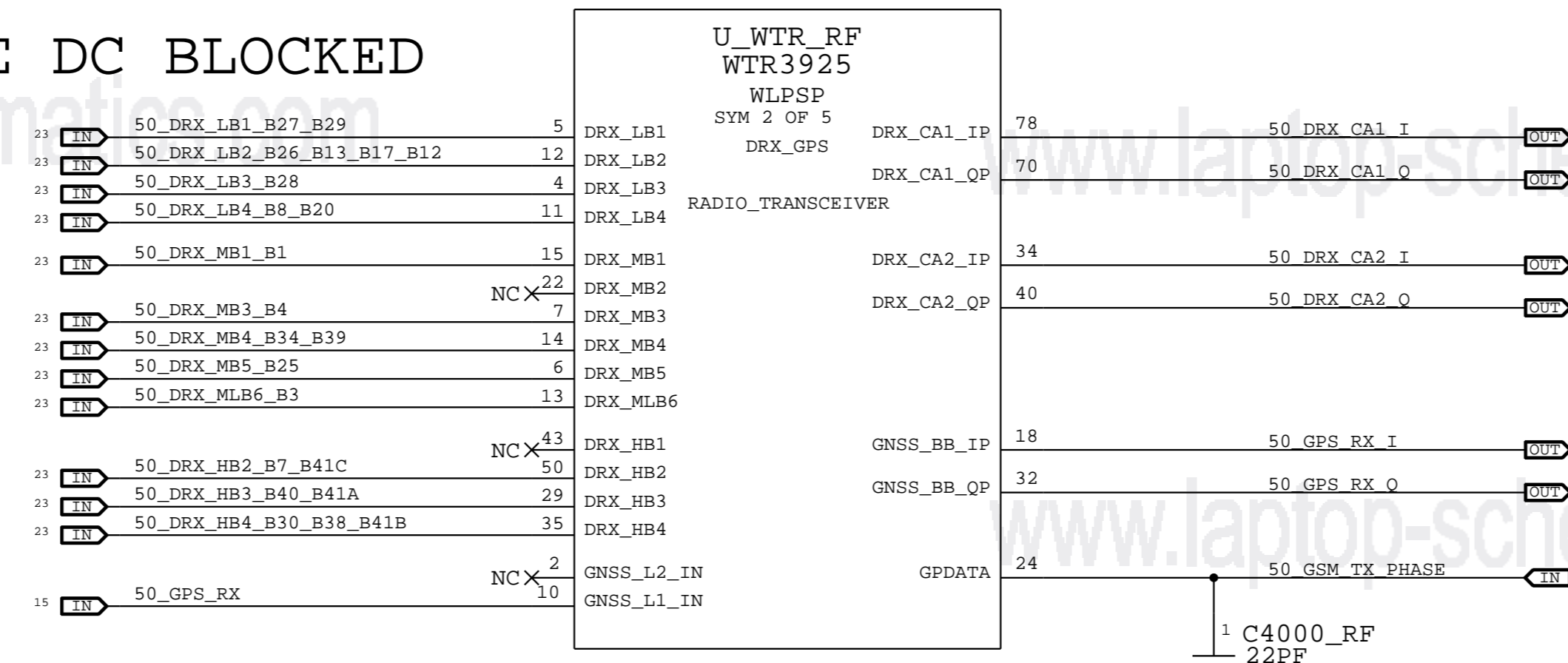
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DRX MODULE PORTS ARE DC BLOCKED

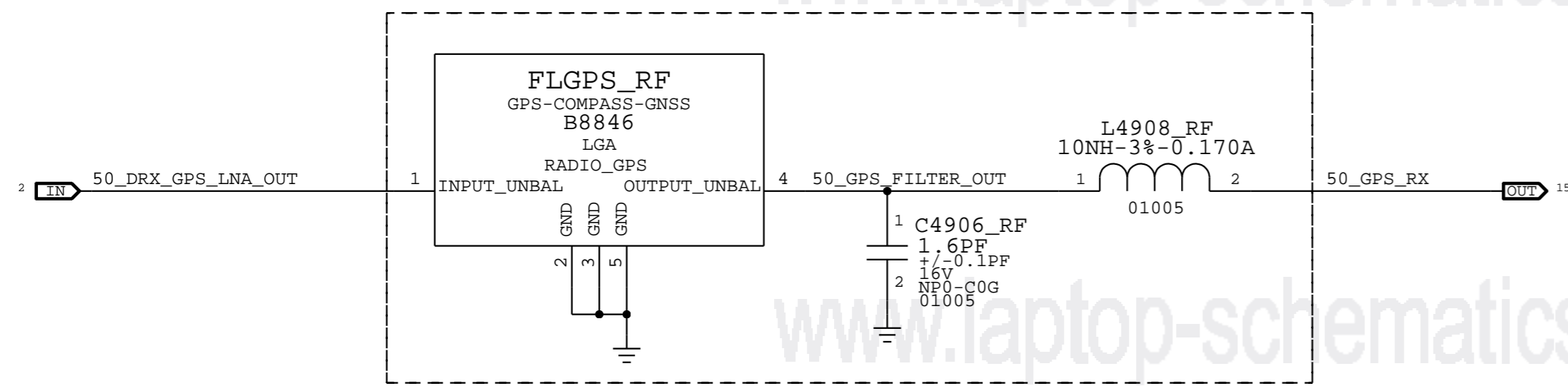
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PLACE CAP CLOSE TO MDM GPIO14
IMPROVES RXBN BY 4DB

GPS FILTER

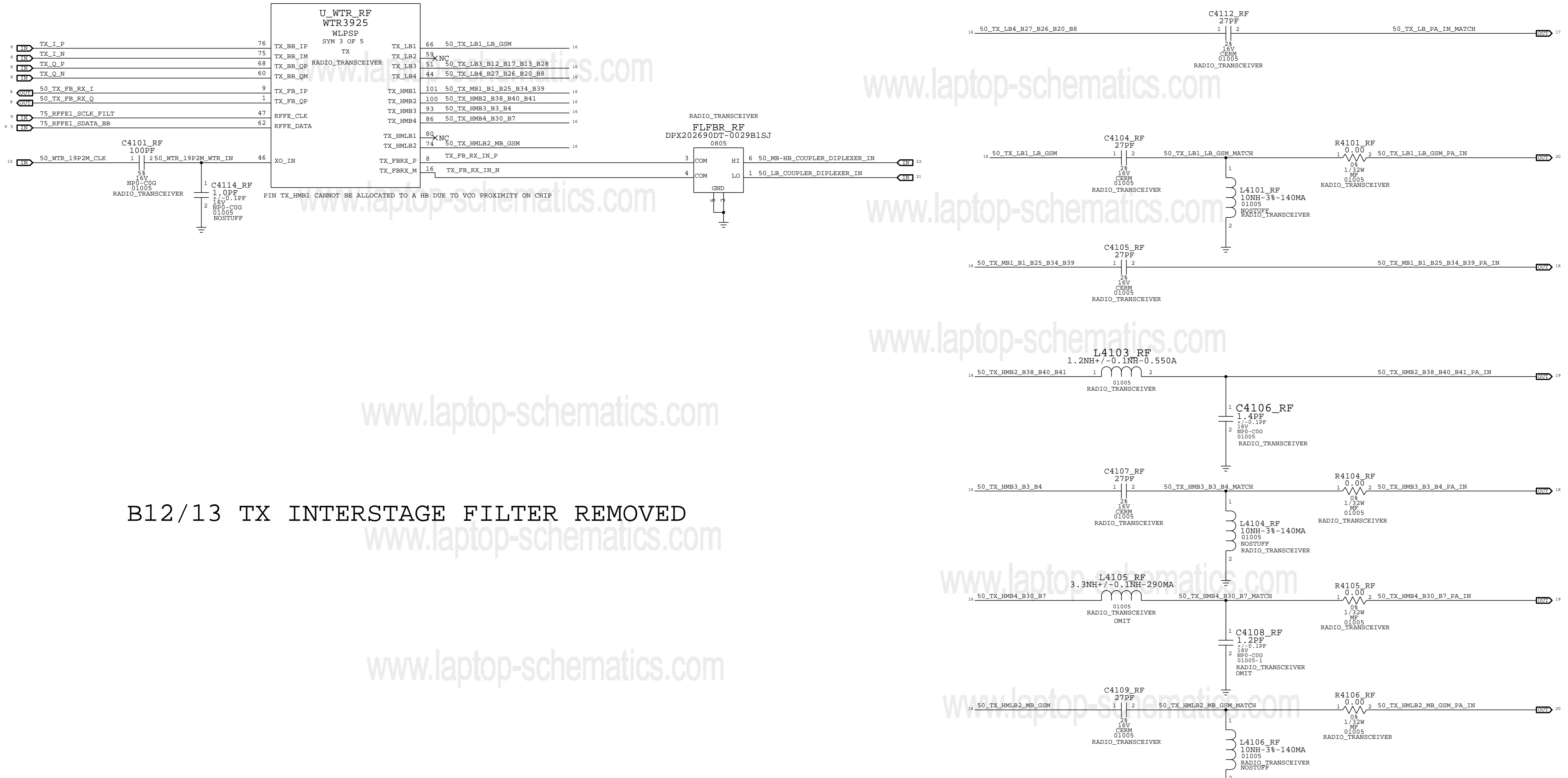
PLACE NEAR U_WTR



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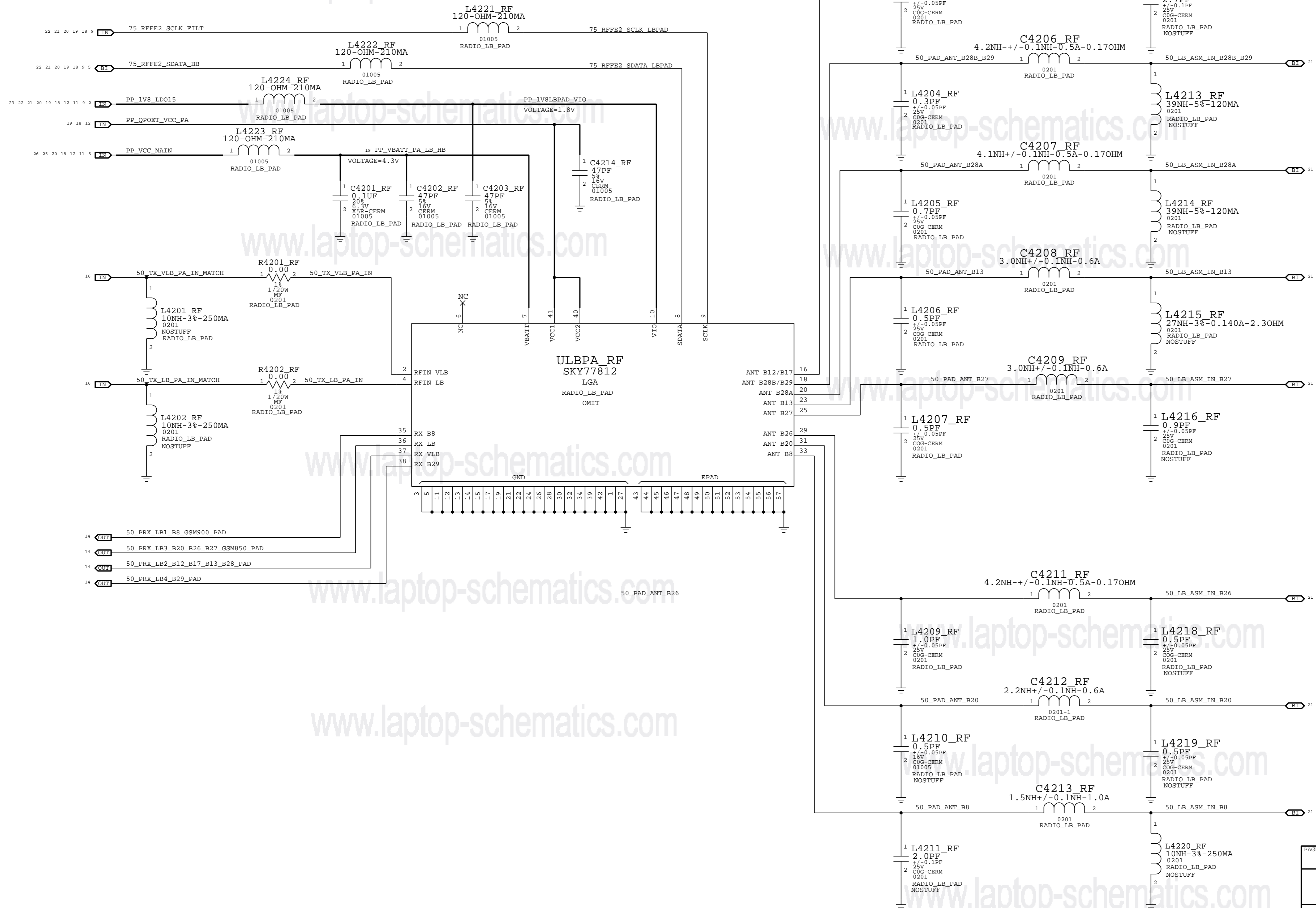
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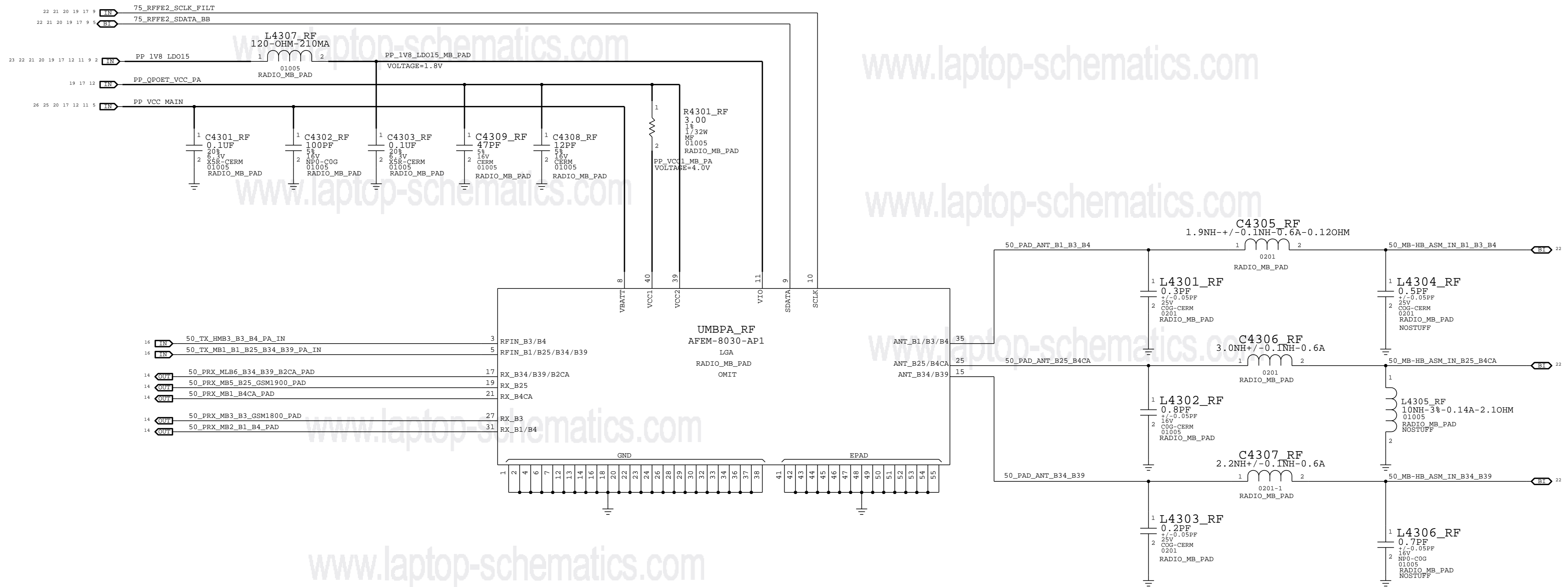
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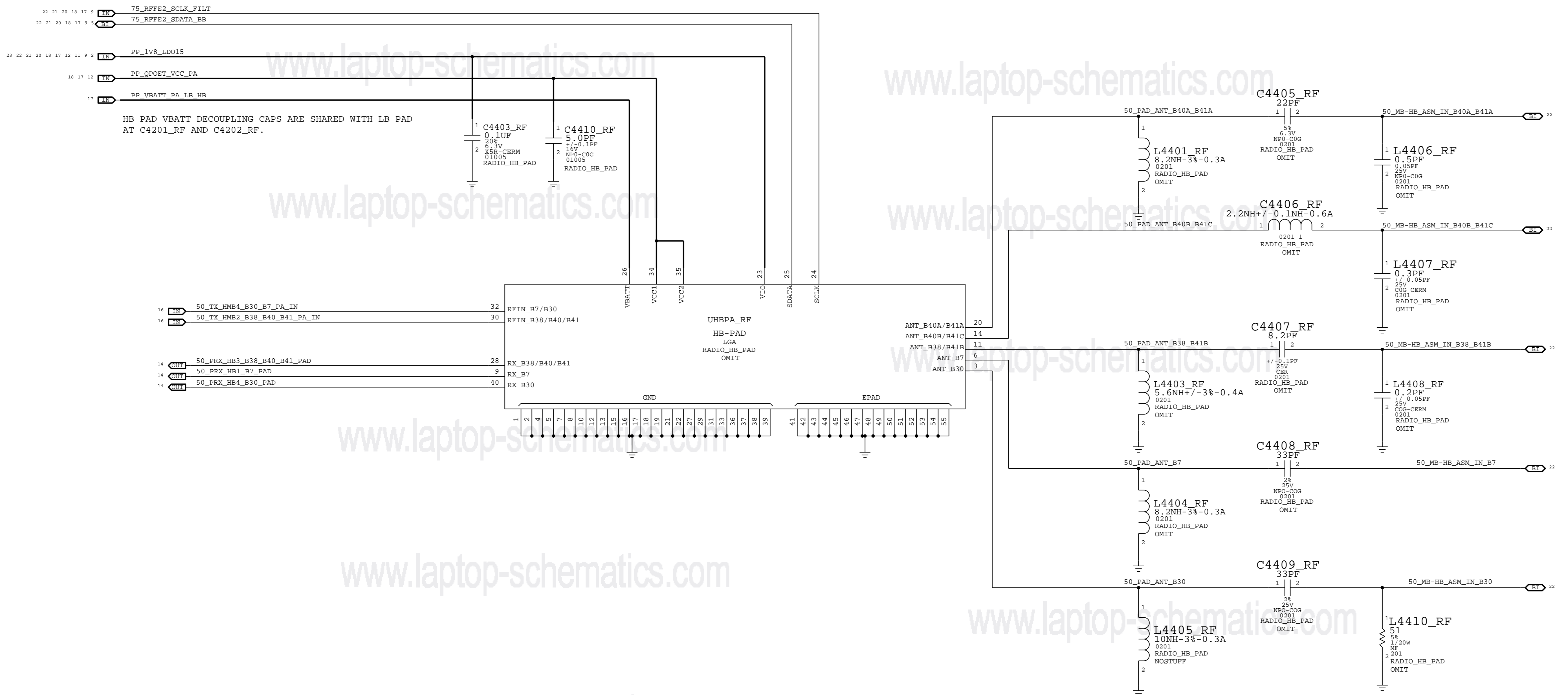
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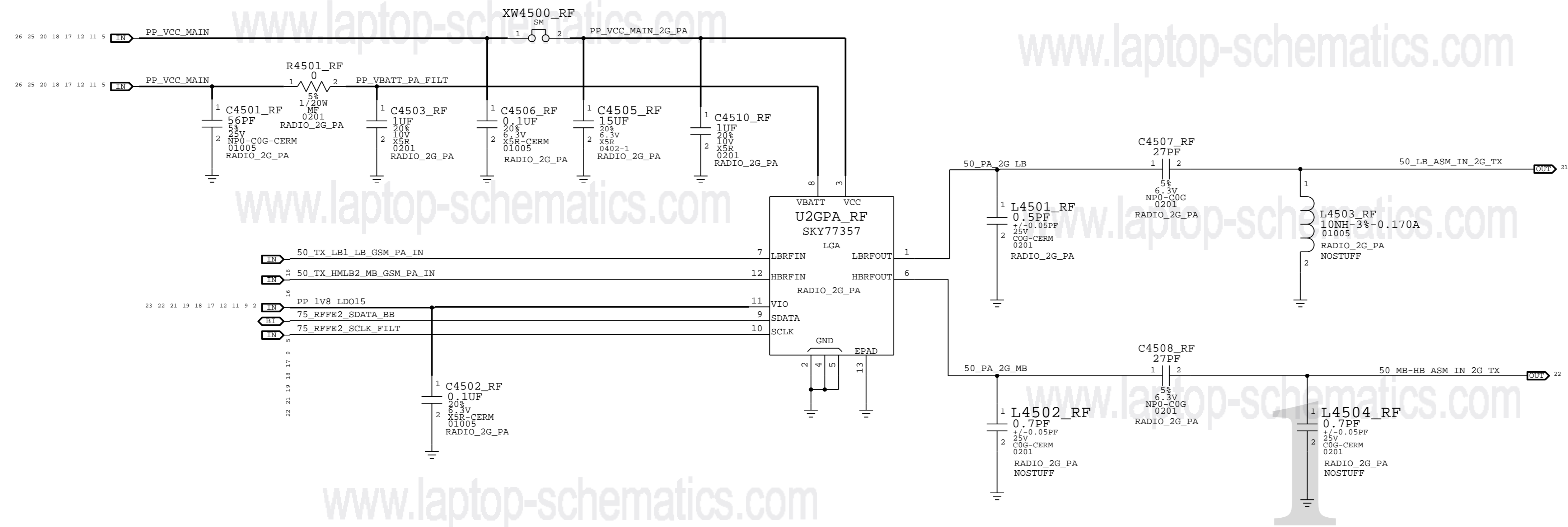
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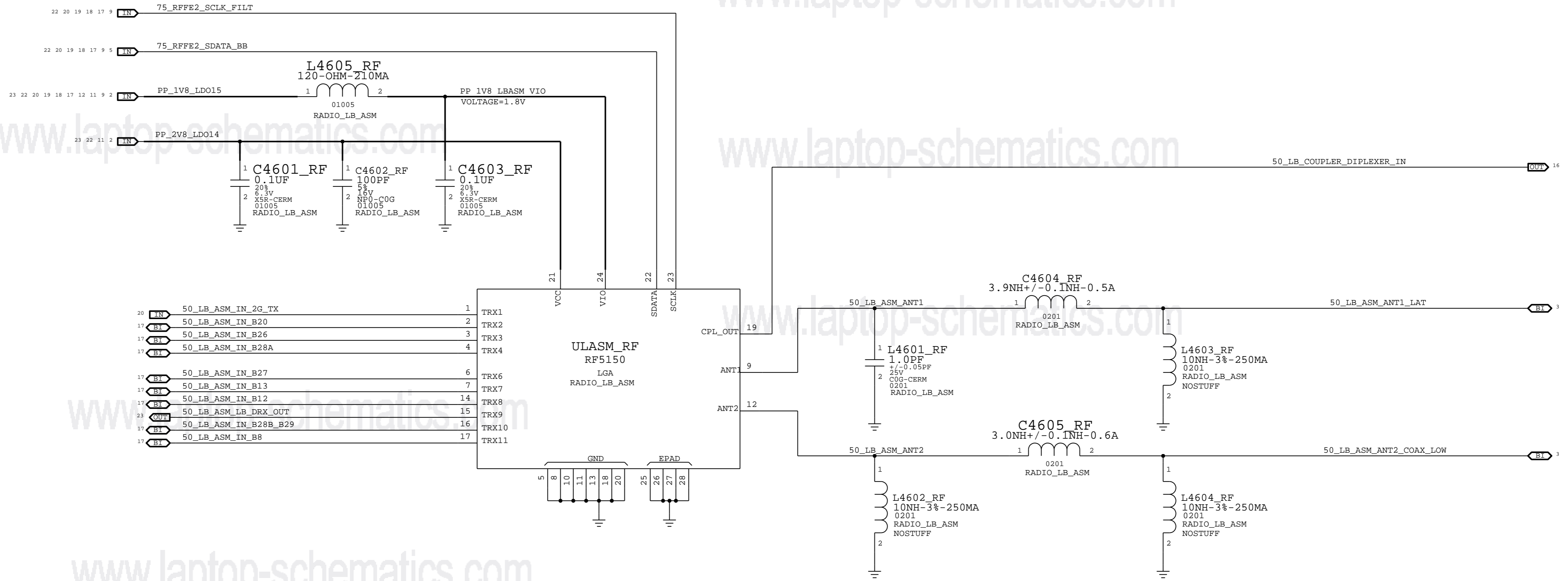
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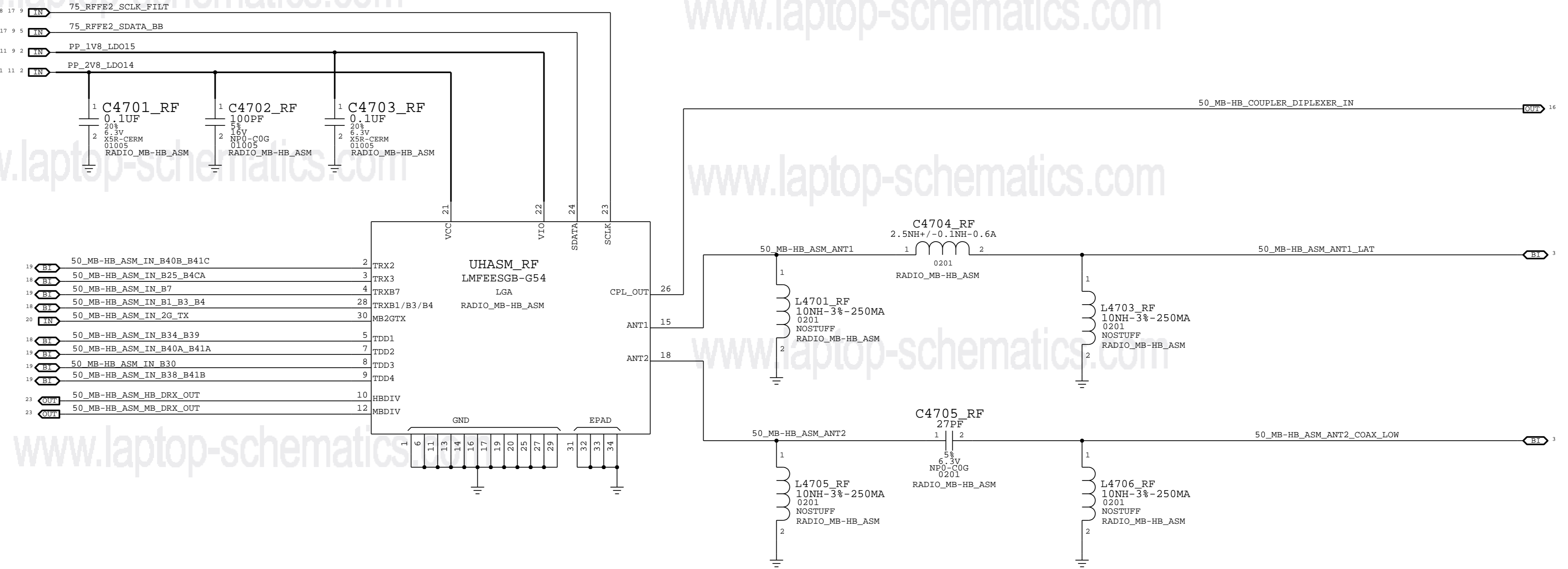
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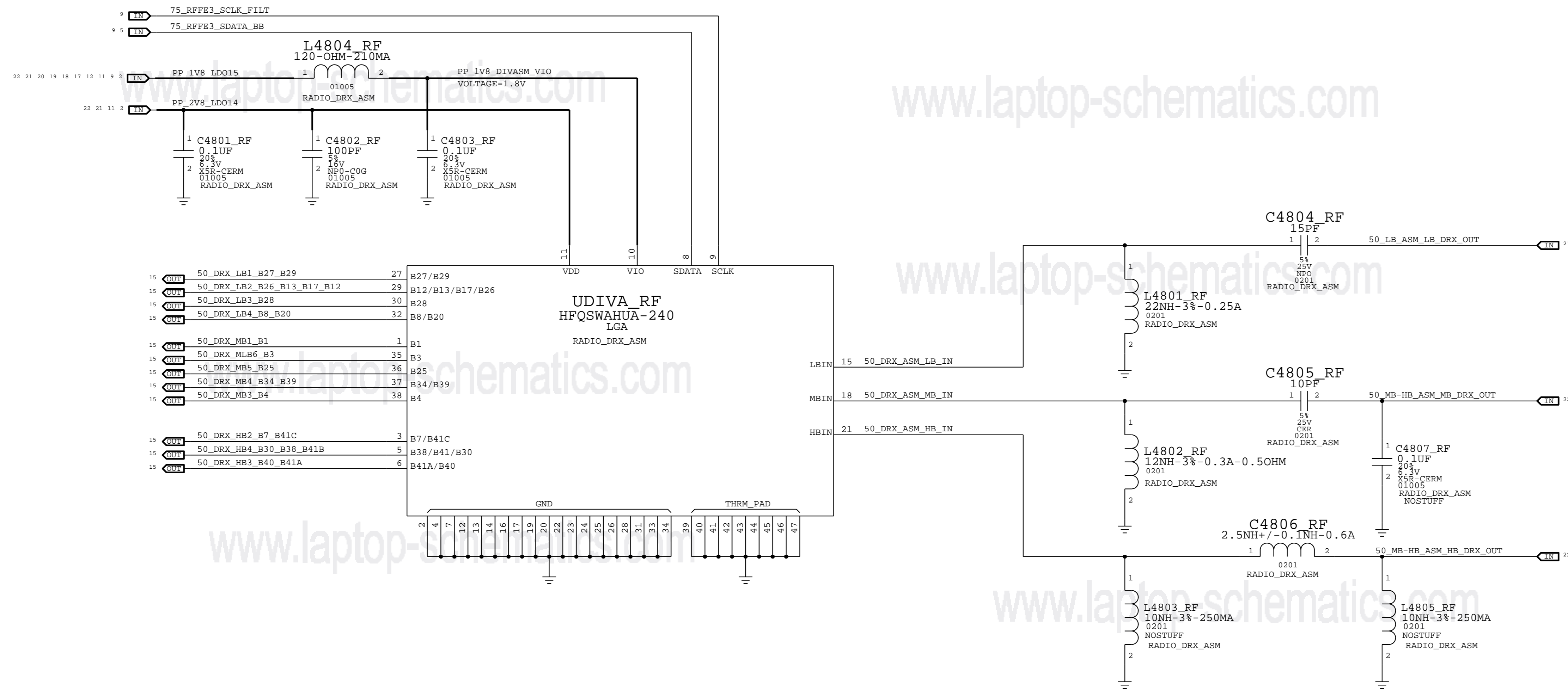
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DIVERSITY MODULE

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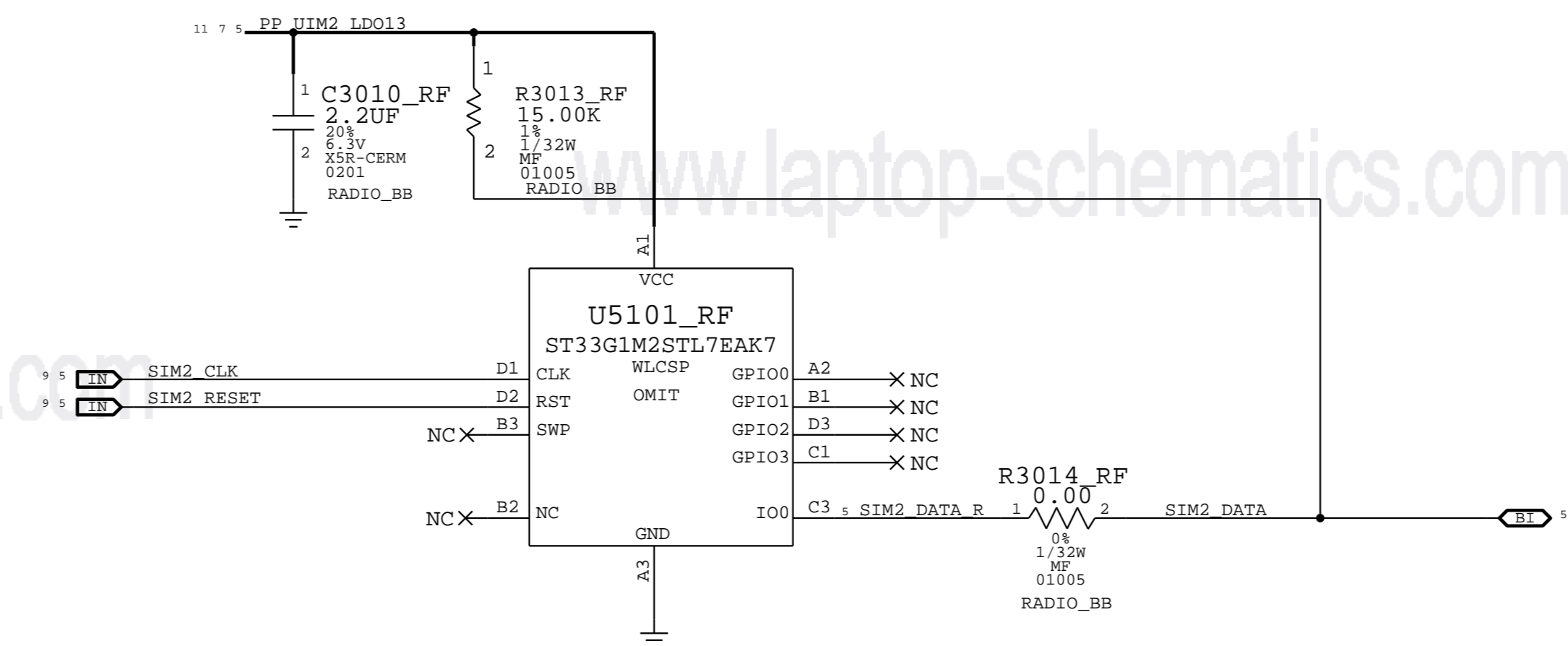
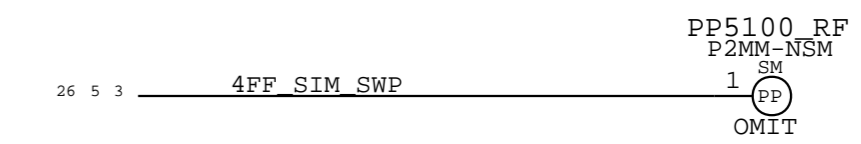
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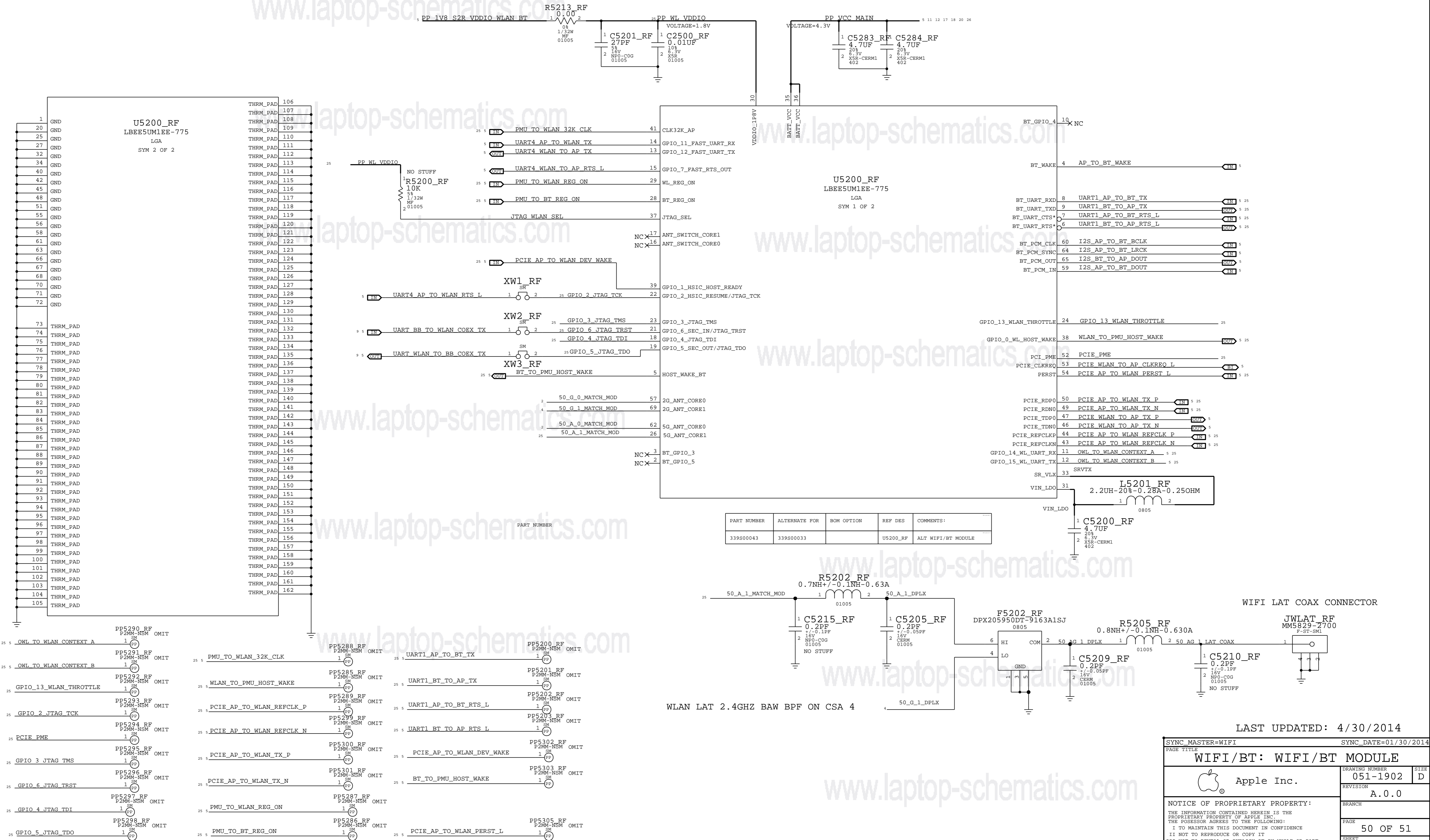
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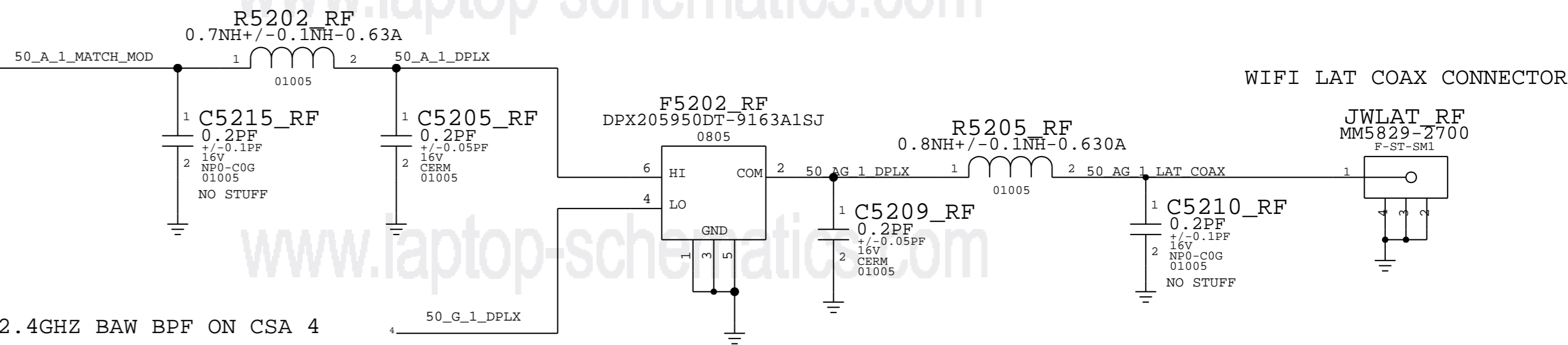
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PART NUMBER	ALTERNATE FOR	BOM OPTION	REF DES	COMMENTS:
339S00043	339S00033		U5200_RF	ALT WIFI/BT MODULE



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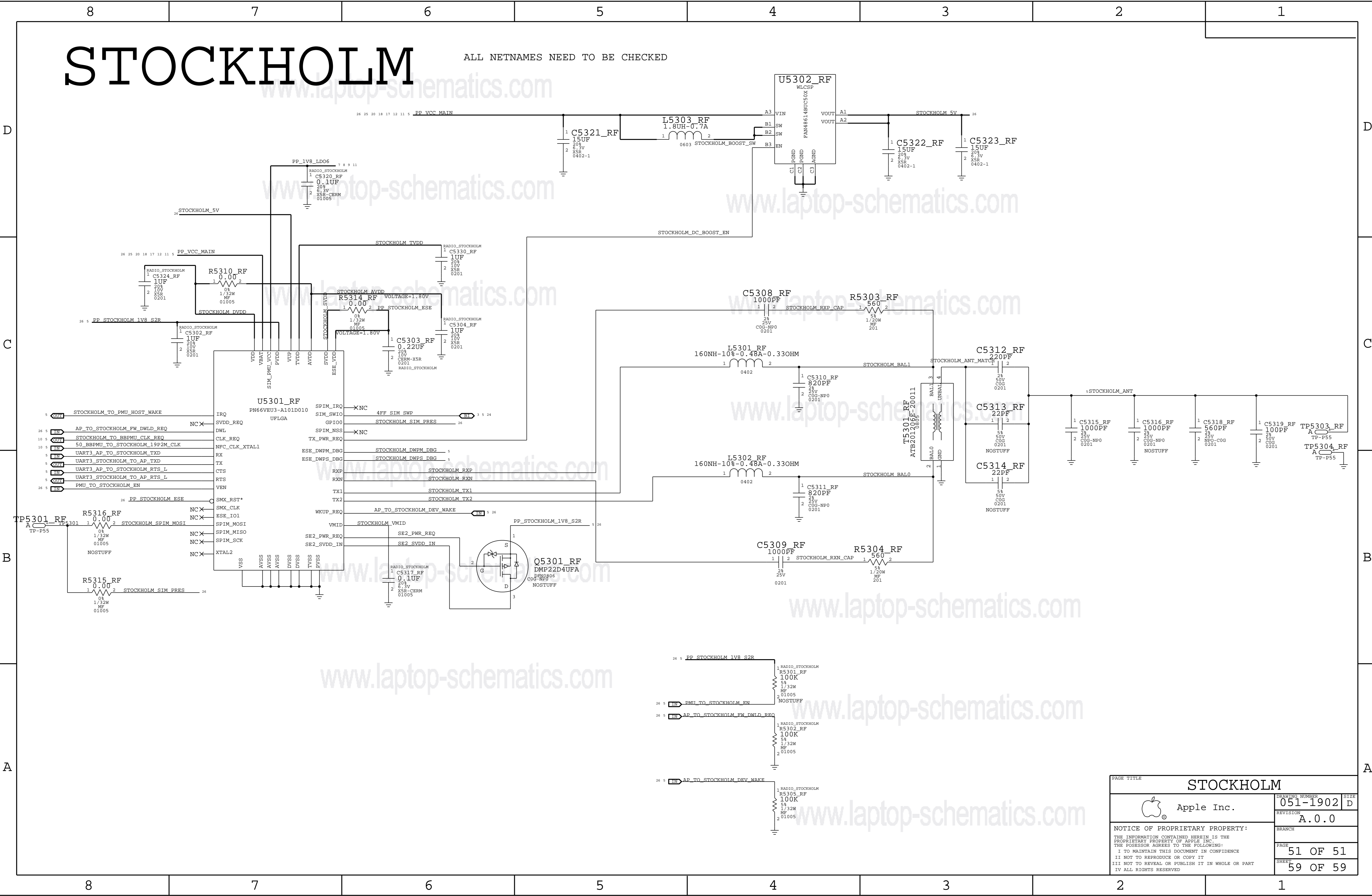
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